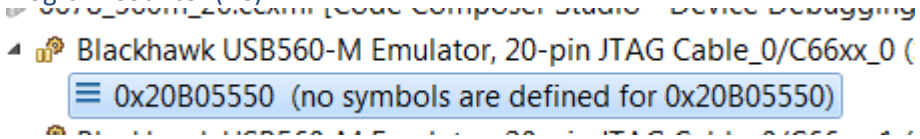
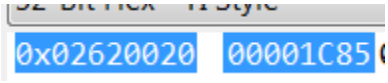
How to test BOOTP C6678 EVM in BE mode

Step 1: make sure under LE IBL Ethernet boot mode you can see bootp (this verifies your environment)

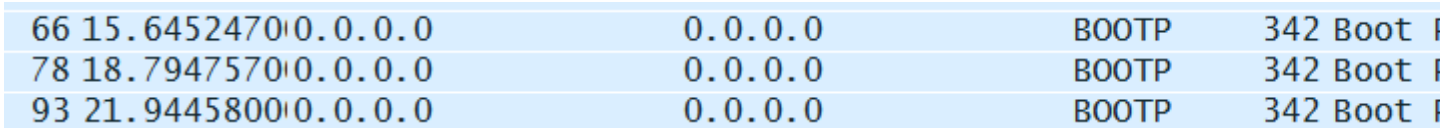
Program Counter (PC):



DEVSTAT:

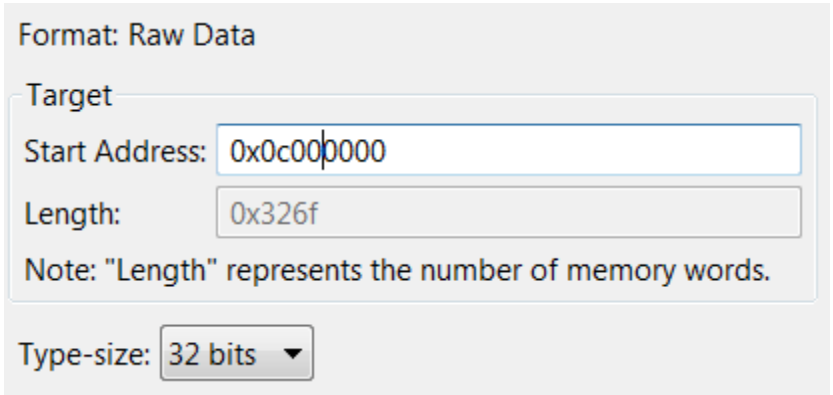


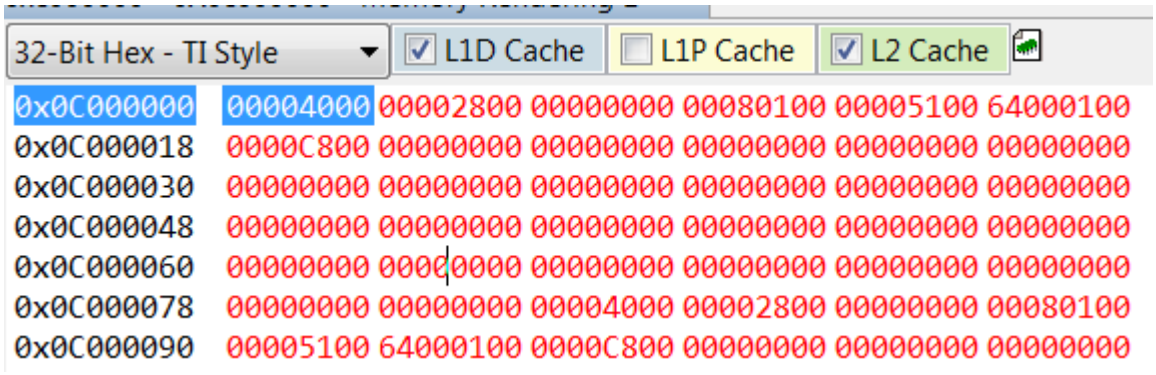
Wireshark:



Step 2: Update IBL to big endian (use everything under latest MCSDK 2.1.2.6):

- 1)eepromwriter_evm6678l.out located at ..\tools\writer\eeprom\evmc6678l\bin built in big endian mode. ====➔ you don't need to re-build it in big endian mode, just use it as it is for LE eeprom writer
  2)Copied *i2crom_0x51_c6678_be.bin* from ..\tools\boot_loader\ibl\src\make\bin
  to ..\tools\writer\eeprom\evmc667#l\bin. Copied file renamed to *app.bin*.
  3)eepromwriter_input.txt in ..\tools\writer\eeprom\evmc6678l\bin. file_name setequal to app.bin and bus_addr equal to 0x51. Madesure start_addr and swap_data are set to 0.
  4)EVM switched to No boot mode and LE mode.
  5)Connect and gel file load.
  6)eepromwriter_evm6678l.out loaded on core 0.
  7)Memory load of app.bin at 0x0C000000 (size 32-bits).





- 8)"EEPROM programming completed successfully" displayed at console.

```
[C66xx_0] EEPROM Writer Utility Version 01.00.00.05

Writing 51644 bytes from DSP memory address 0x0c000000 to EEPROM bus address 0x0051
starting from device address 0x0000 ...
Reading 51644 bytes from EEPROM bus address 0x0051 to DSP memory address 0x0c010000
starting from device address 0x0000 ...
Verifying data read ...
EEPROM programming completed successfully
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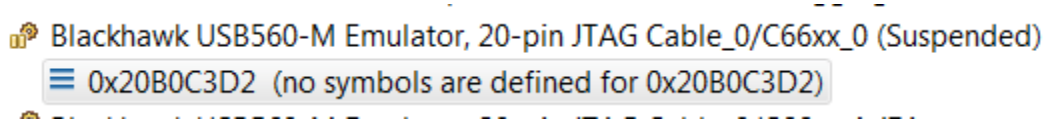
STEP 3: Test IBL BE:

PC:



DEVSTAT

Wireshark: