

Case 1

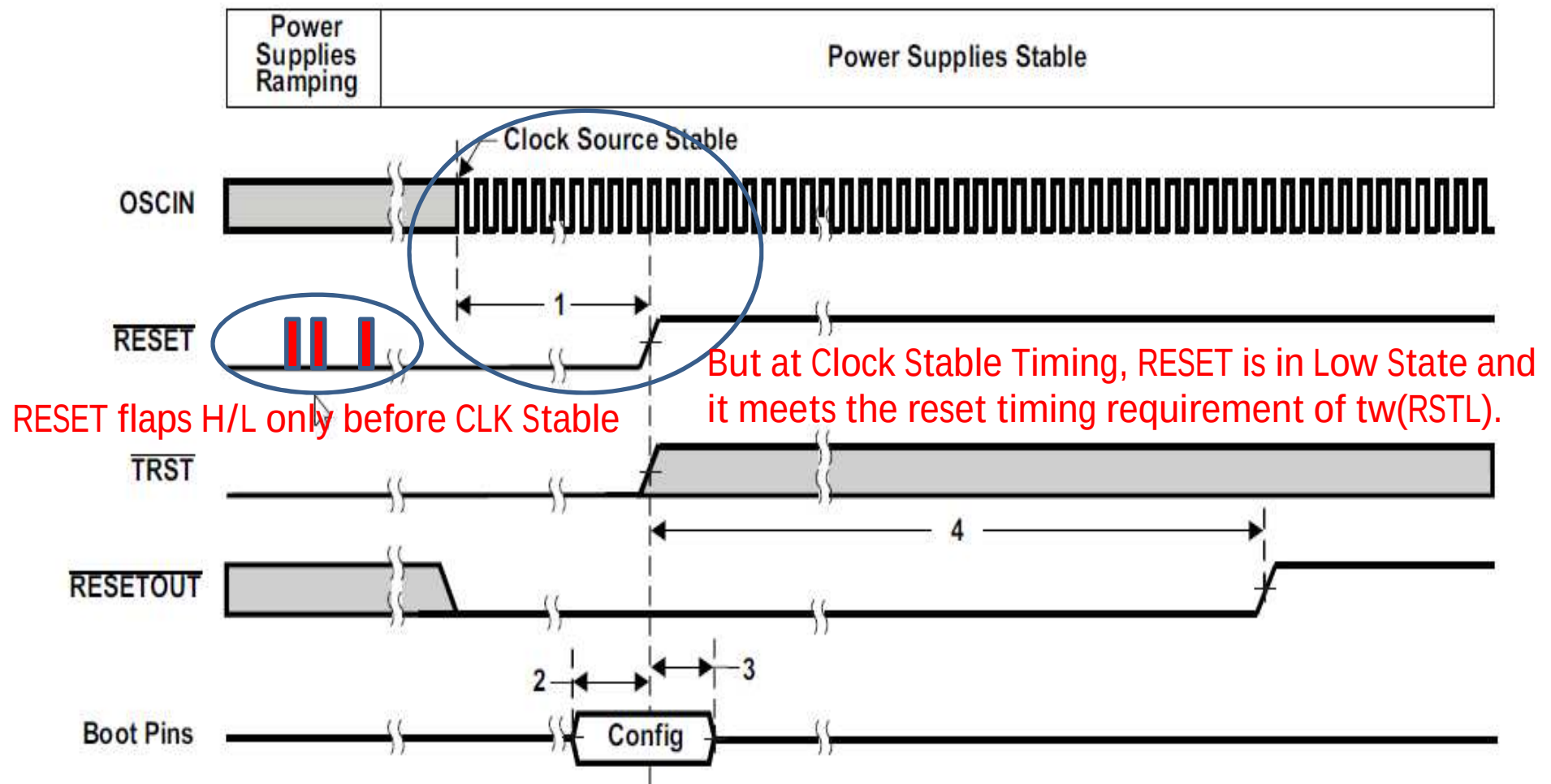


Figure 6-4. Power-On Reset (RESET and TRST active) Timing

In this case, can I think as that the reset timing requirement is met?

Case2

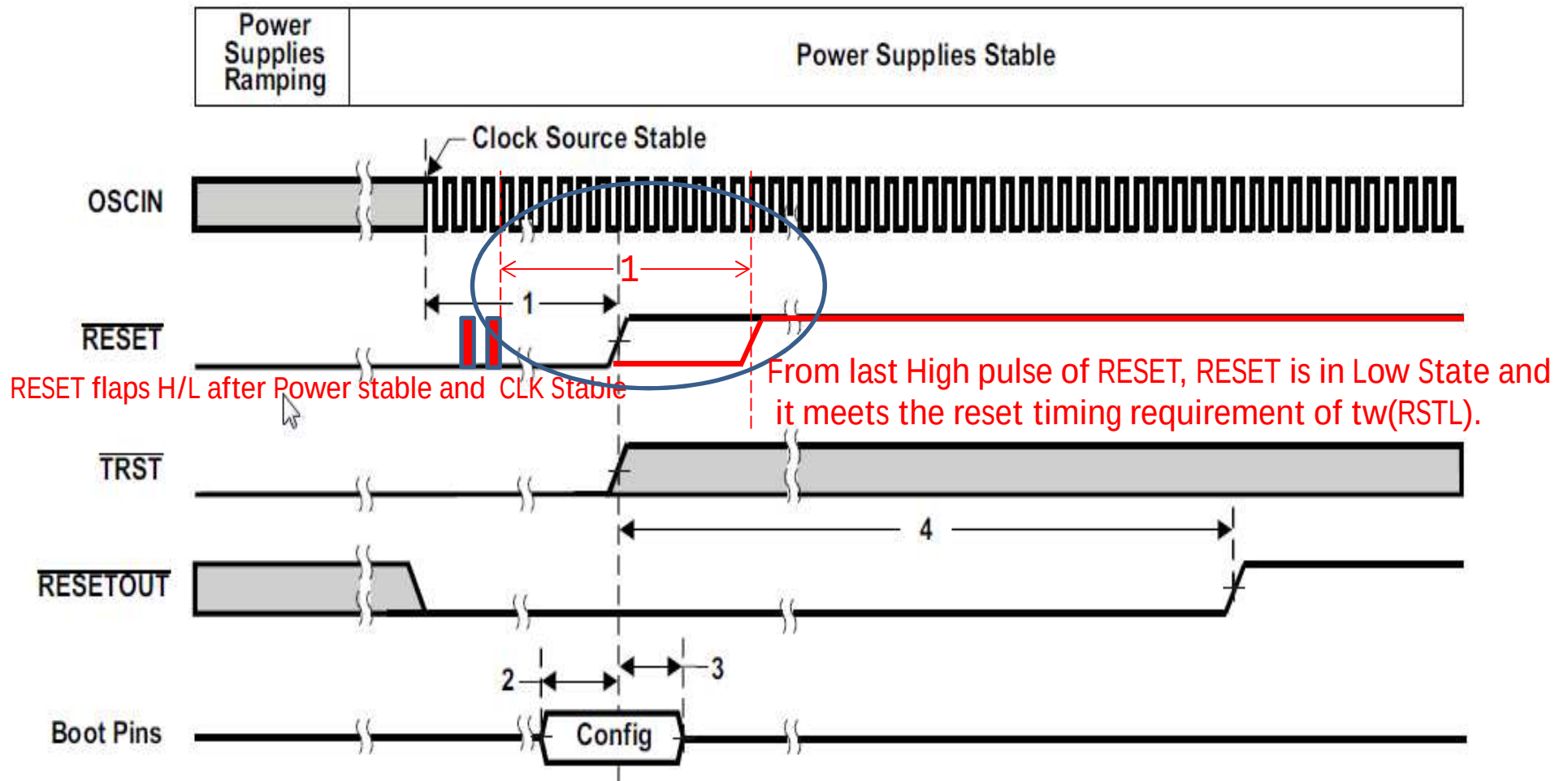


Figure 6-4. Power-On Reset (RESET and TRST active) Timing

In this case, can I think as that the reset timing requirement is met?

Case3

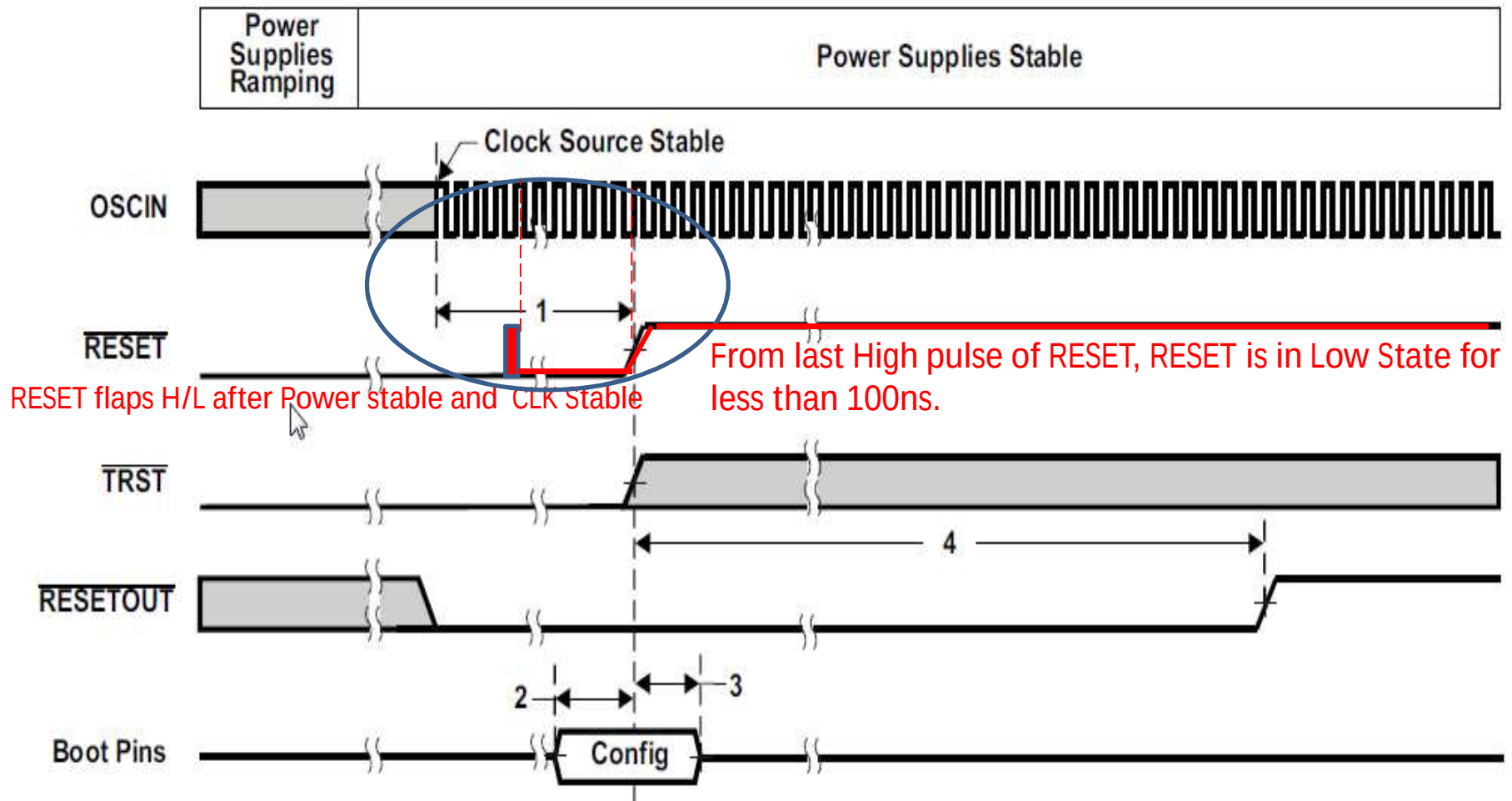


Figure 6-4. Power-On Reset (RESET and TRST active) Timing

In this case, can I think as that it doesn't meet the reset timing requirement ?