

JN_AM3352_V0.1 SCHEMATIC SHEETS (REV A)

REV	DESCRIPTION	DATE	APPROVED
A			

SCHEMATIC CONTENTS

sh01 - TITLE
sh02 - POWER IN & TEM
sh03 - AM335X Section 1of 3
sh04 - AM335X Section 2of 3
sh05 - AM335X Section 3of 3
sh06 - PMIC TPS65217D
sh07 - DDR3L Memory
sh08 - EMMC & SD
sh09 - USB2.0 & Keys
sh10 - CONSOLE
sh11 - EXP 4-20ma & IO & RS485
sh12 - 4G & Zigbee & GPRS_CONN
sh13 - WIFI_CONNECTOR
sh14 - LCD_CONNECTOR
sh15 - LCD_Power
sh16 - BackLight Power

REVISION STATUS OF SHEETS

REV										
SH										
REV										
SH										
REV										
SH										
REV										
SH										
REV										
SH	21	22	23	24	25	26	27	28		
REV	A	A	A	A	A	A	A	A		
SH	11	12	13	14	15	16	17	18	19	20
REV	A	A	A	A	A	A	A	A	A	A
SH	1	2	3	4	5	6	7	8	9	10

DWN	DATE
CHK	DATE
ENGR	DATE
ENGR-MGR	DATE
QA	DATE
MFG	DATE
RLSE	DATE



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Size A4	Document Number sh01 - TITLE	Rev V11
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D

C

B

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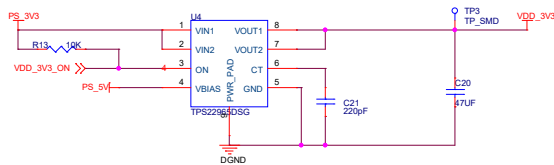
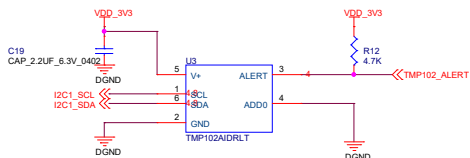
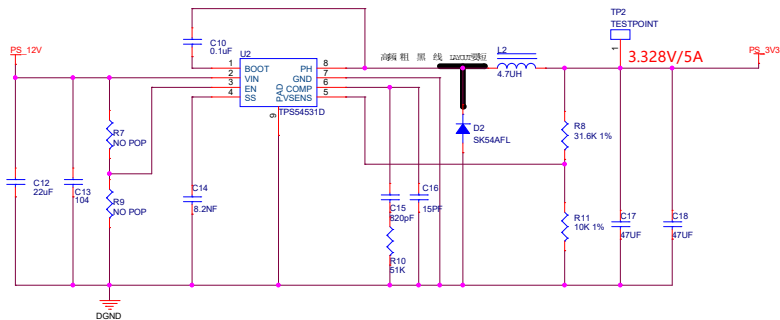
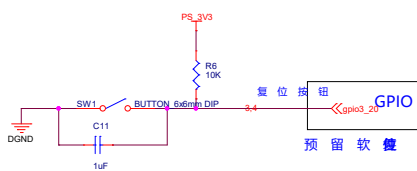
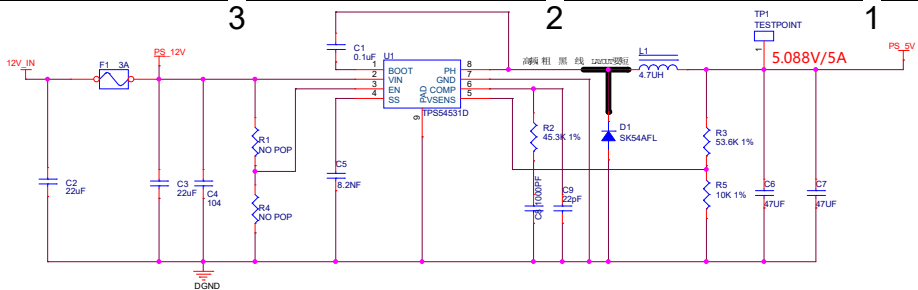
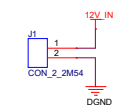
4

3

2

1

电源输入



JN_AM3352_V0.1		
Size	Document Number	Rev
A3	sh02 - POWER_IN & TEM	A
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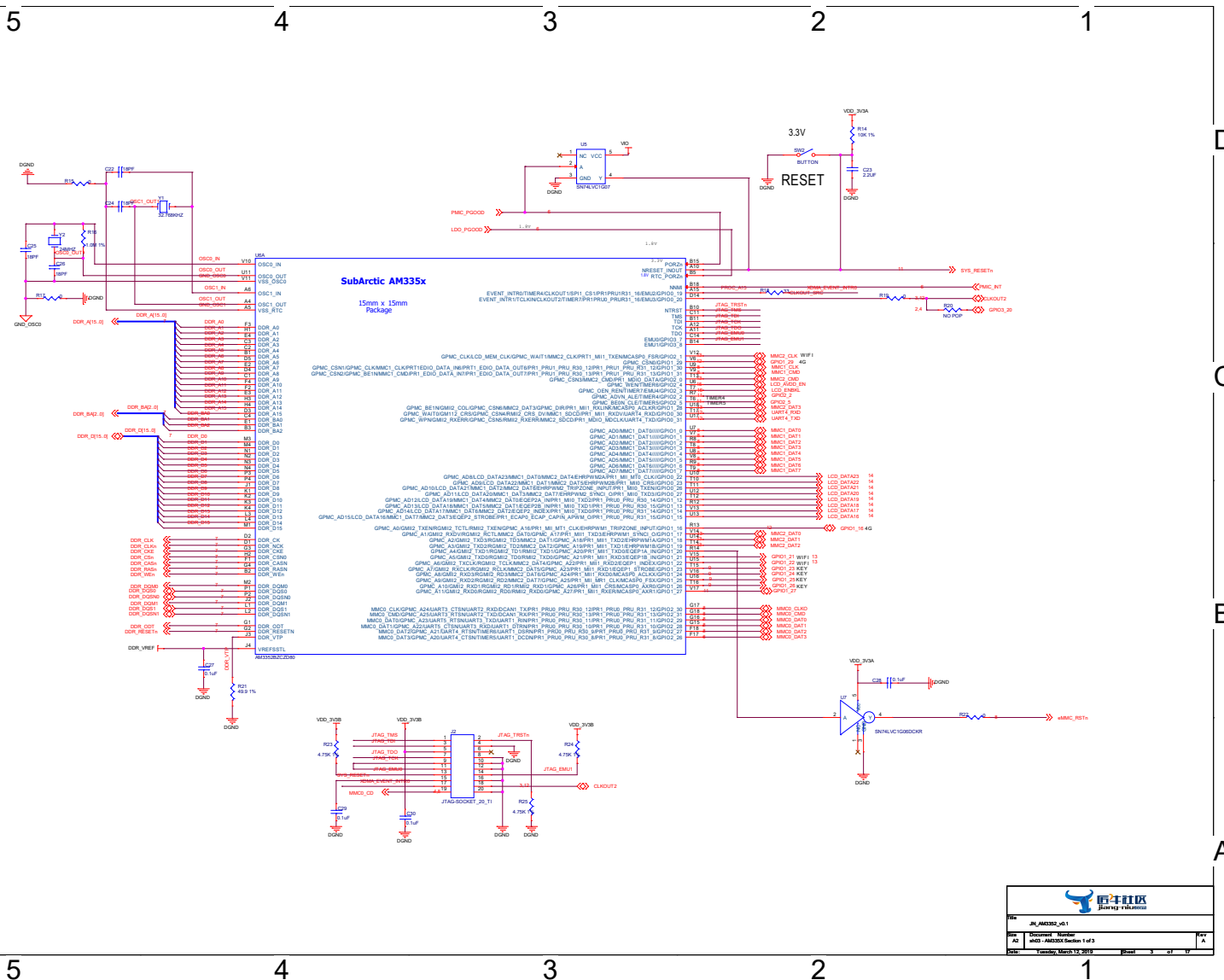
5

4

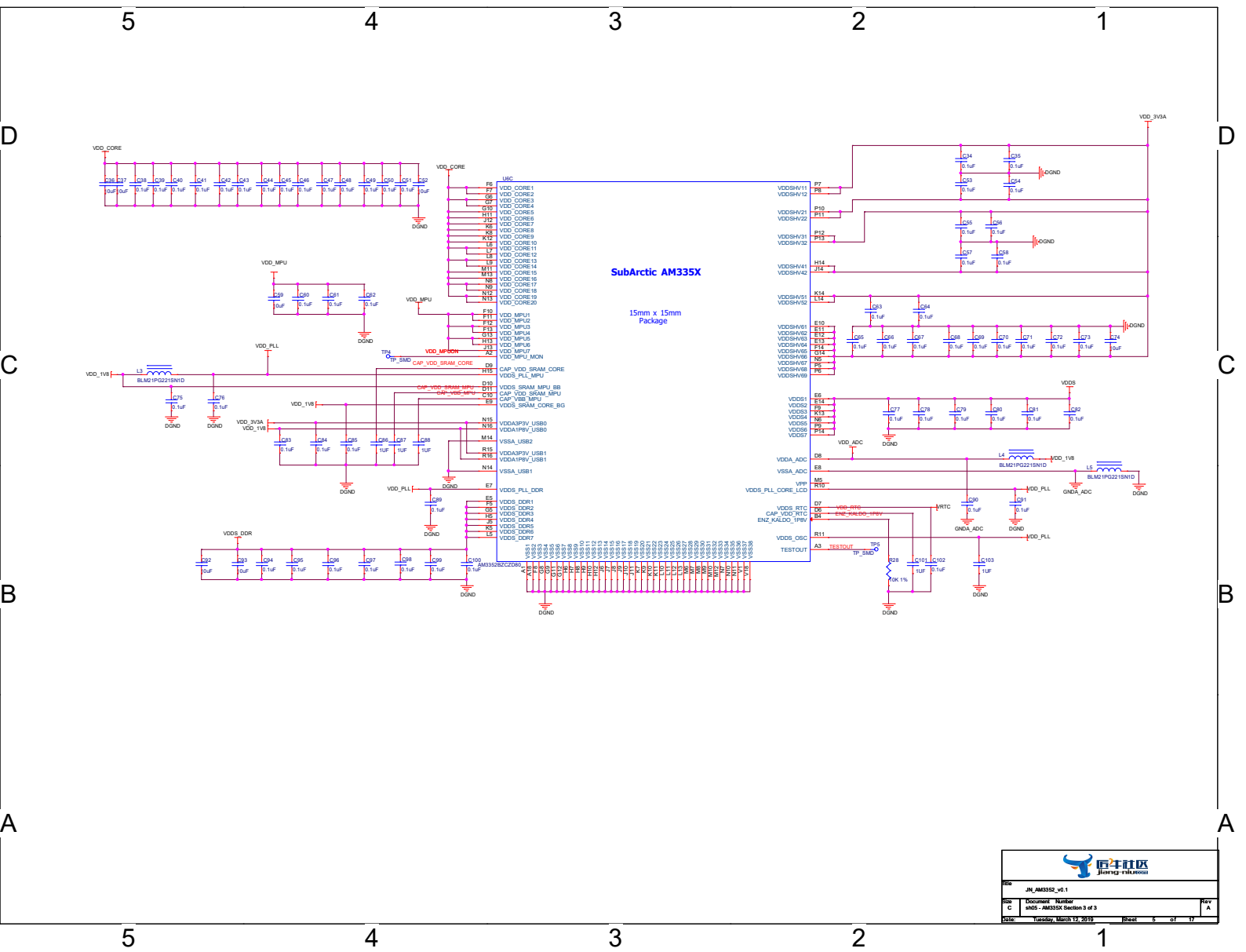
3

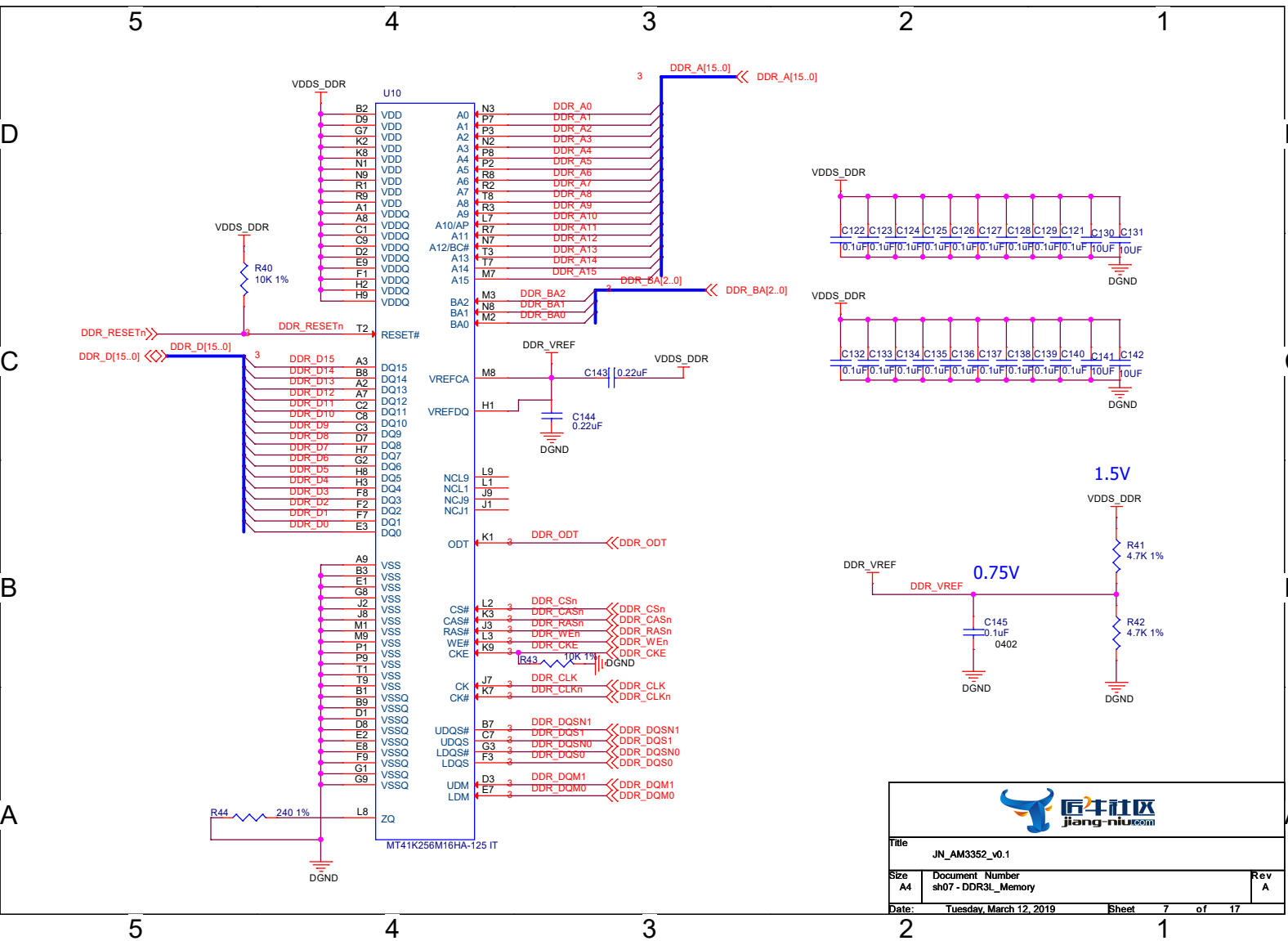
2

1

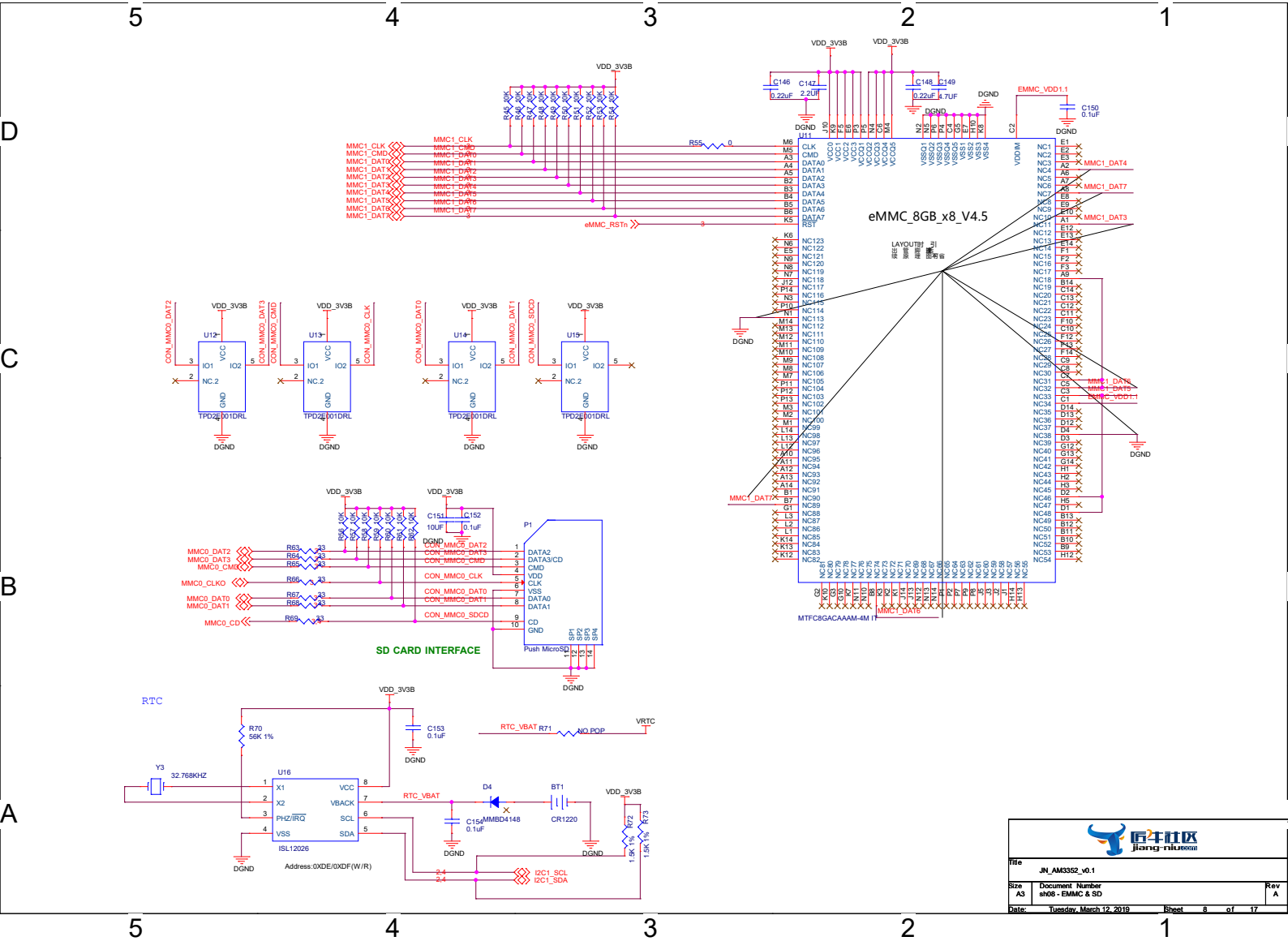


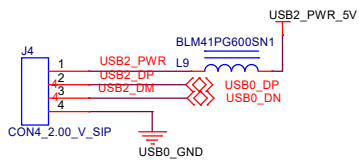
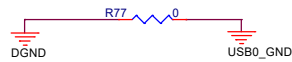
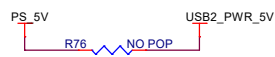
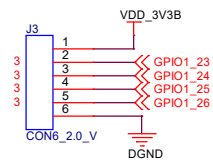
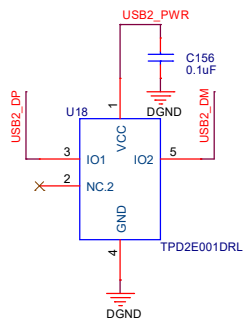
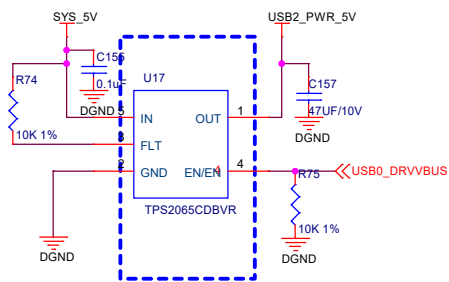




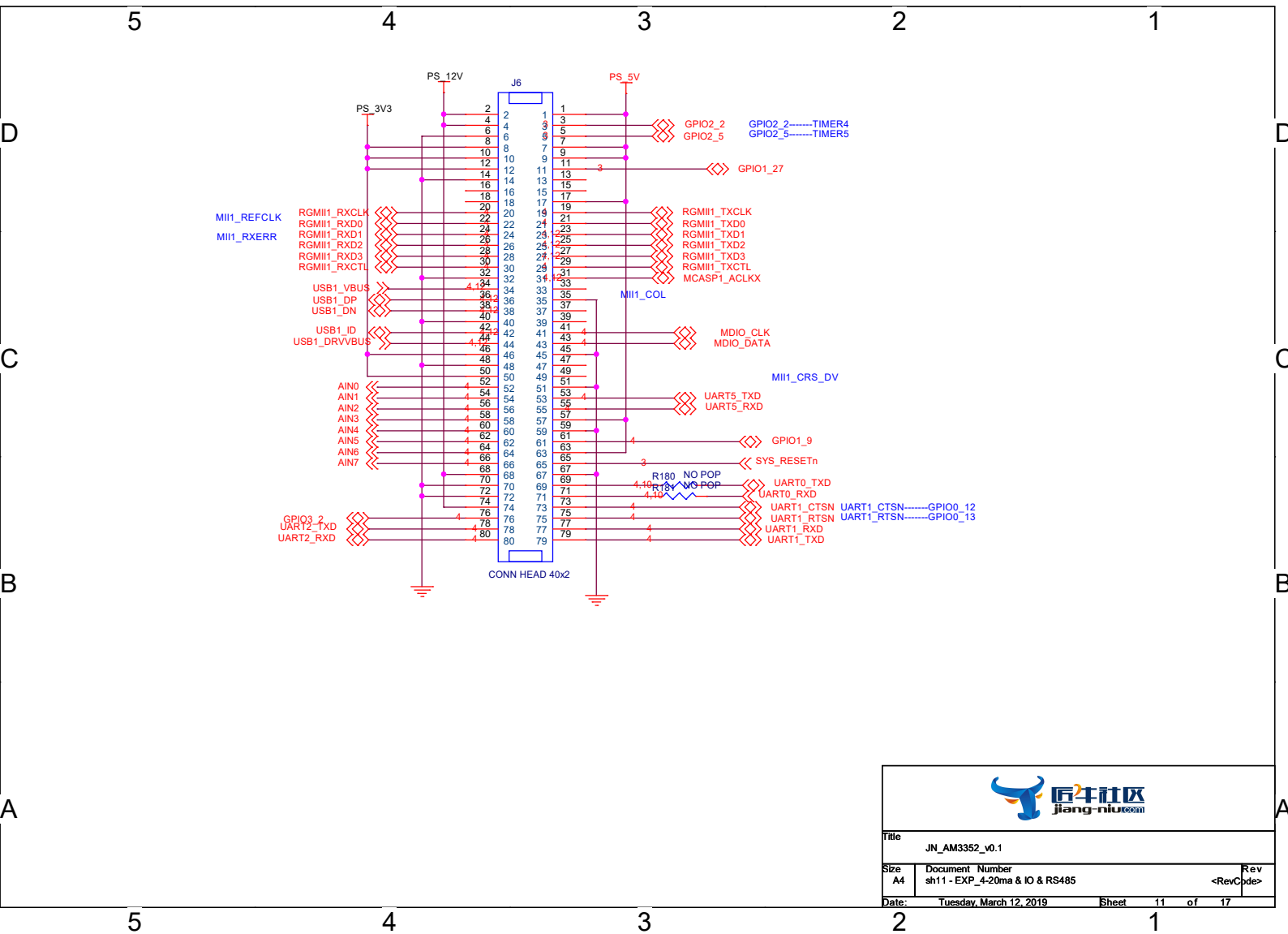


Title		
JN_AM3352_v0.1		
Size	Document Number	Rev
A4	sh07 - DDR3L_Memory	A
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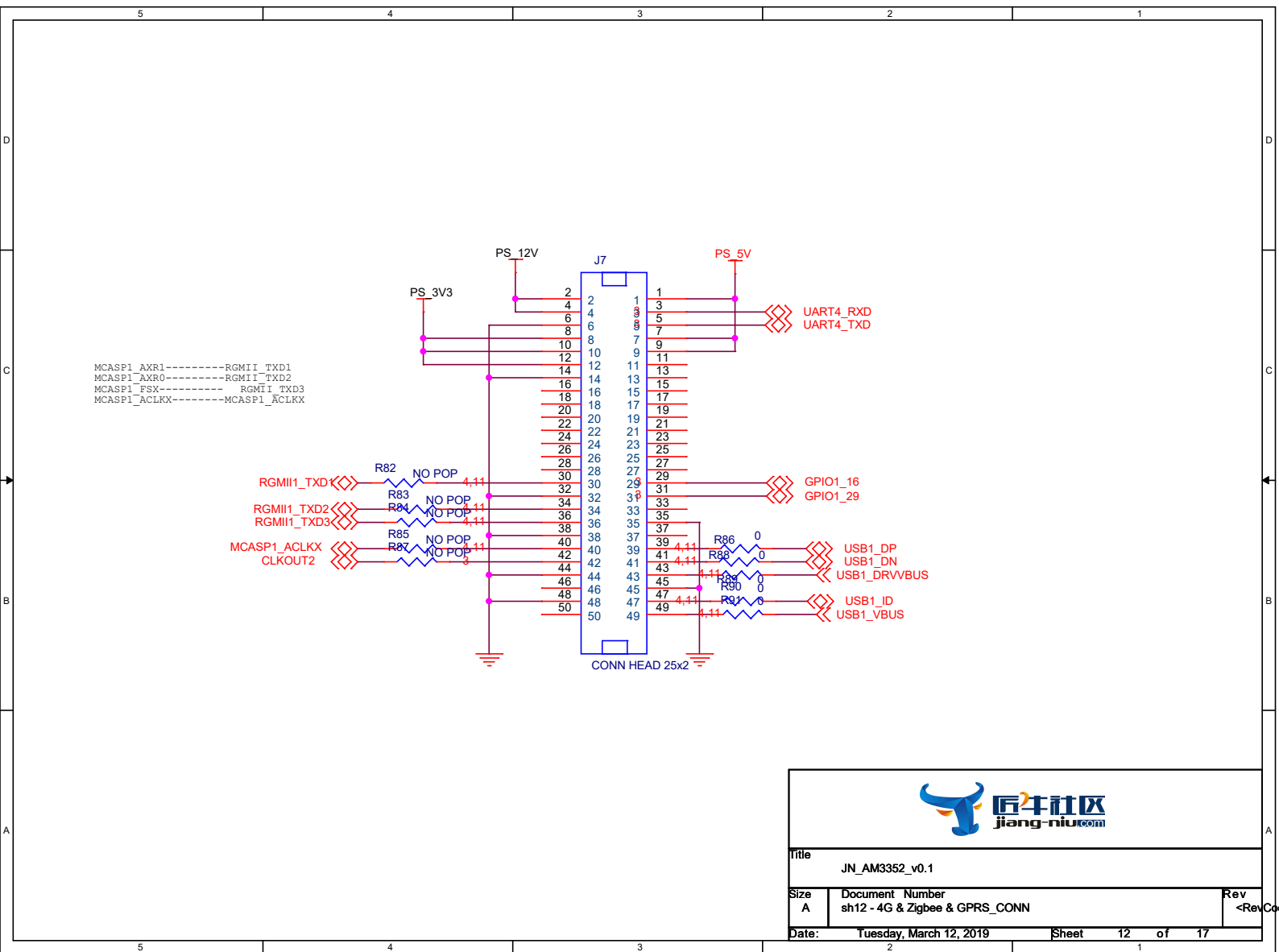




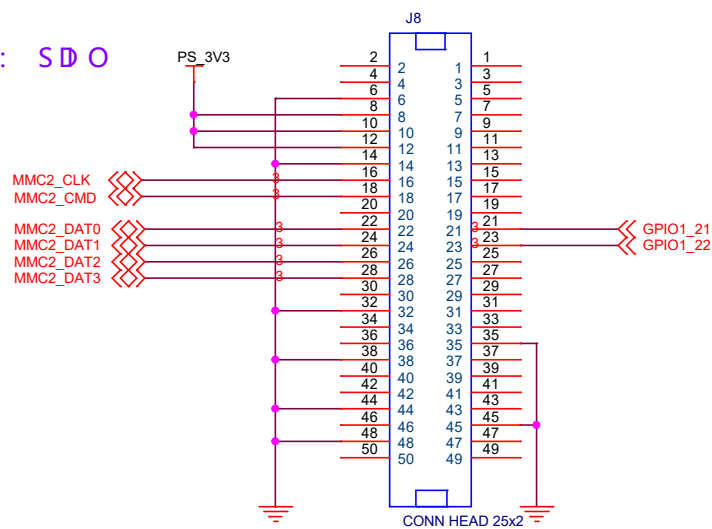
Title			
JN_AM3352_V0.1			
Size	Document	Number	Rev
A4	sh09 - USB2.0 & Keys		0.1
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JN_AM3352_v0.1			
Size	Document Number	Rev	
A4	sh11 - EXP_4-20ma & IO & RS485	<RevCode>	
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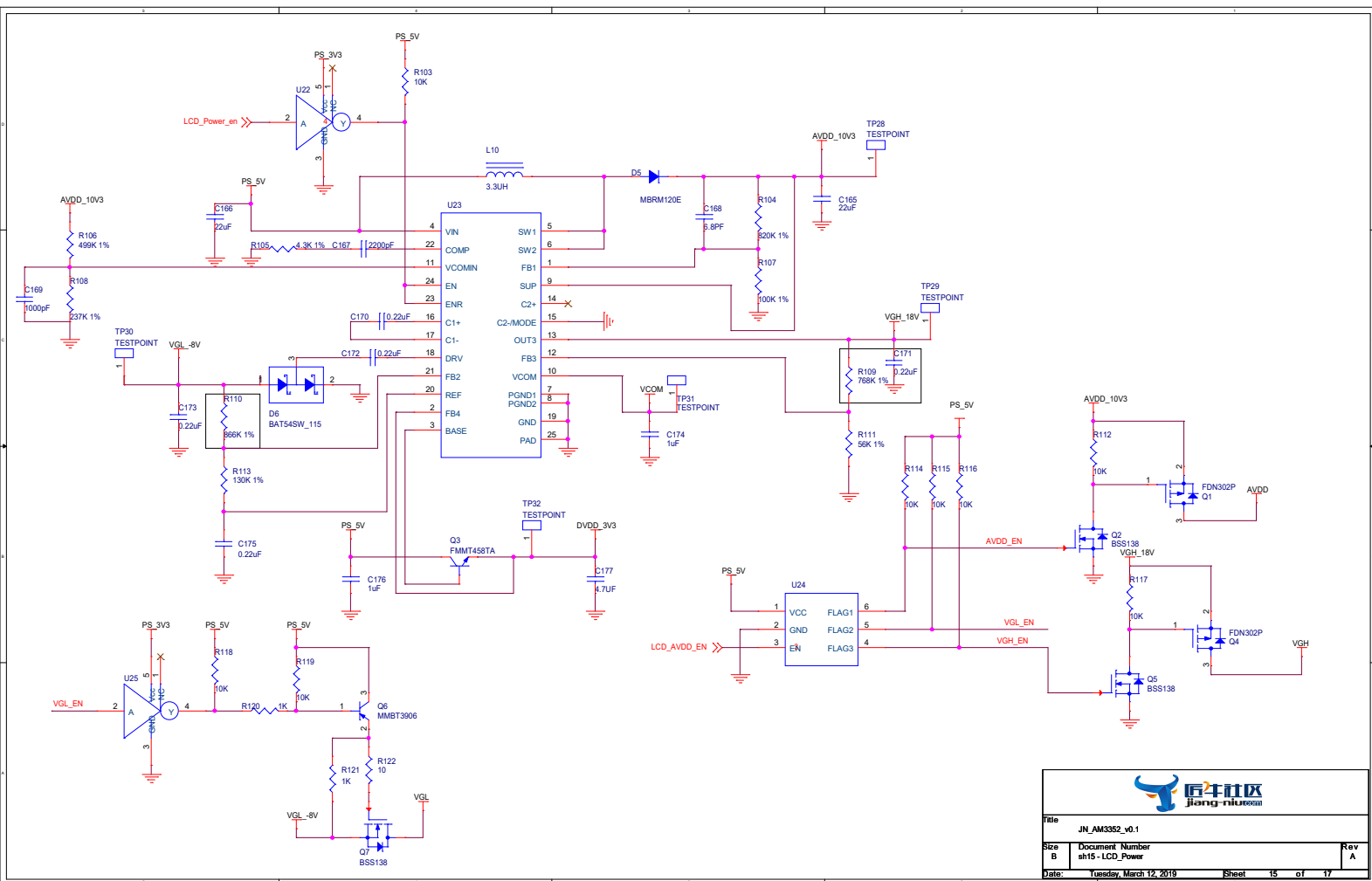
WIFI: S D O



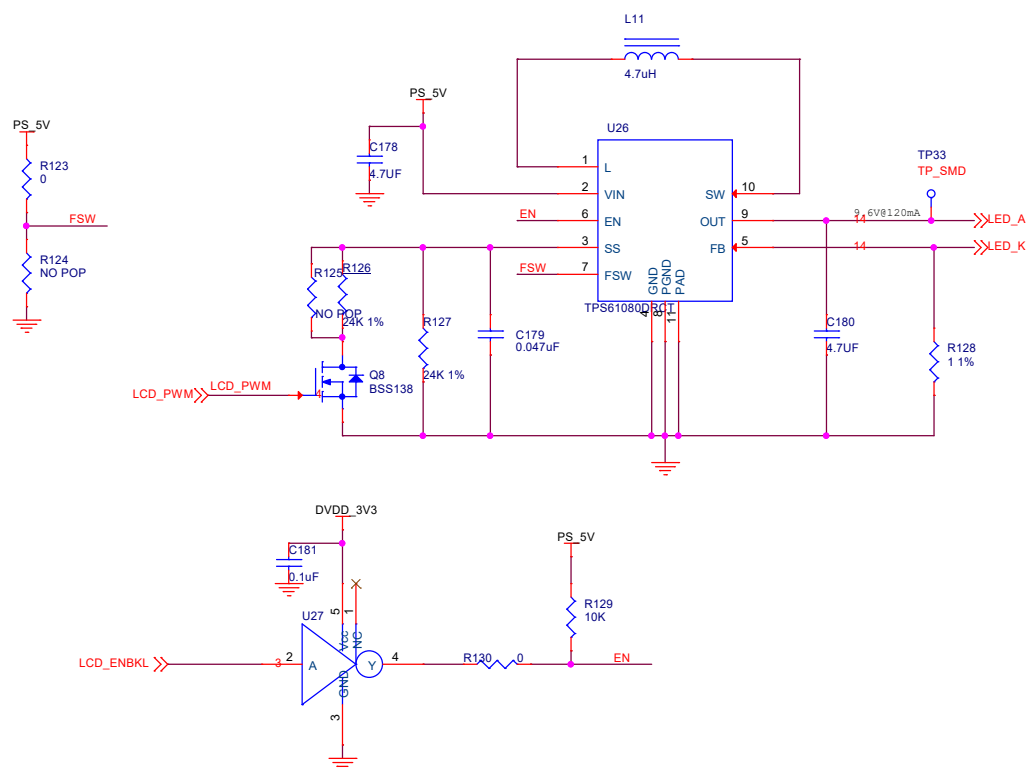
Title			JN_AM3352_v0.1
Size	Document Number	Rev	
A	sh13 - WIFI_CONNECTOR	<RevCo	
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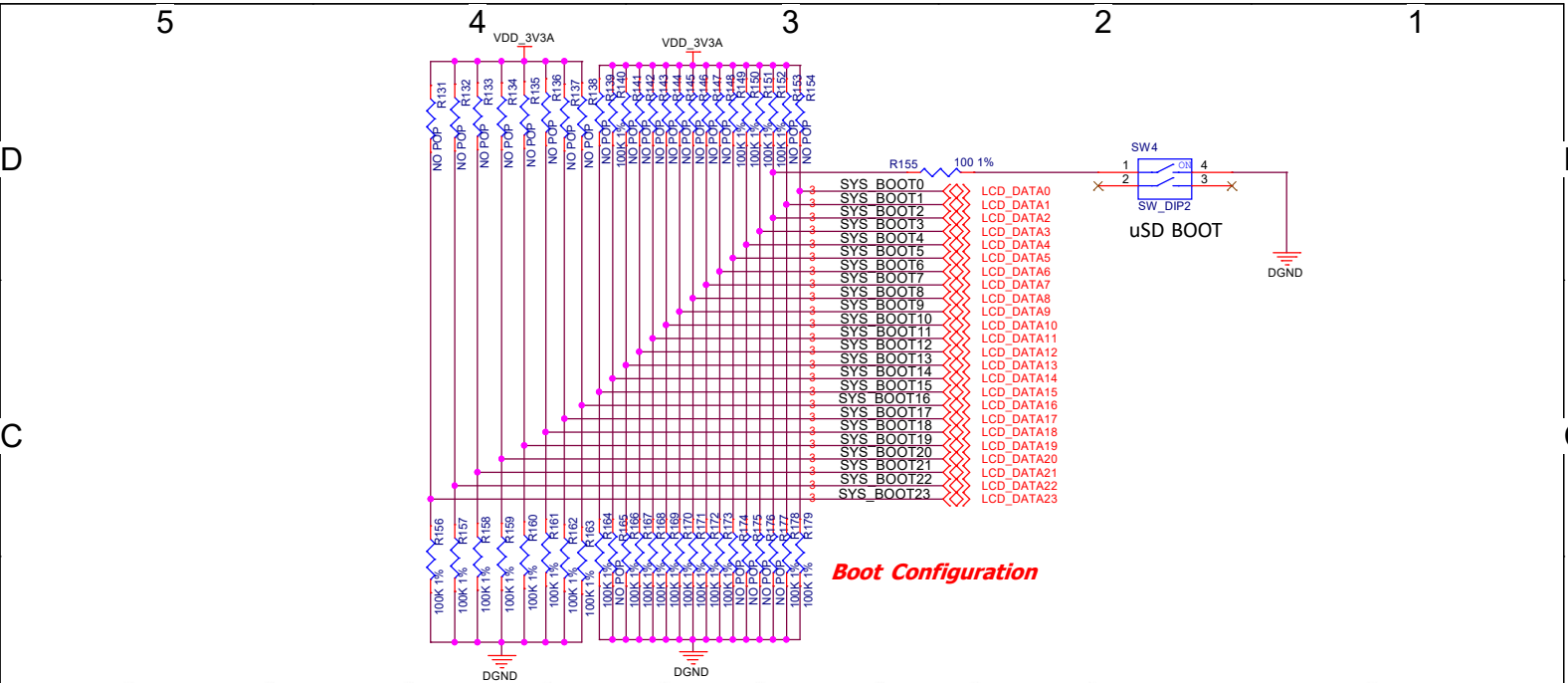
Rev	RevCod
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Title			JN_AM3352_v0.1
Size	Document Number	Rev	
B	sh15 - LCD_Power	A	
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A4	sh16 - BackLight Power		A
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SYSBOOT[15:14]	SYSBOOT[13:12]	SYSBOOT[11:10]	SYSBOOT[9]	SYSBOOT[8]	SYSBOOT[7:6]	SYSBOOT[5]	SYSBOOT[4:0]	Boot Sequence			
00b = 19.2MHz 01b = 24MHz 10b = 25MHz 11b = 26MHz	00b (all other values reserved)	Don't care for ROM code	Don't care for ROM code	Don't care for ROM code	Don't care for ROM code	0 = CLKOUT1 disabled 1 = CLKOUT1 enabled	11100b	MMC1	MMC0	UART0	USB0[5]
00b = 19.2MHz 01b = 24MHz 10b = 25MHz 11b = 26MHz	00b (all other values reserved)	Don't care for ROM code	Don't care for ROM code	Don't care for ROM code	Don't care for ROM code	0 = CLKOUT1 disabled 1 = CLKOUT1 enabled	11000b	SPI0	MMC0	USB0[5] 1	UART0



Title				JN_AM3354_ZDRK
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A4	<Doc>		A	
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