



CS0 : 1 word read access against Address 0x1003FF00

1st and 2nd address was output from GPMC.
 But it seem ADVn was asserted once because I guess
 ADVAADMUXRDOFFTIME and ADVONTIME was set
 to the same value(4)

Problem: CS0 never be deasserted.
 The device(not AM437x) is set as not to assert the
 WAIT signal during GPMC read access.
 In such a case, I guess AM437x side will end the
 read access but CS0 signal are keep asserting.
 Why?