



WARMRST is used only as output.

IMPORTANT
When black-out is detected, signal PMIC_POWER_EN to initiate Power-Down Sequence.

Tie nPFO to power-down sequence by setting OFFnPFO bit in the CONTROL register to 1.

USB

A1	VSS_1	VSS_37	P14
A25	VSS_2	VSS_38	P15
H15	VSS_3	VSS_39	R12
H18	VSS_4	VSS_40	R15
J9	VSS_5	VSS_41	R17
J12	VSS_6	VSS_42	R18
J15	VSS_7	VSS_43	T12
J17	VSS_8	VSS_44	T15
K9	VSS_9	VSS_45	T17
K11	VSS_10	VSS_46	U8
K12	VSS_11	VSS_47	U9
K14	VSS_12	VSS_48	U10
K15	VSS_13	VSS_49	U11
L8	VSS_14	VSS_50	U12
L9	VSS_15	VSS_51	U13
L11	VSS_16	VSS_52	U16
L15	VSS_17	VSS_53	U17
L17	VSS_18	VSS_54	U18
L18	VSS_19	VSS_55	U19
M10	VSS_20	VSS_56	V9
M11	VSS_21	VSS_57	V10
M13	VSS_22	VSS_58	V11
M15	VSS_23	VSS_59	V12
M16	VSS_24	VSS_60	V13
N10	VSS_25	VSS_61	V14
N11	VSS_26	VSS_62	V18
N12	VSS_27	VSS_63	V18
N13	VSS_28	VSS_64	AA23
N14	VSS_29	VSS_65	AE1
N15	VSS_30	VSS_66	AE7
P8	VSS_31	VSS_67	AE8
P9	VSS_32	VSS_68	AE10
P10	VSS_33	VSS_69	AE25
P11	VSS_34	VSS_OSC	B24
P12	VSS_35	VSS_RTC	AD4
P13	VSS_36	VSSA_ADC	AC15
		VSSA_USB	W23

AM4378BZDND100

Title: POWER MANAGEMENT IC		
Size: A4	Revision: *	Drawn by: *
Date: 18/09/2022	Sheet 3 of 28	Check by: *
File:		

