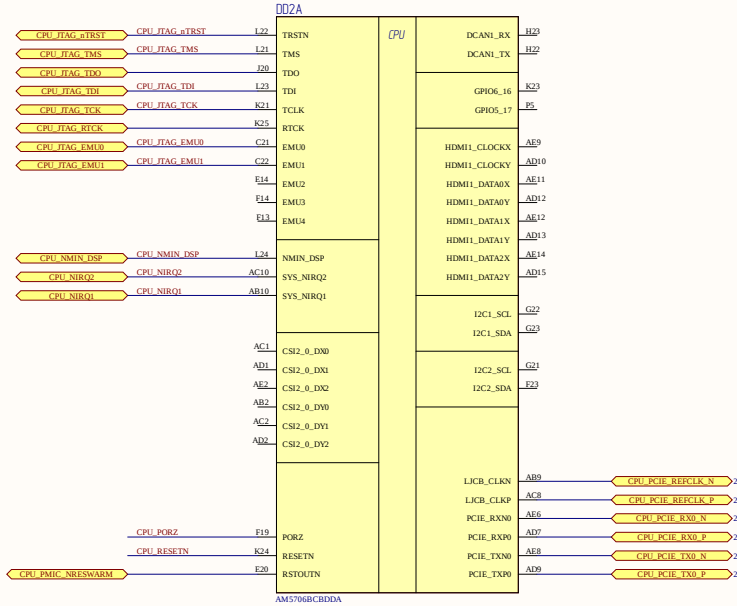


CPU



FPGA

