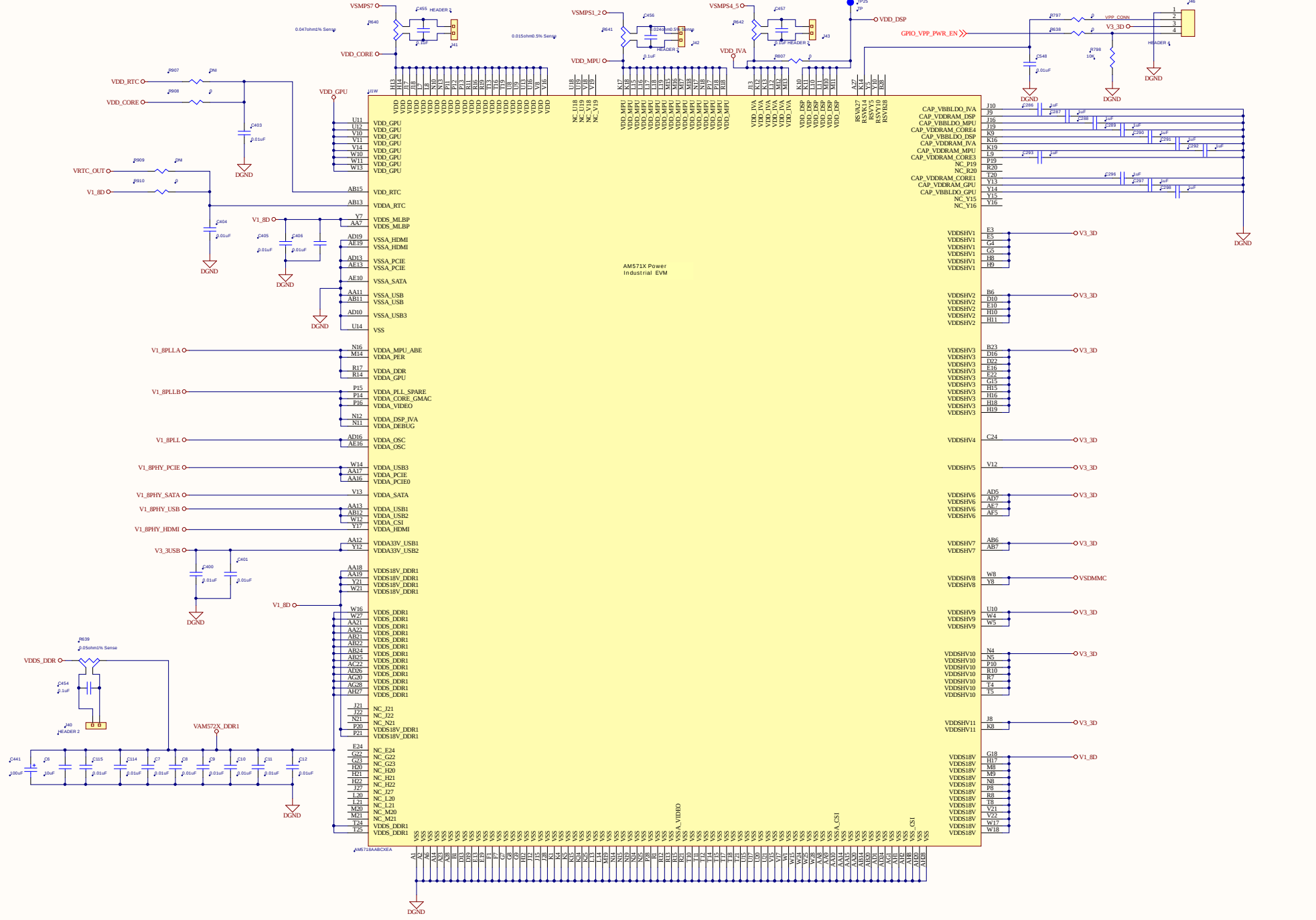
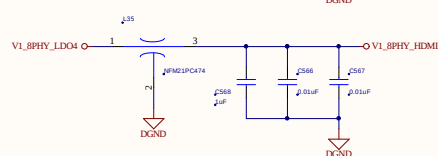
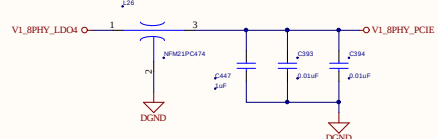
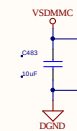
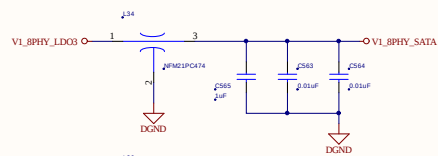
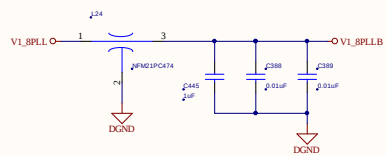
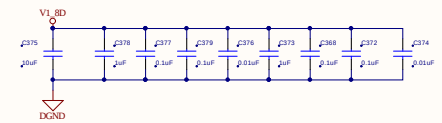
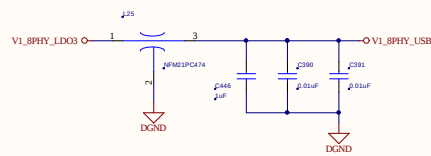
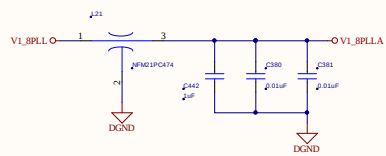
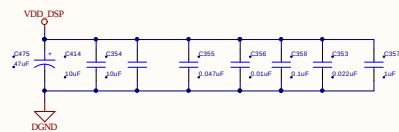
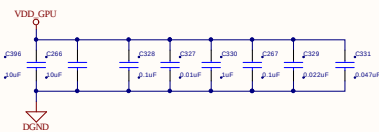
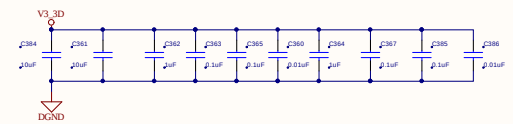
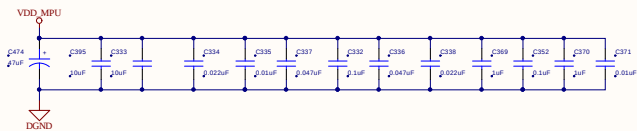
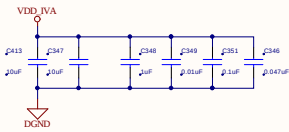
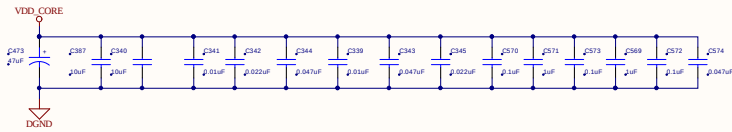
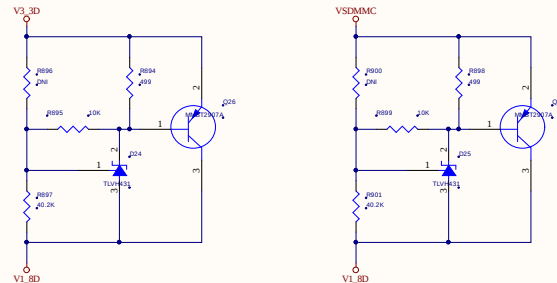
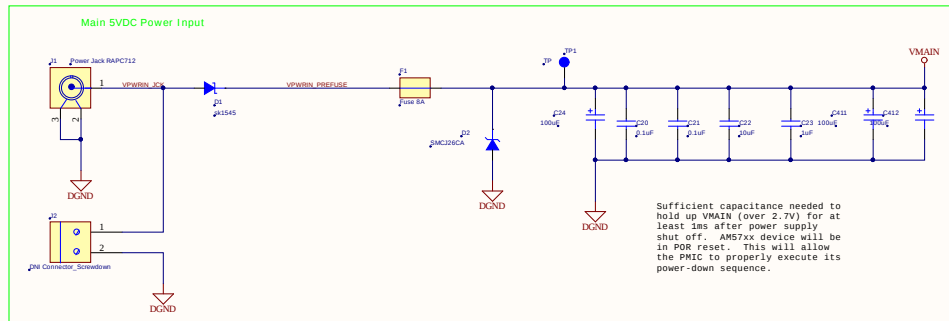
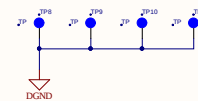
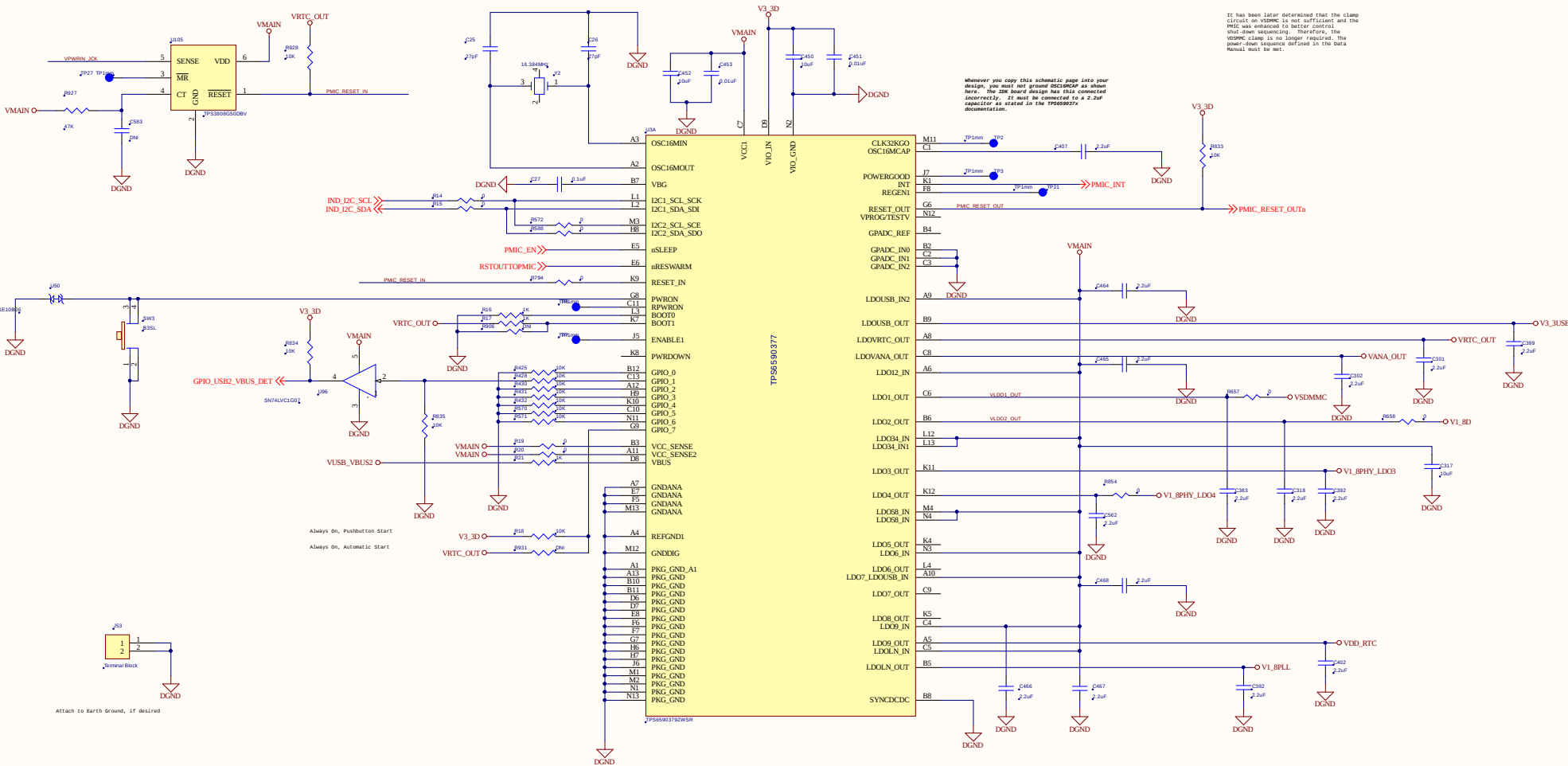




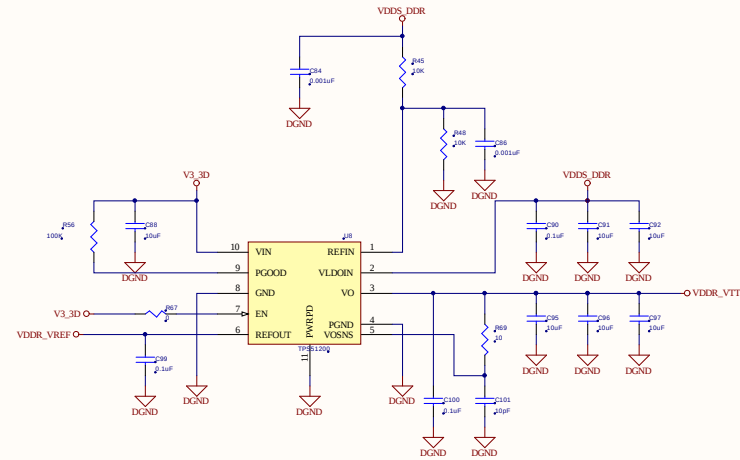
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Doc #	Document Number	Modified By	Rev
C48000		John Johnson	1.0
Date	Sheet		2 of 2



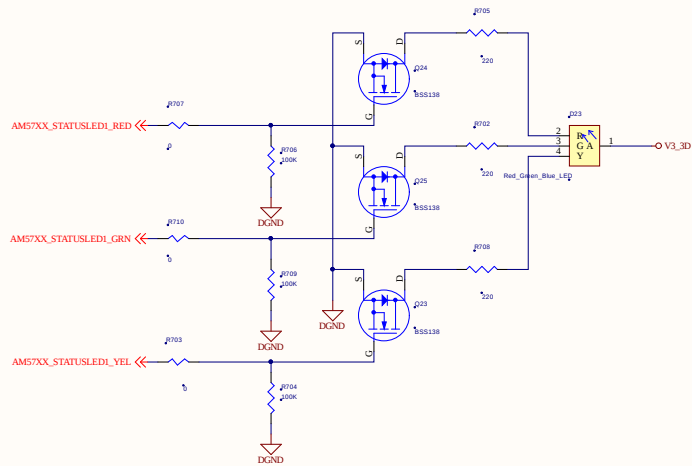
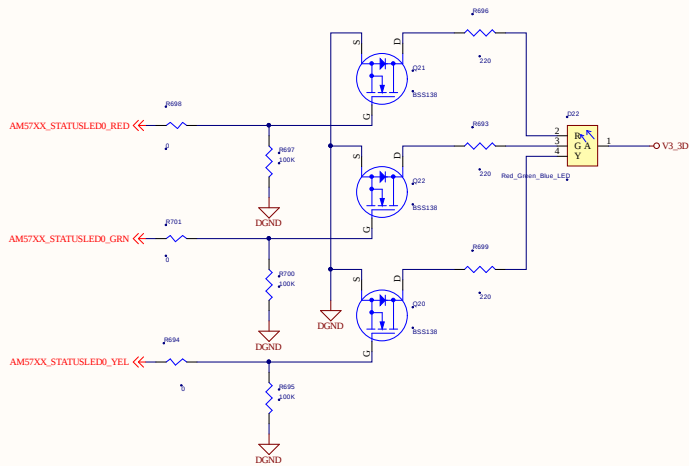
It has been later determined that the clamp circuit on VSDMMC is not sufficient and the PMIC was enhanced to better control shut-down sequencing. Therefore, the VSDMMC clamp is no longer required. The power-down sequence defined in the Data Manual must be met.



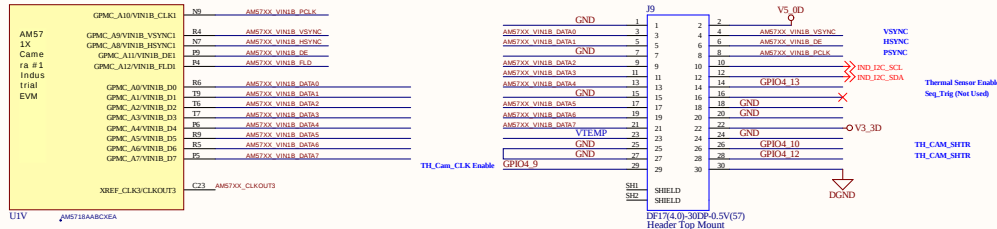
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Status LED's

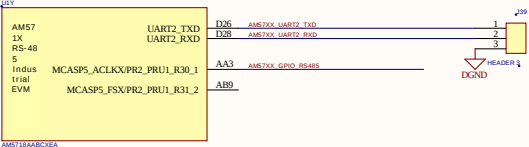


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Doc #	Document Number	Modified By	Rev
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Date		Sheet	2 of 2

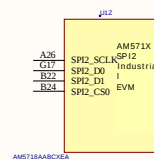


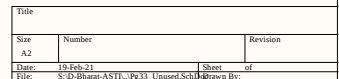












Board Slack Report