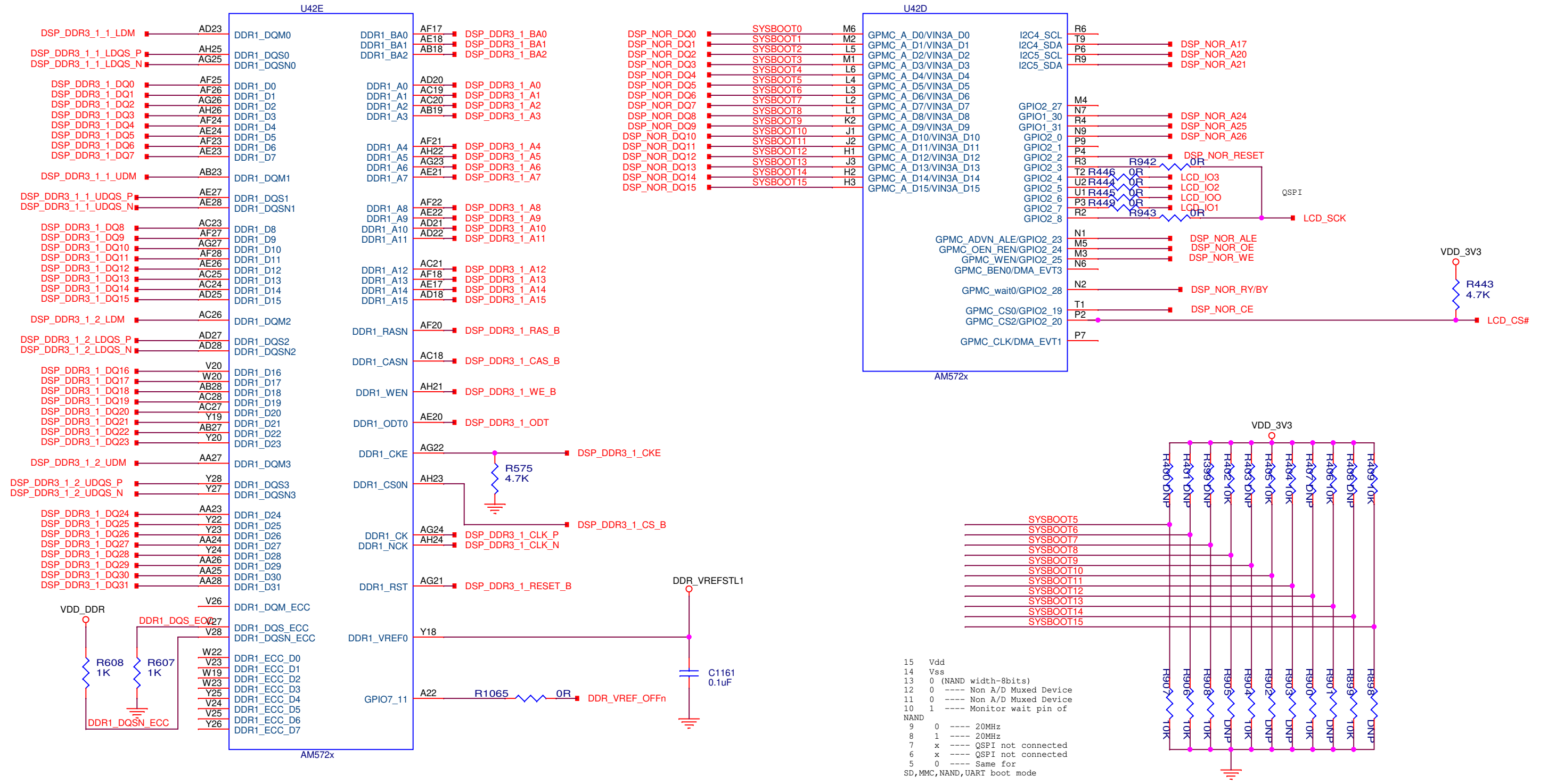
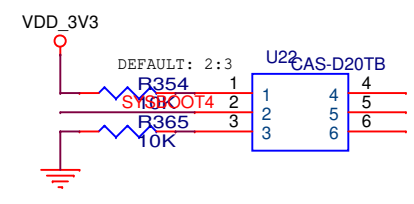
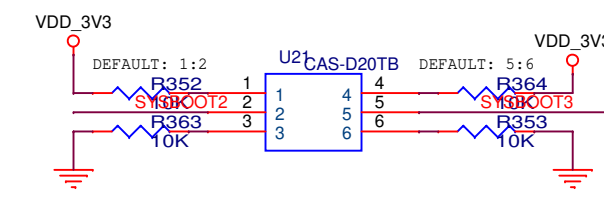
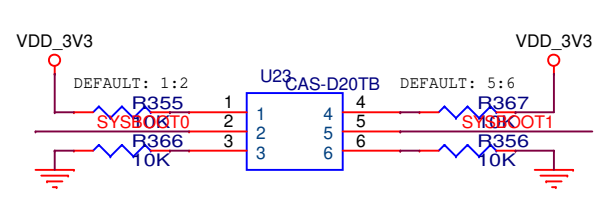


Processor-AM5728

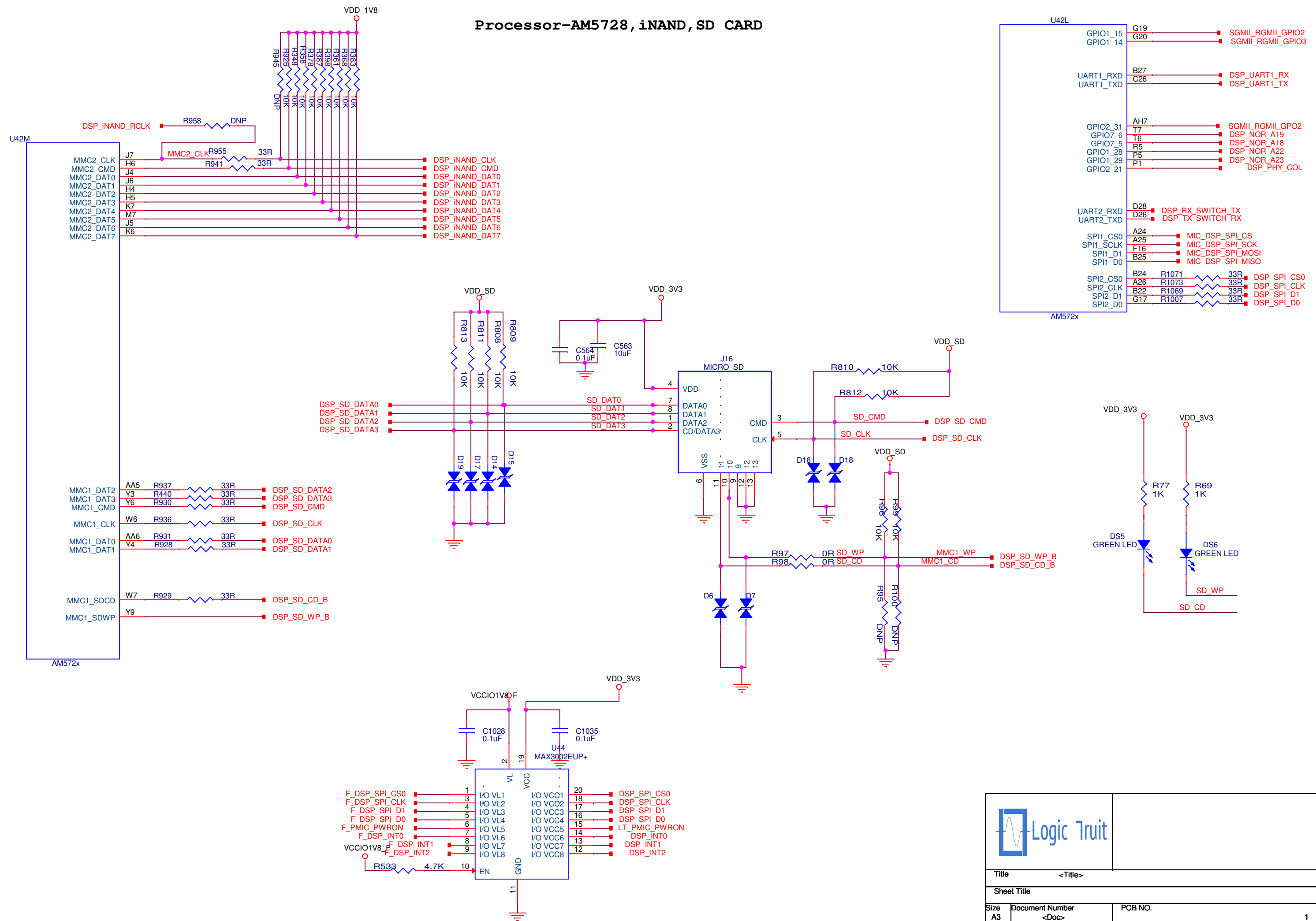


15 Vdd
 14 Vss
 13 0 (NAND width-8bits)
 12 0 ---- Non A/D Muxed Device
 11 0 ---- Non A/D Muxed Device
 10 1 ---- Monitor wait pin of
 NAND
 9 0 ---- 20MHz
 8 1 ---- 20MHz
 7 x ---- QSPI not connected
 6 x ---- QSPI not connected
 5 0 ---- Same for
 SD, MMC, NAND, UART boot mode



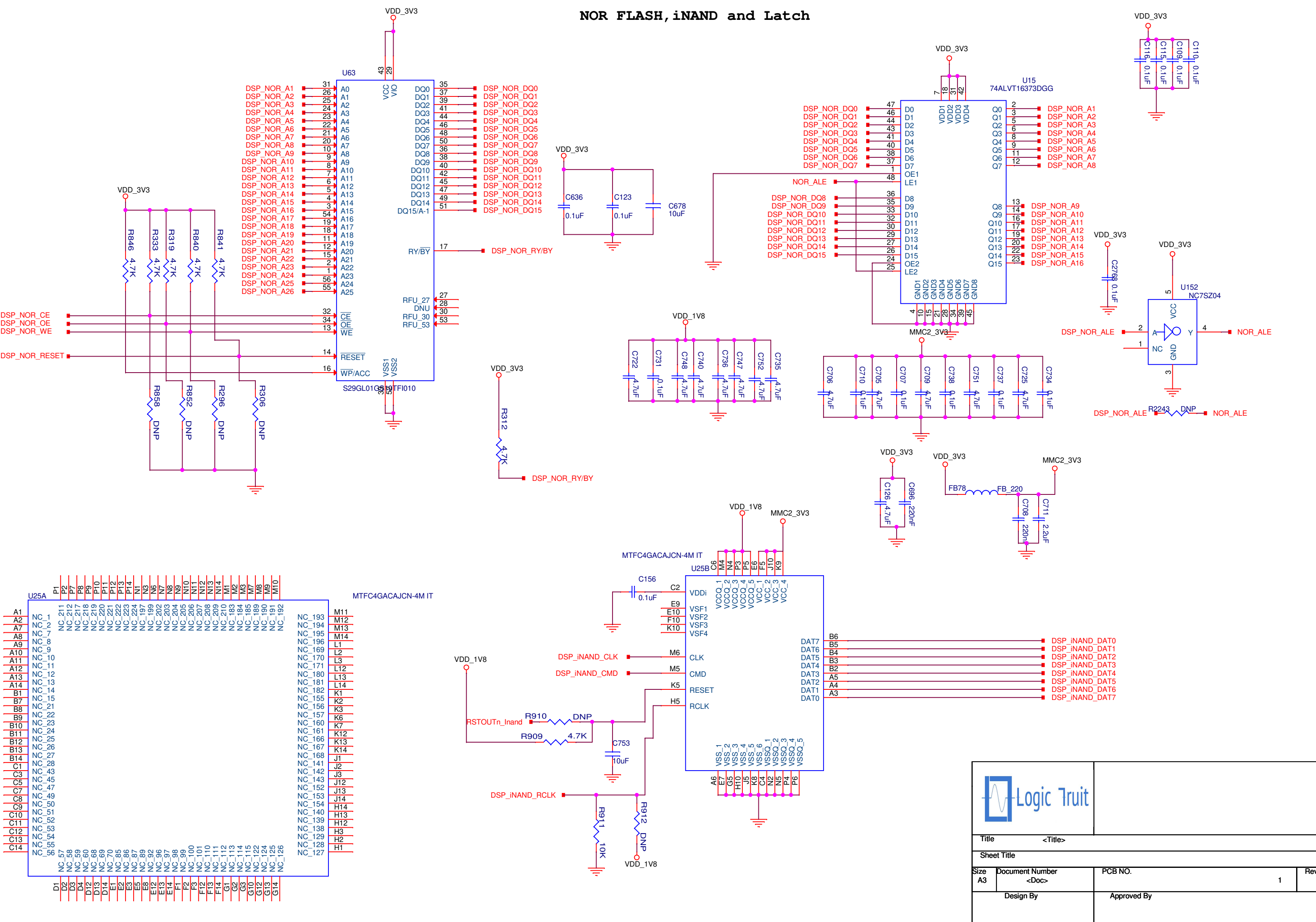
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Design By		Approved By	
Date: Thursday, July 25, 2019		Sheet 7	of 71


Processor-AM5728, iNAND, SD CARD



		Title <Title>	
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Size A3	Document Number <Doc>	PCB NO.	Rev 1
Design By		Approved By	
Date: Thursday, July 25, 2019		Sheet 12 of 71	

NOR FLASH, iNAND and Latch





Title: <Title>

Sheet Title

Size A3	Document Number <Doc>	PCB NO.	1
Design By		Approved By	
Date: Thursday, July 25, 2019			

Sheet 20 of 71