

REV	DESCRIPTION	DATE	BY
A3a		7/11/2016	TI

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- P02 12V INPUT, 3.3V AND 5V SWITCHERS
- P03 TPS6590376 1 CONTROL
- P04 TPS6590376 2 RESET, & RTC
- P05 TPS6590376 3 MPU,DDR, & DSP SWITCHERS
- P06 TPS6590376 4 CORE & 1.8V
- P07 AM572X CORE POWER & EEPROM
- P08 AM572X CLOCKS AND RESET
- P09 AM572X IO POWER AND GROUND
- P10 AM572X DDR3 POWER & JTAG
- P11 AM572X ETHERNET 1
- P12 AM572X ETHERNET 2
- P13 AM572X GPMC & LCD
- P14 AM572X USB INTERFACES
- P15 AM572X I/O, LEDs, AND GPIO
- P16 AM572X AUDIO & DEBUG SERIAL
- P17 AM572X HDMI PORT
- P18 AM572X eMMC, MMC3, & uSD
- P19 AM572X DDR INTERFACE 1
- P20 AM572X DDR INTERFACE 2
- P21 DDR3 BANK 1
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Catalog Processors

Texas Instruments, Inc.

Catalog Processors Business Unit
12500 TI Blvd
Dallas, TX 75243



AM572x General Purpose EVM Processor Module

Title Page

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B

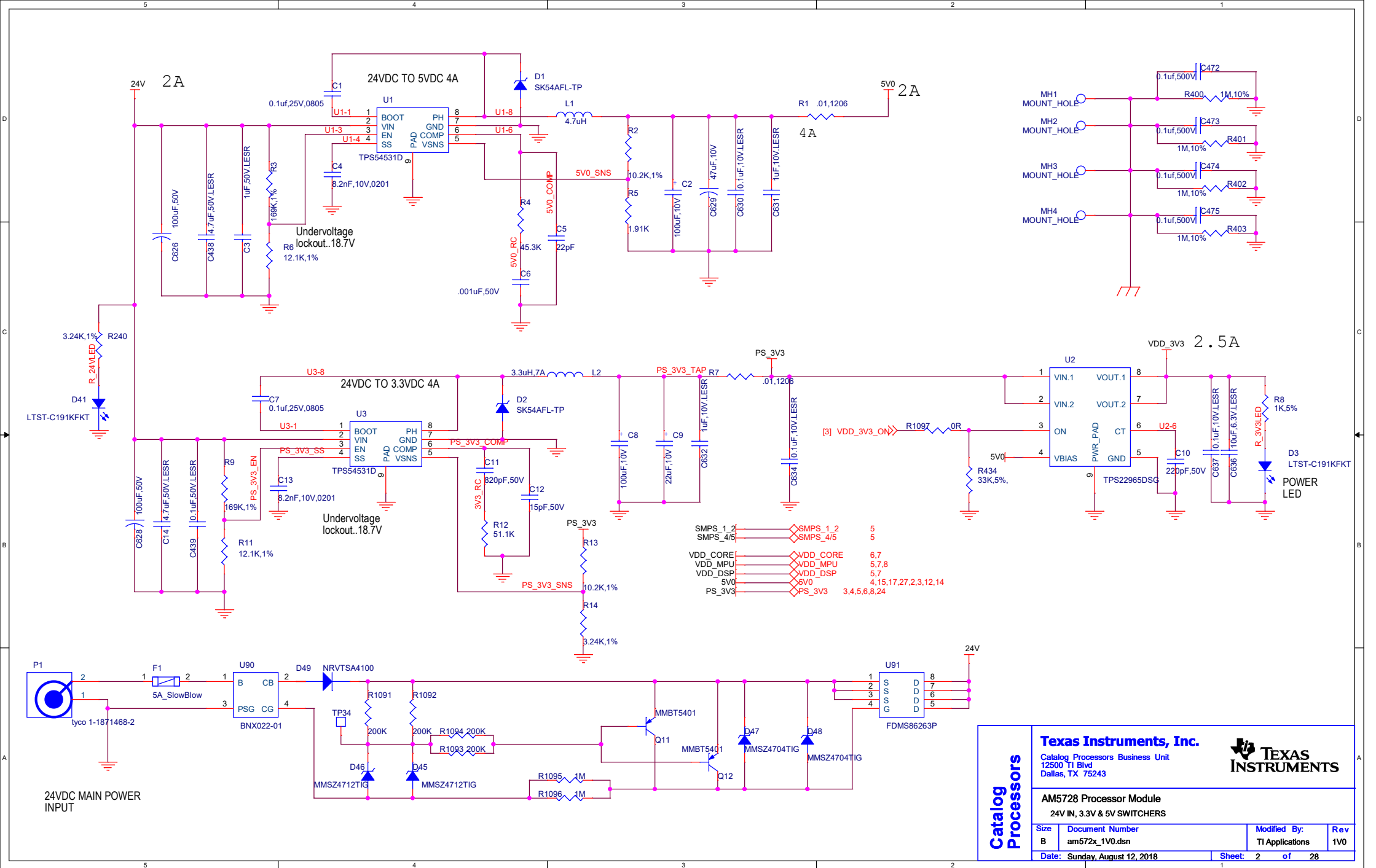
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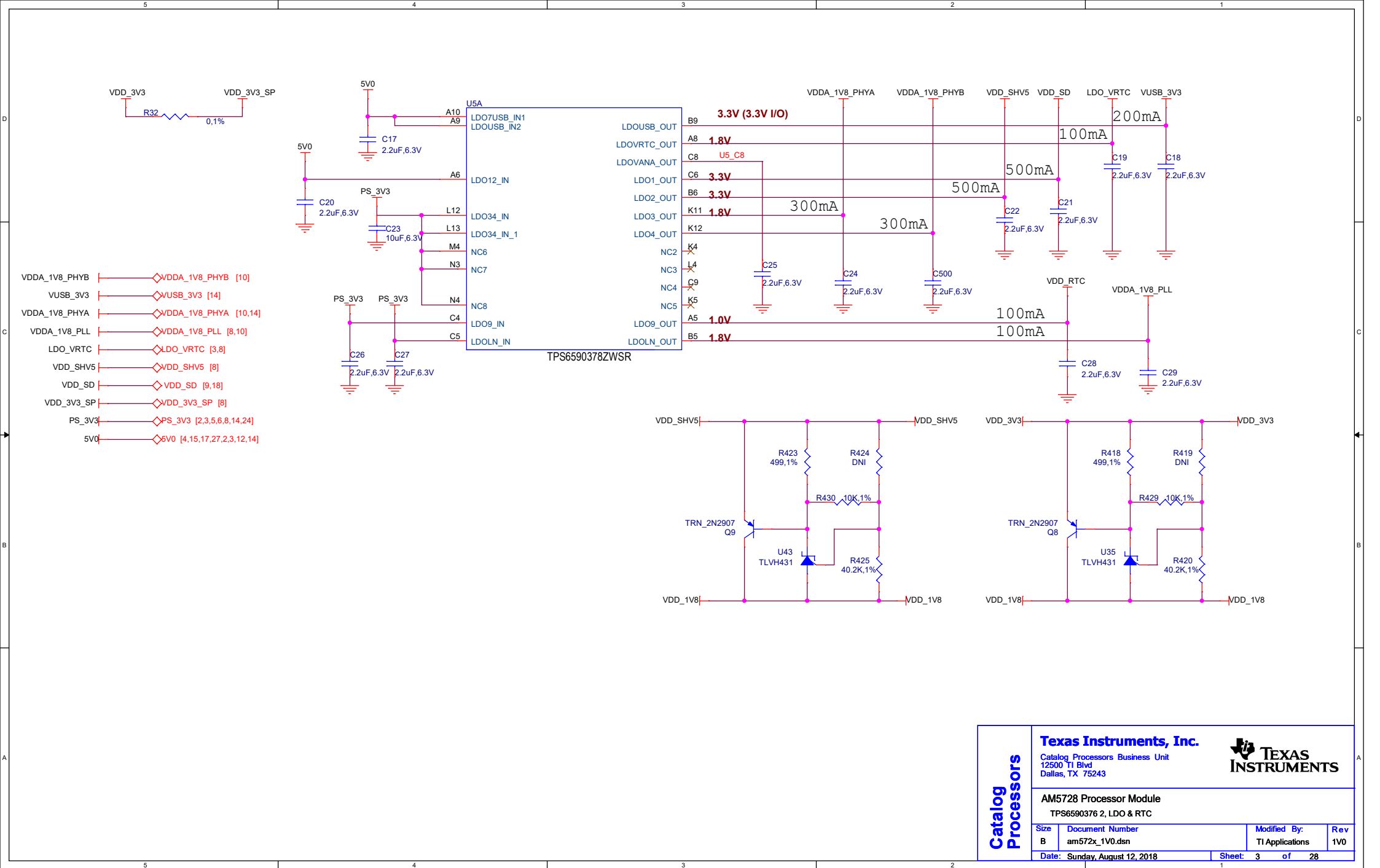
TI Applications

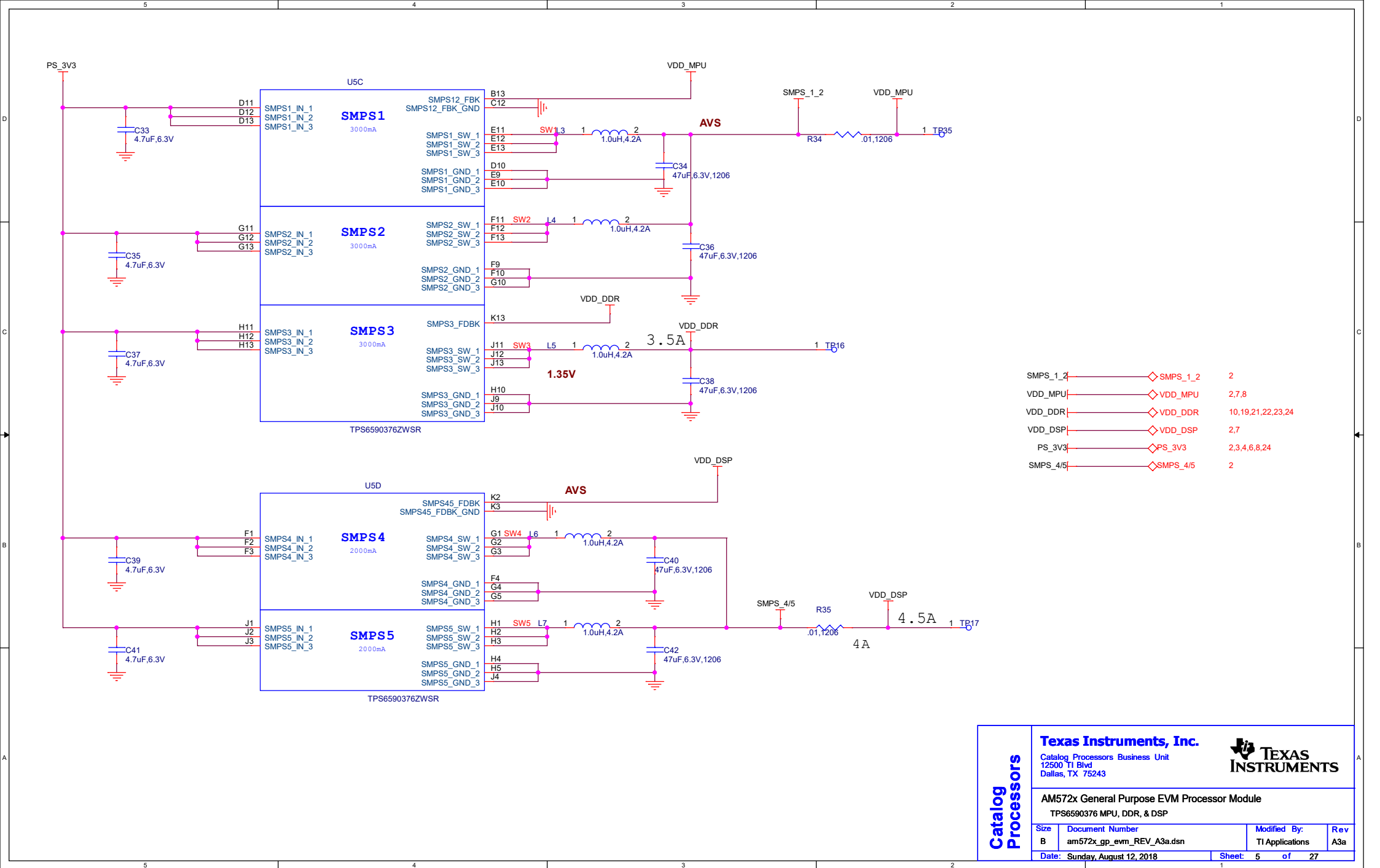
A3a

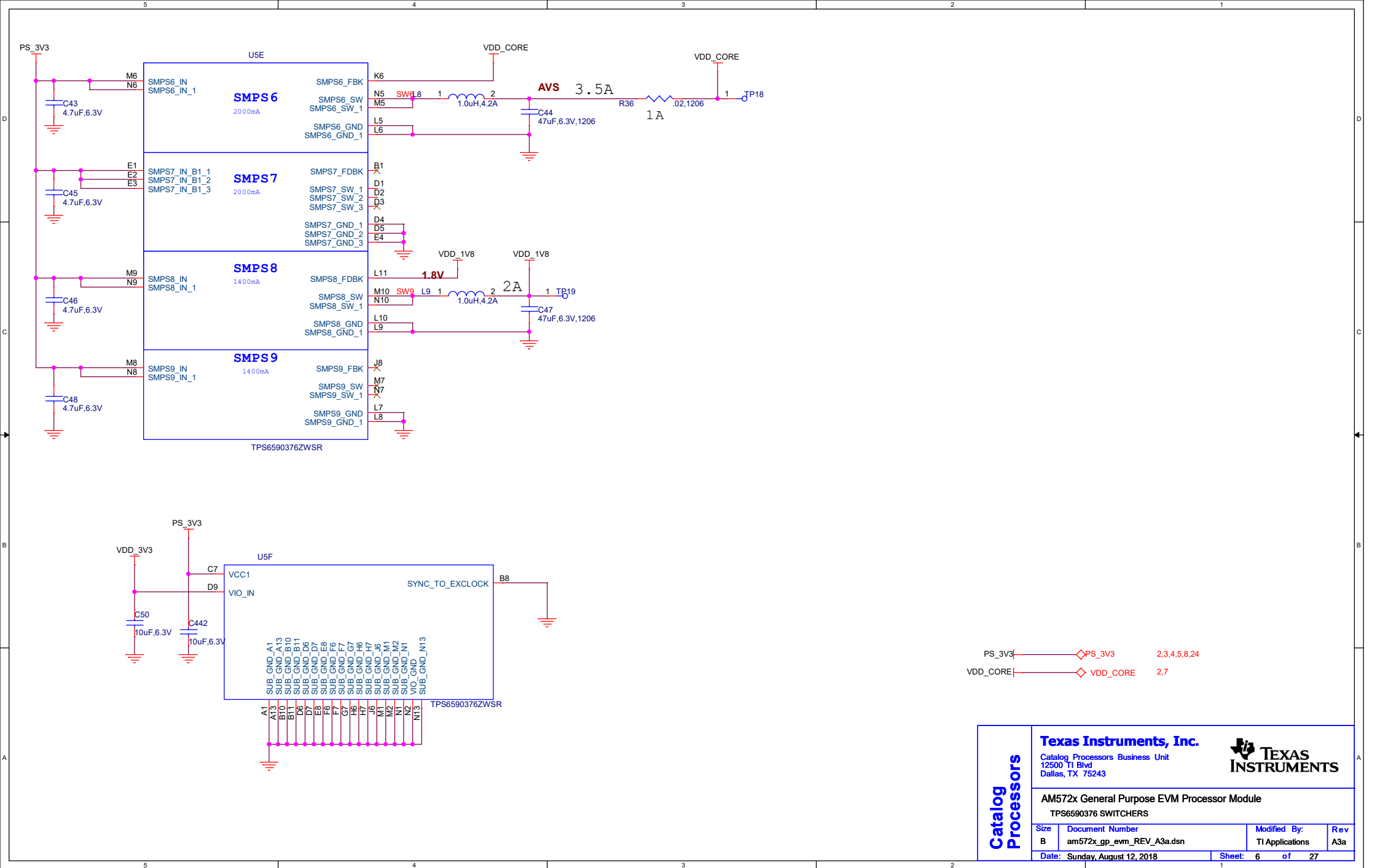
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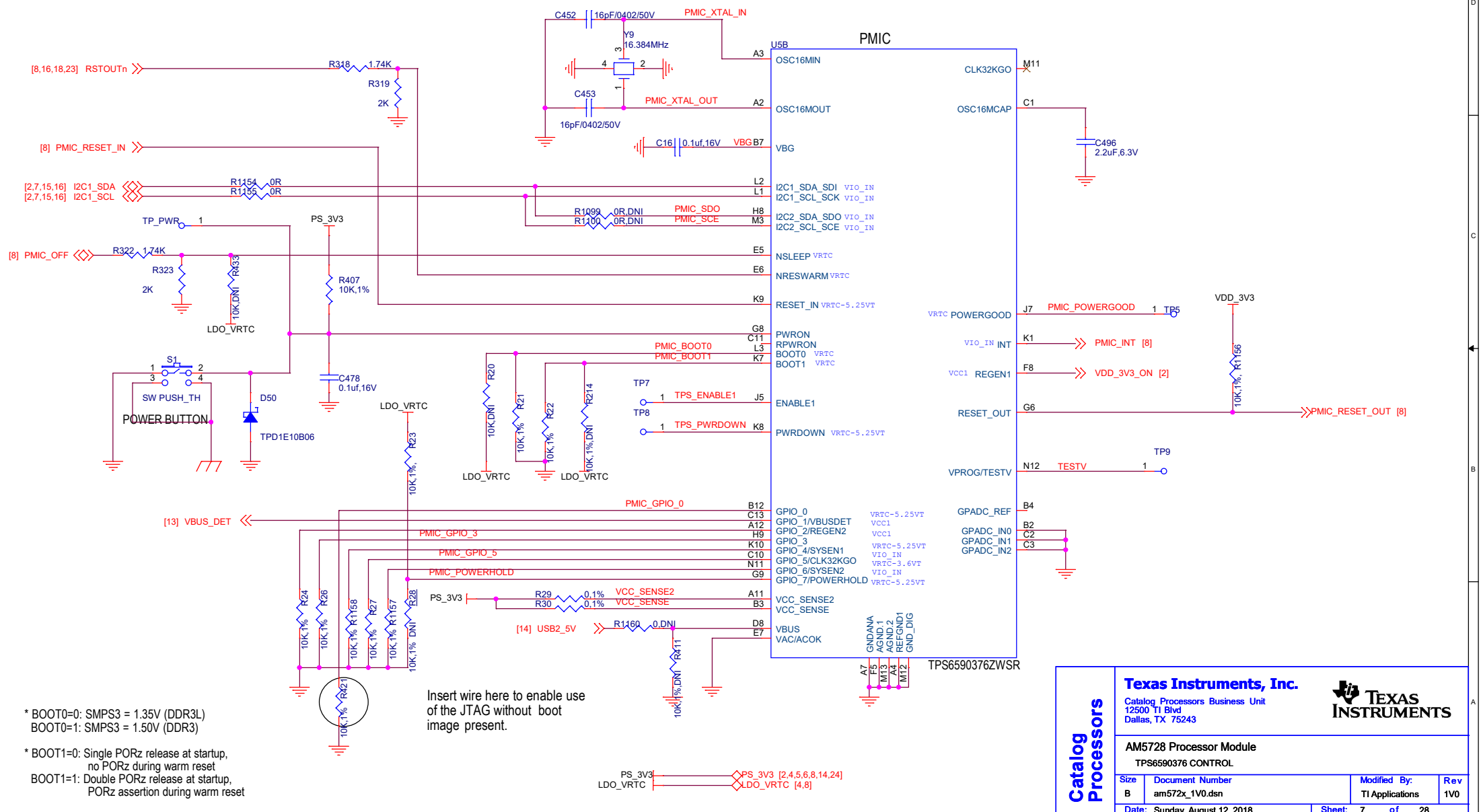


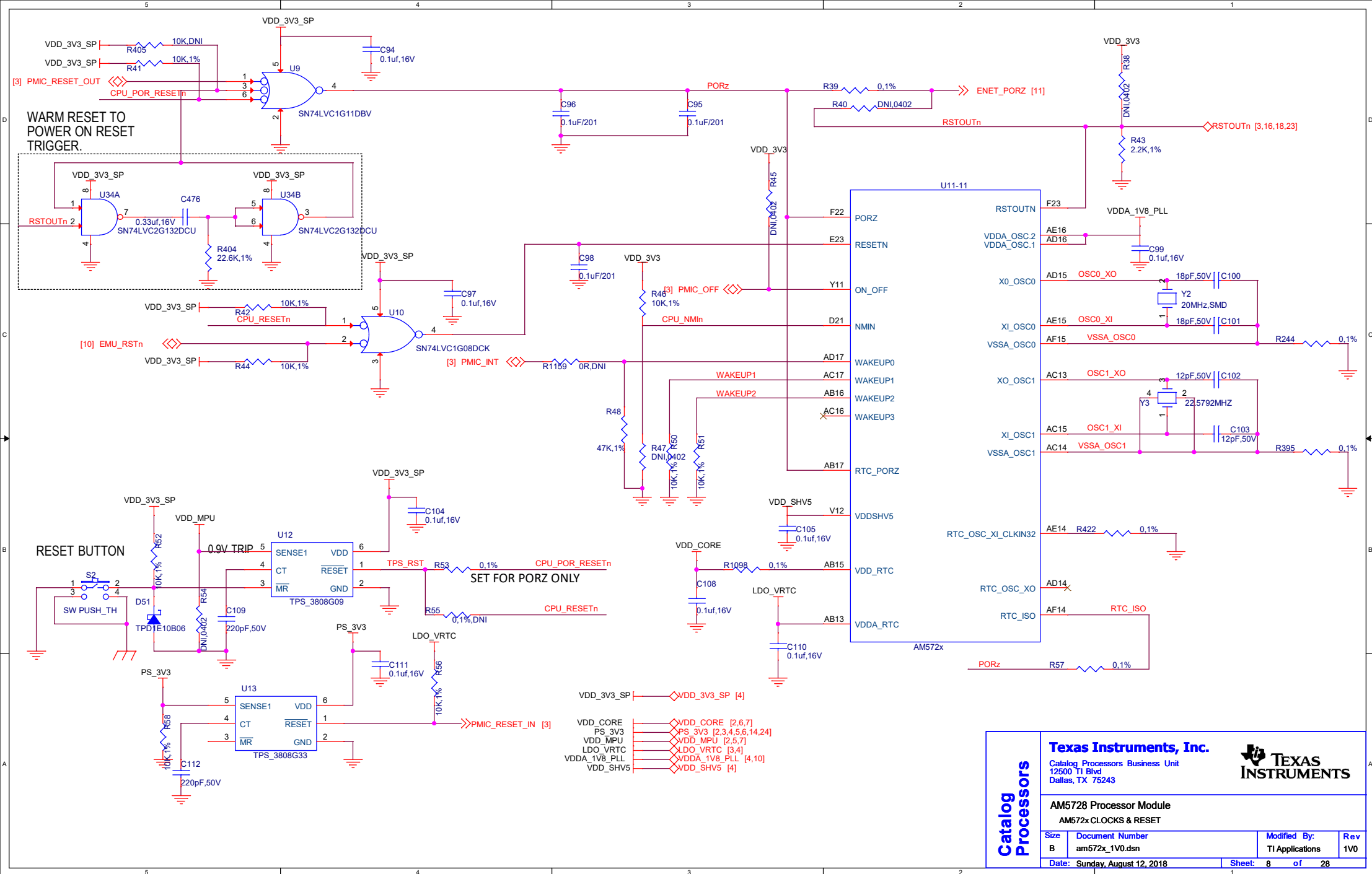


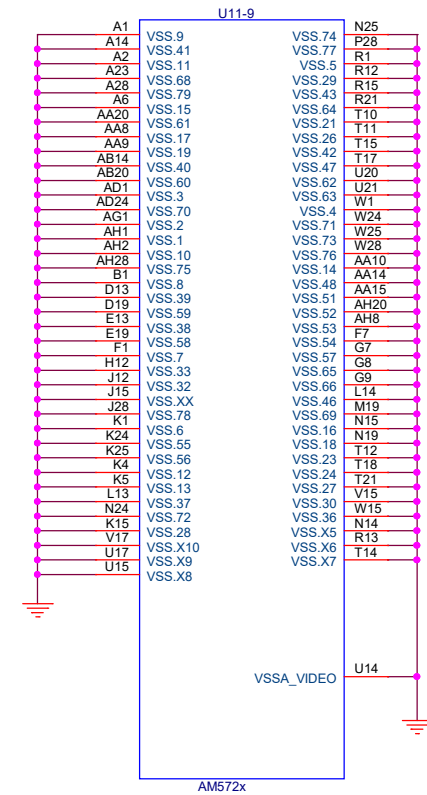
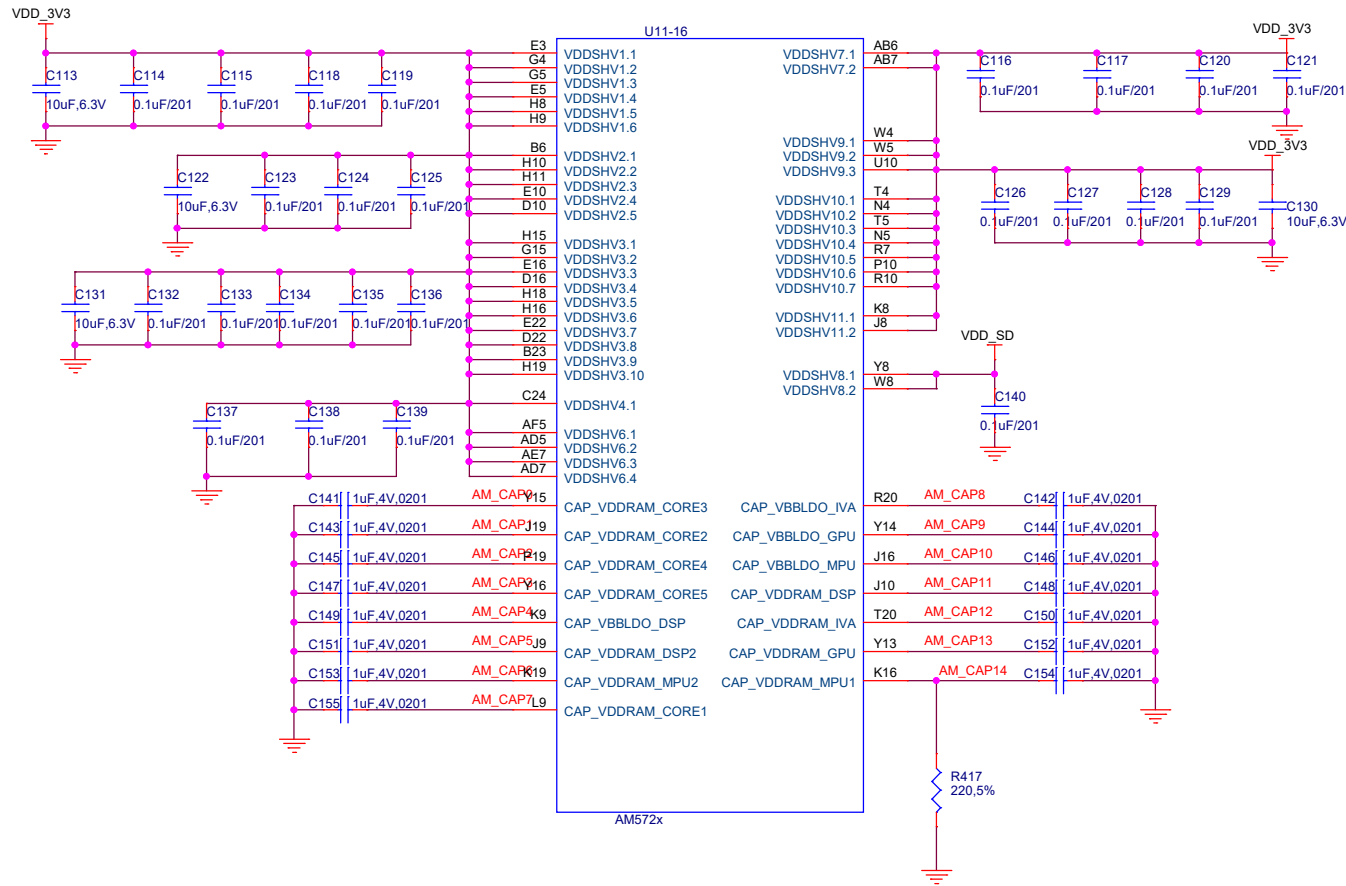




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	AM572x General Purpose EVM Processor Module TPS6590376 SWITCHERS		
	Size	Document Number	Modified By:
B	am572x_gp_evm_REV_A3a.dsn	TI Applications	A3a
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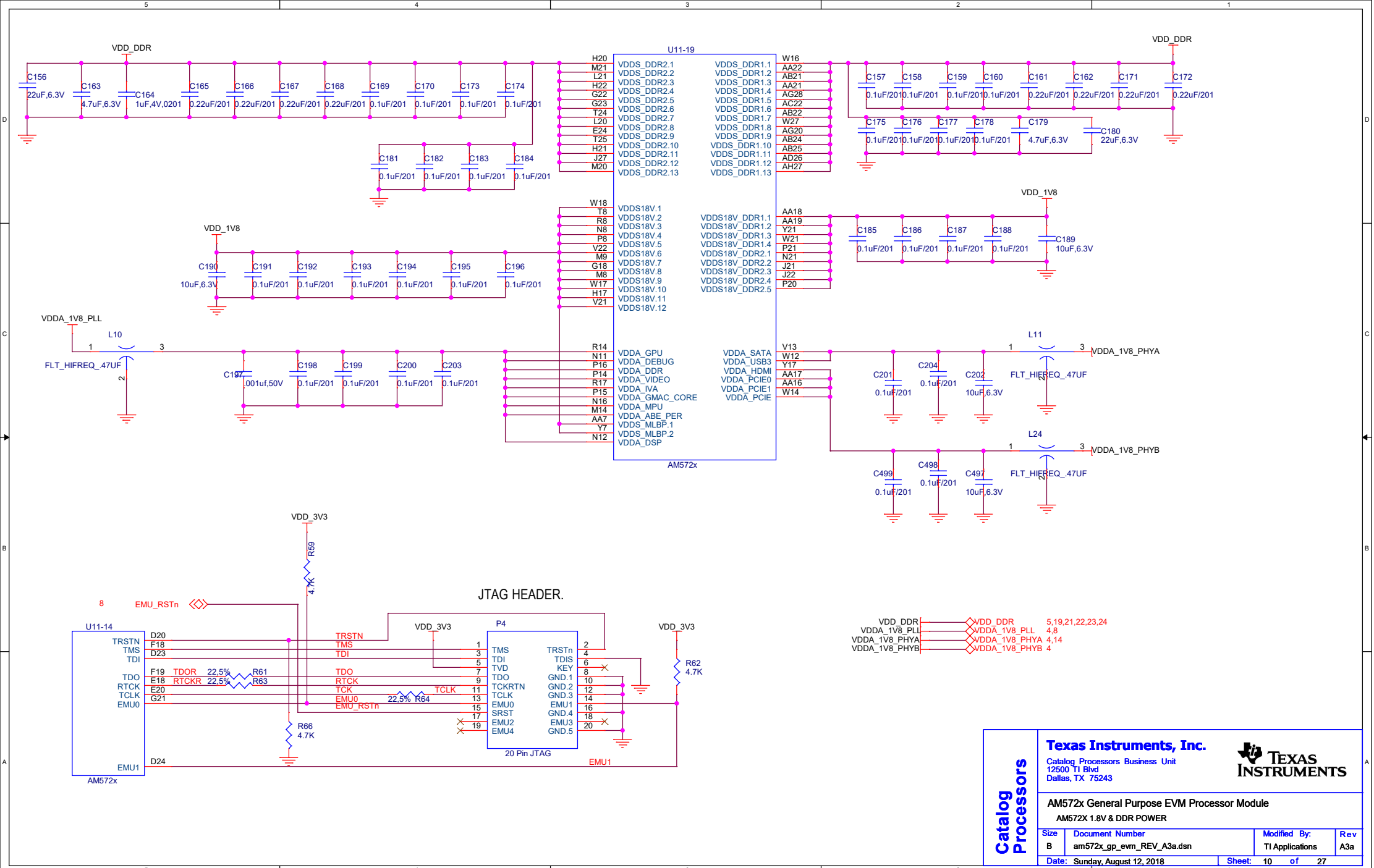


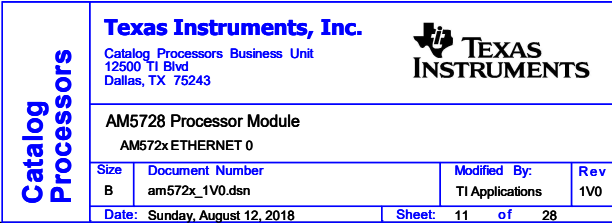


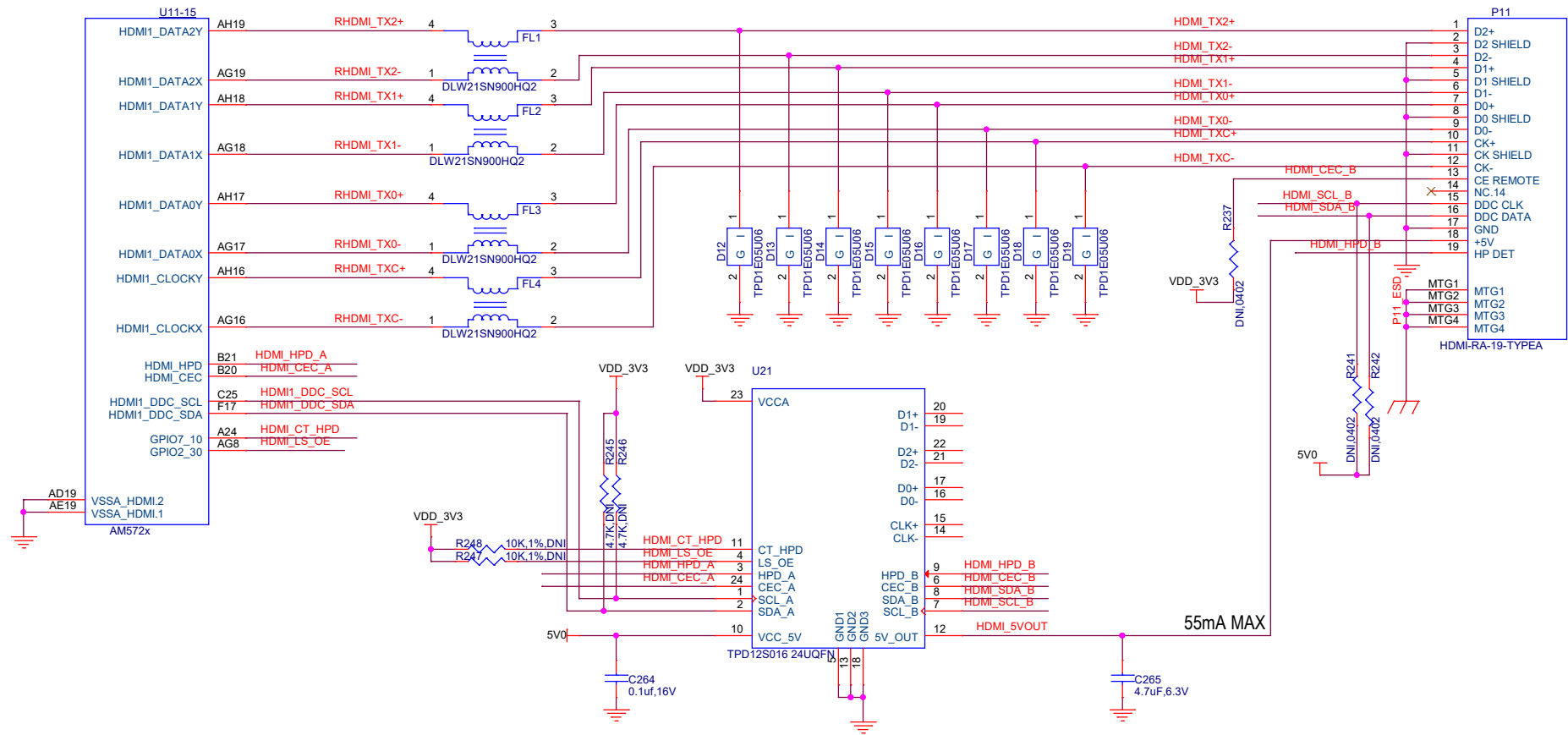


VDD_SD | VDD_SD 4,18

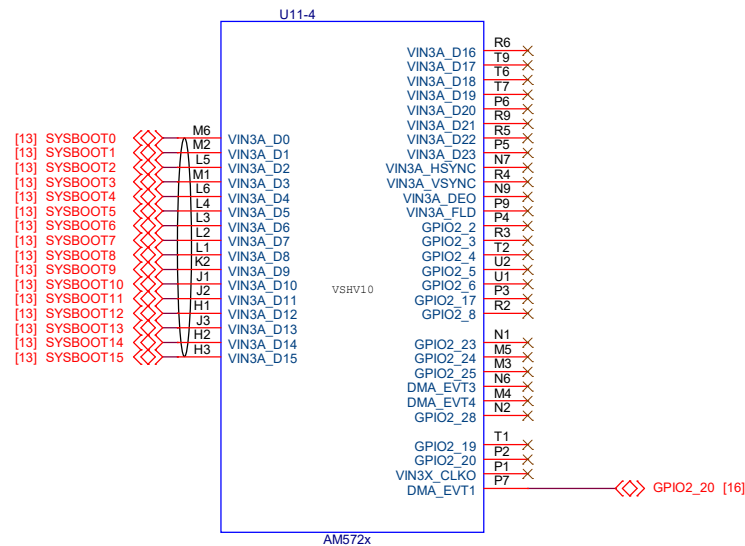
Catalog Processors	Texas Instruments, Inc. Catalog Processors Business Unit 12500 TI Blvd Dallas, TX 75243				
	AM572x General Purpose EVM Processor Module AM572X I/O POWER & GROUND				
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	B	am572x_gp_ewm_REV_A3a.dsn	T1 Applications		A3a
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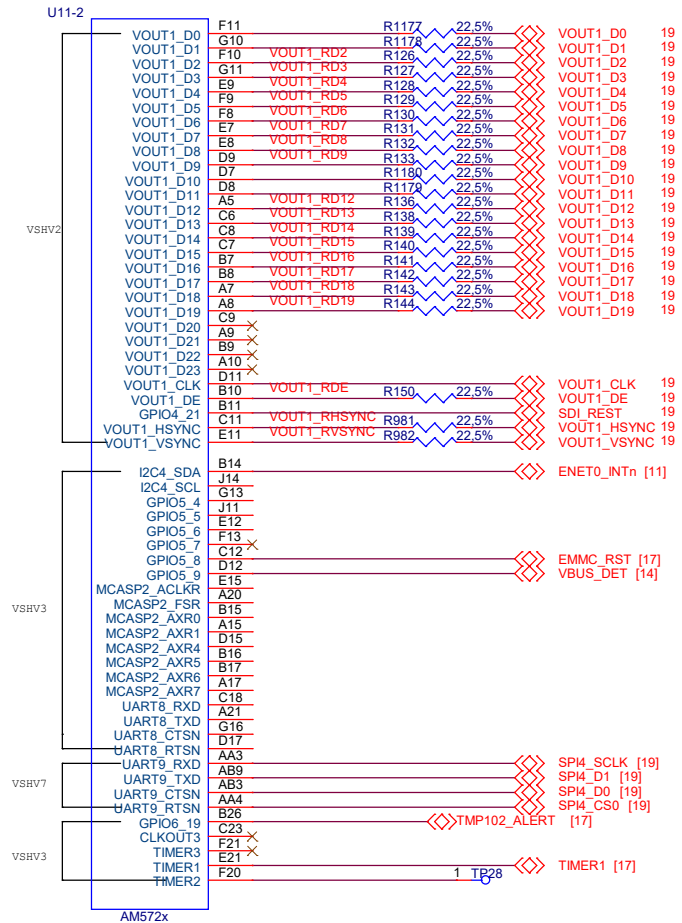
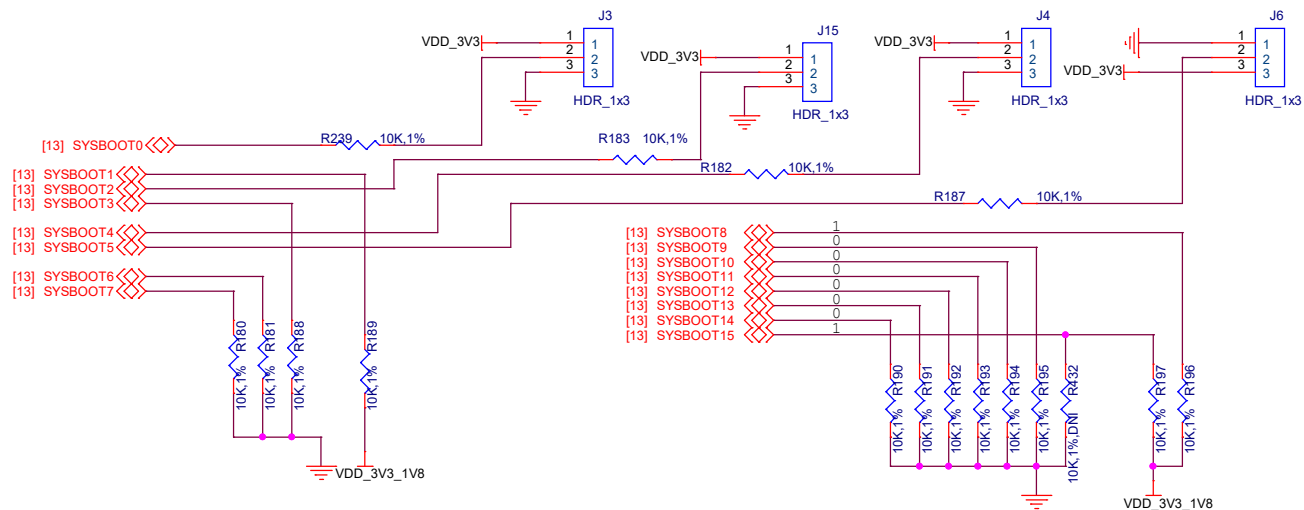
Catalog Processors	Texas Instruments, Inc. Catalog Processors Business Unit 12500 TI Blvd Dallas, TX 75243				
	AM5728 Processor Module AM572x HDMI PORT				
	Size	Document Number	Modified By:		Rev
	B	am572x_1v0.dsn	T1 Applications		1V0
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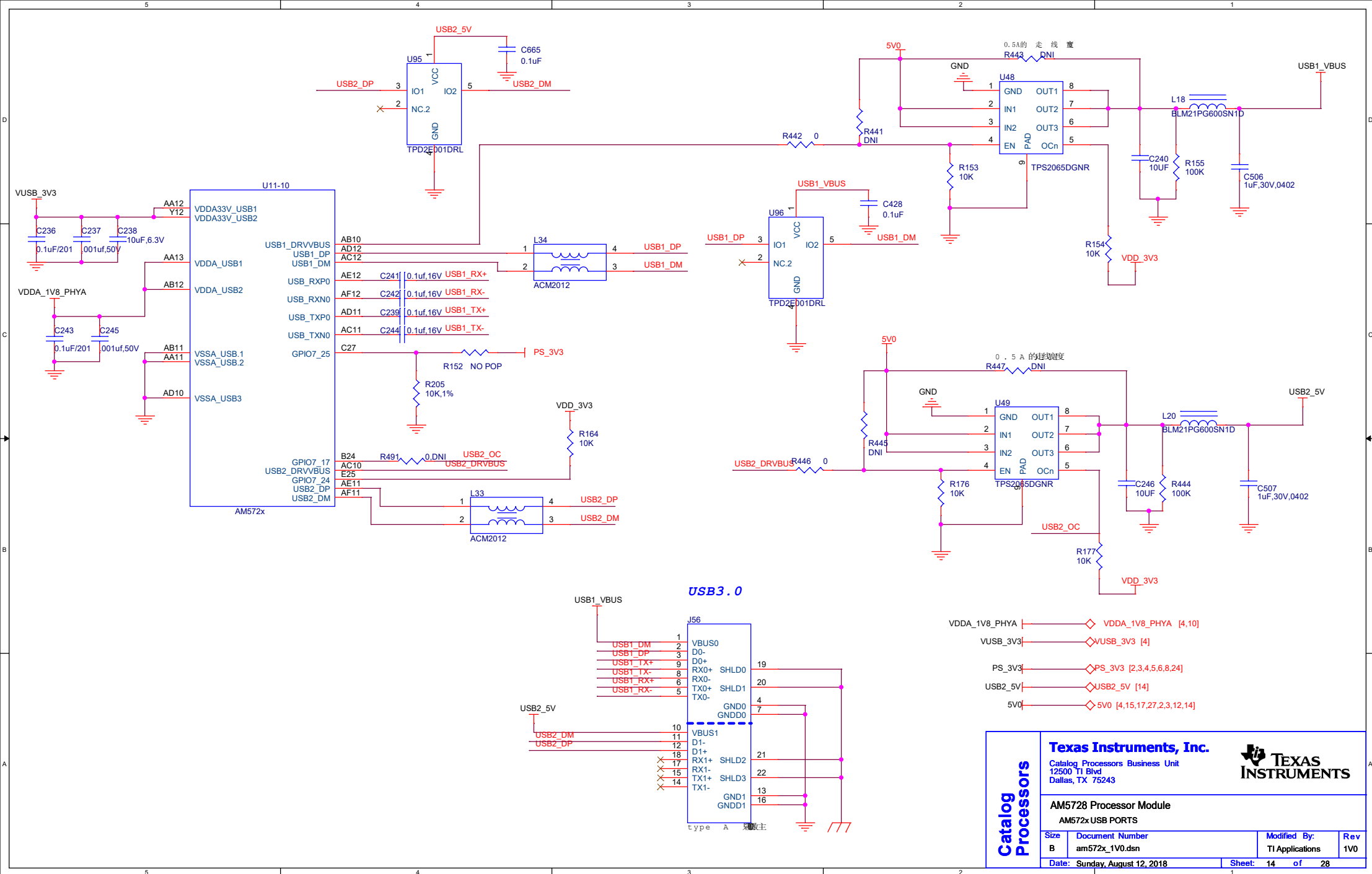
Table 33-8. Booting Devices Order

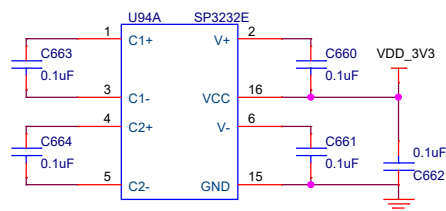
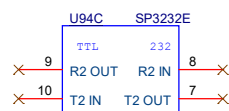
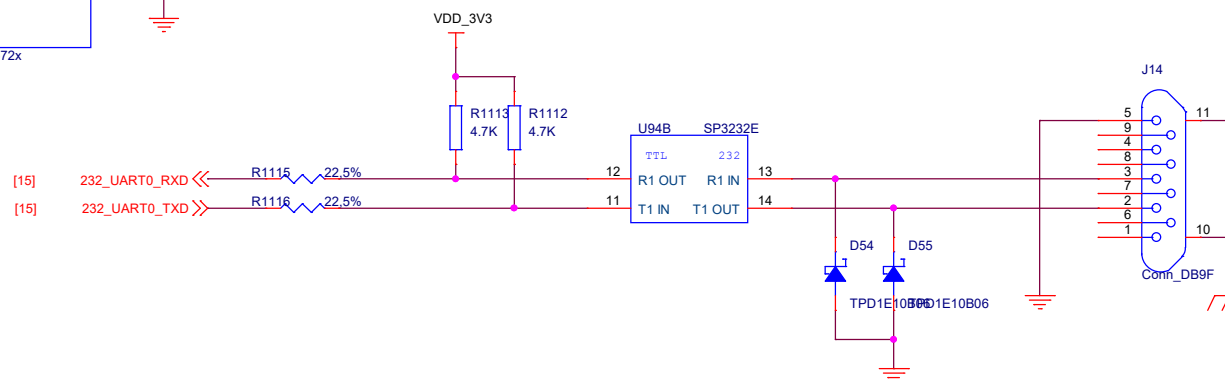
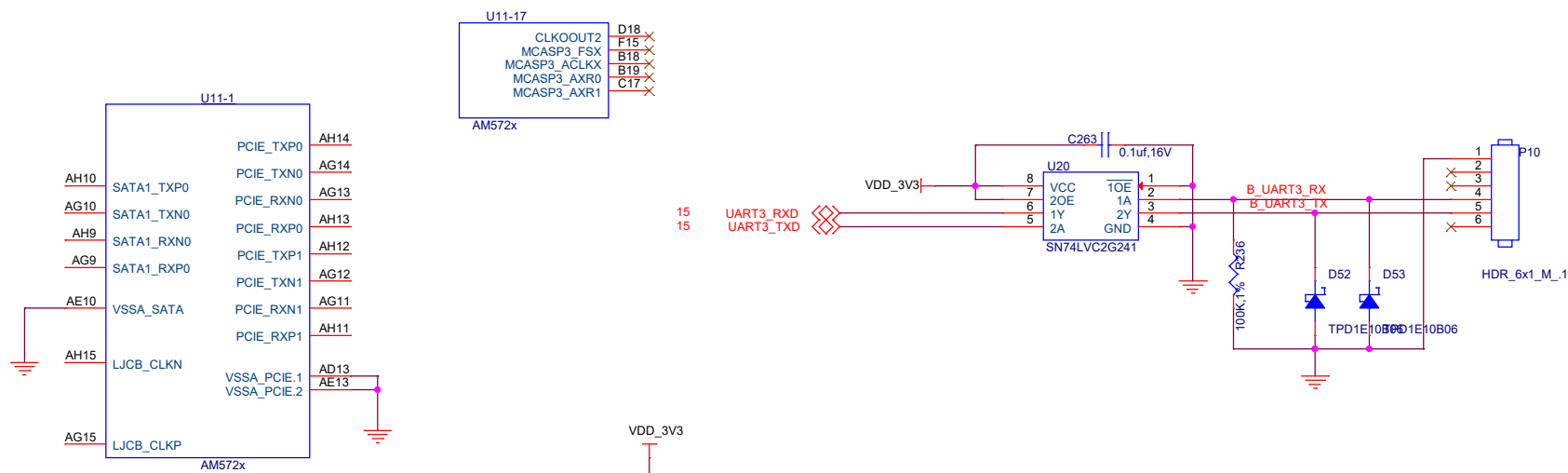
Peripheral Preferred Booting				
sysboot[5-4]	sysboot[3-0]	First Device	Second Device	Third Device
0b00	0b0000	USB	eMMC	
0b00	0b0001	USB	NAND	
0b00	0b0010	USB	SD	eMMC
0b00	0b0011	USB	SATA	SD
0b00	0b0100	USB	UART	XIP
0b00	0b0101	SD	XIP	
0b00	0b0110	SD	QSPI_1	
0b00	0b0111	SD	QSPI_4	
0b00	0b1010	SD	Fast XIP	
Recovery/Upgrade or Development Booting				
0b01	0b0000	USB		
0b01	0b0011	UART		
0b01	0b0100	SD	USB	
0b01	0b0101	SD	USB	
0b01	0b0110	SD	USB	
0b01	0b0111	SD	USB	
0b01	0b1000	SD	USB	
0b01	0b1001	SD	USB	
0b01	0b1010	SD	USB	
0b01	0b1011	SD	USB	
Memory Preferred Booting				
0b10	0b0000	eMMC	USB	
0b10	0b0001	NAND	USB	
0b10	0b0010	SD	eMMC	USB
0b10	0b0011	SATA	SD	USB
0b10	0b0100	XIP	USB	UART
0b10	0b0101	XIP	SD	USB
0b10	0b0110	QSPI_1	SD	USB
0b10	0b0111	QSPI_4	SD	USB
Production Booting ⁽¹⁾				
0b11	0b0000	SD		
0b11	0b0100	SATA		
0b11	0b0101	XIP		
0b11	0b0110	QSPI_1		
0b11	0b0111	QSPI_4		
0b11	0b1000	eMMC		
0b11	0b1001	NAND		
0b11	0b1010	Fast XIP		
0b11	0b1011	eMMC (boot part.) ⁽²⁾		

(1) After 10 failed loops through the device list, ROM code initiates immediate global warm reset.

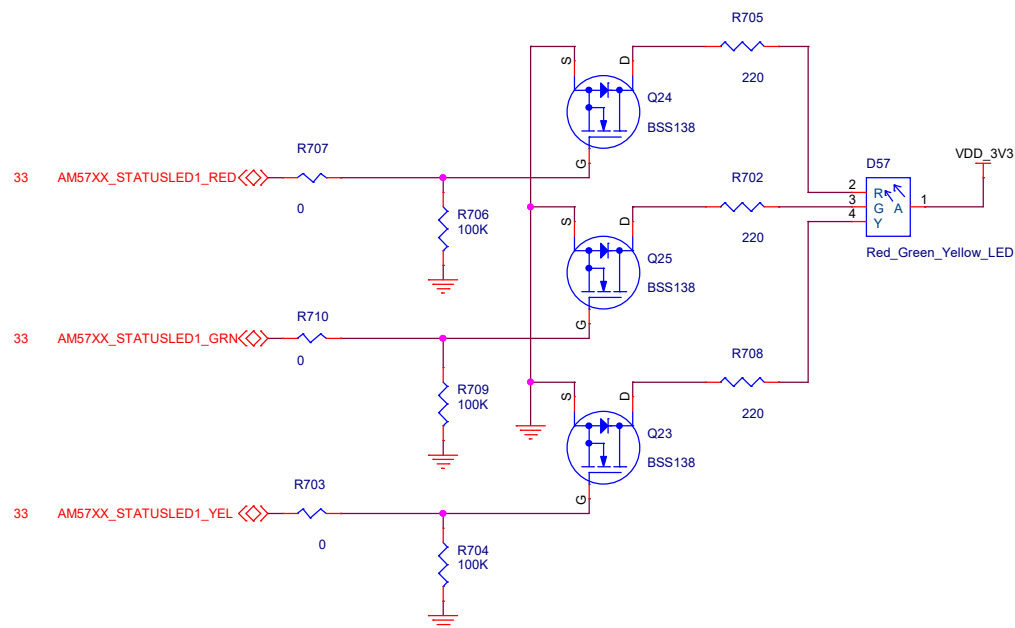
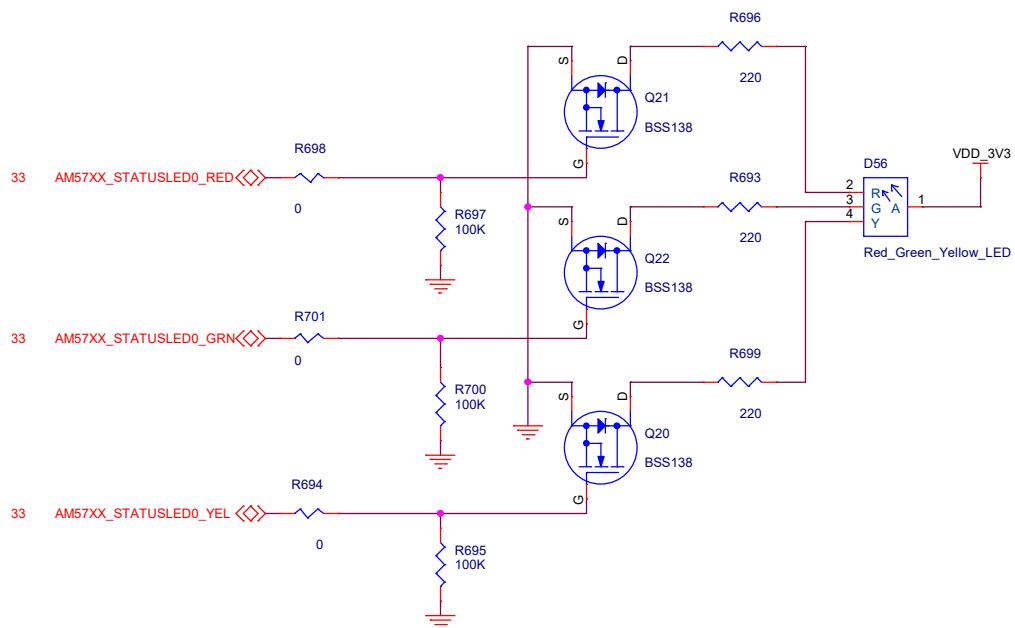
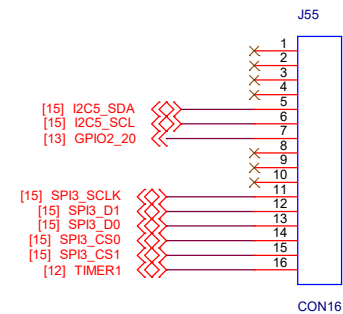
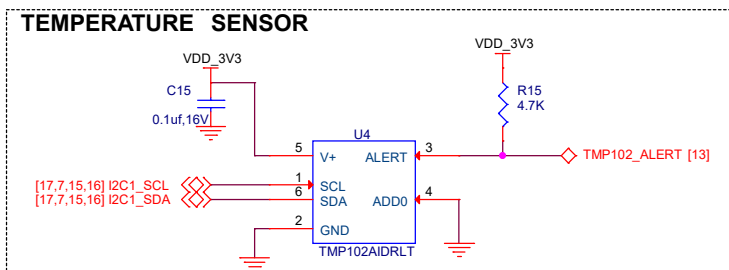
(2) The ROM code boots from eMMC boot partition. If the ROM code fails to retrieve the booting image, it does not try boot from the user area contrary to other eMMC options.



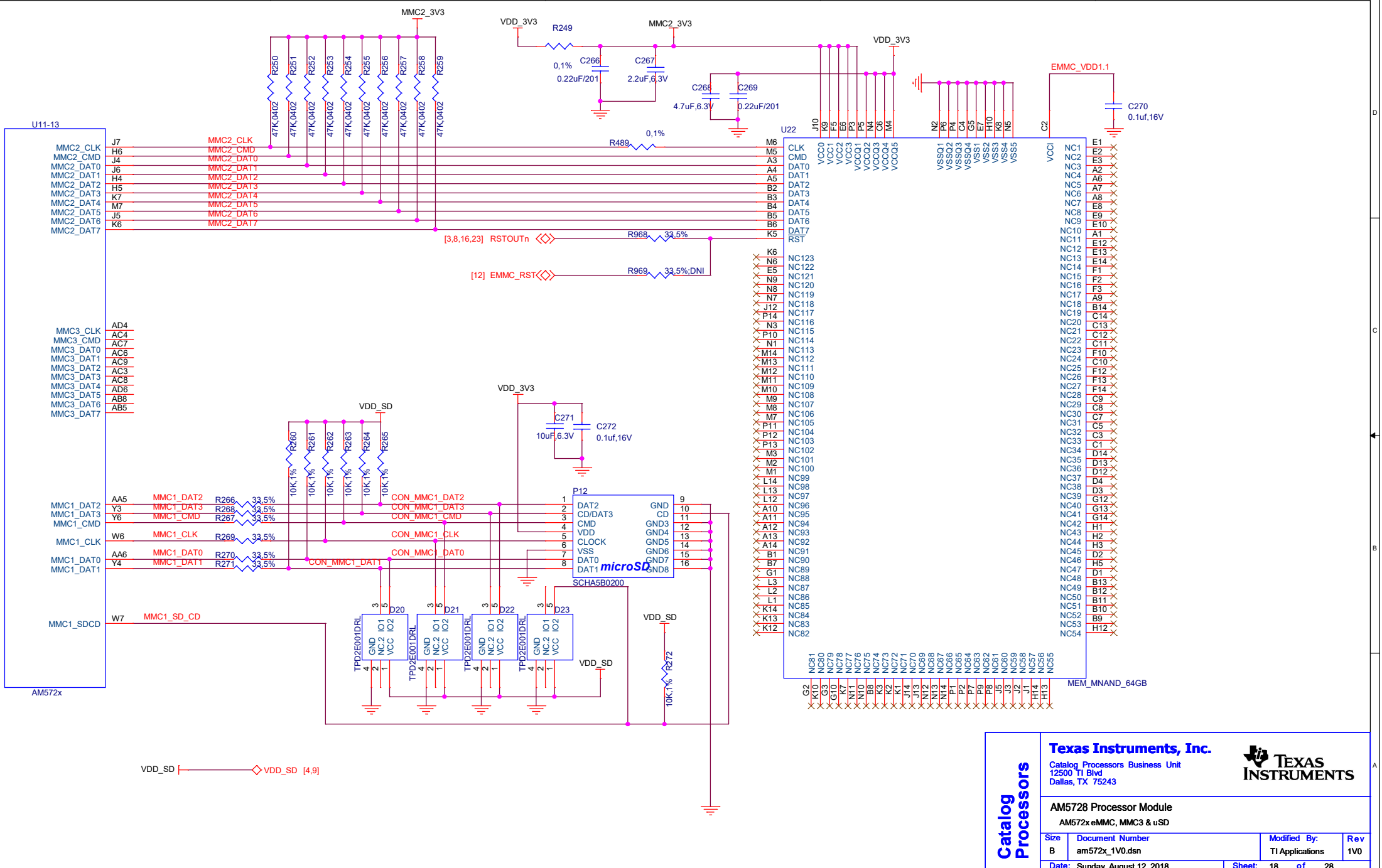




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	AM5728 Processor Module AM572x, AUDIO, & DEBUG			
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	AM5728 Processor Module USER INTERFACE				
	Size	Document Number	Modified By:		Rev
	B	am572x_1v0.dsn	TI Applications		1V0
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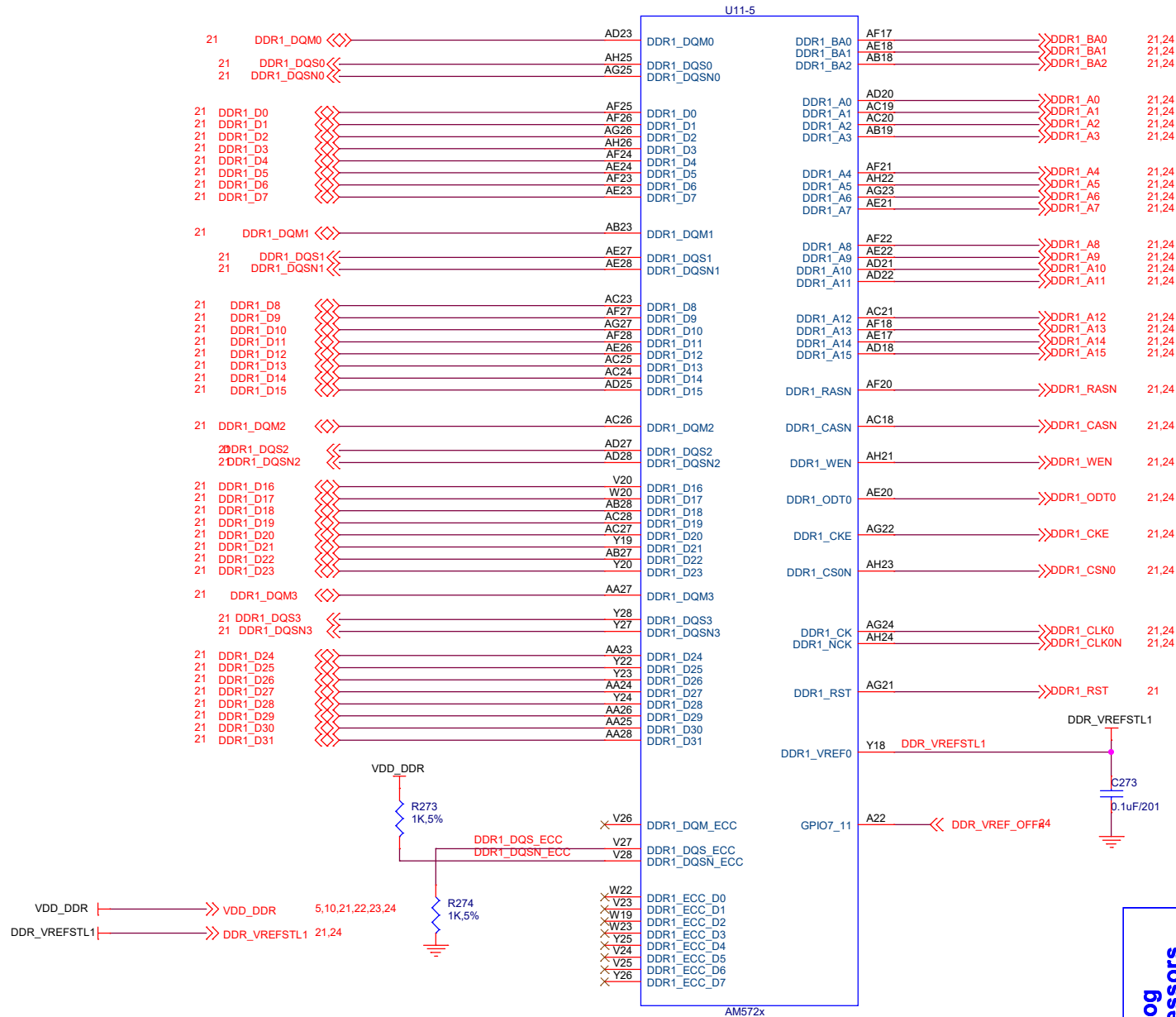


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AM5728 Processor Module
AM572xeMMC, MMC3 & uSD

Size	Document Number	Modified By:	Rev
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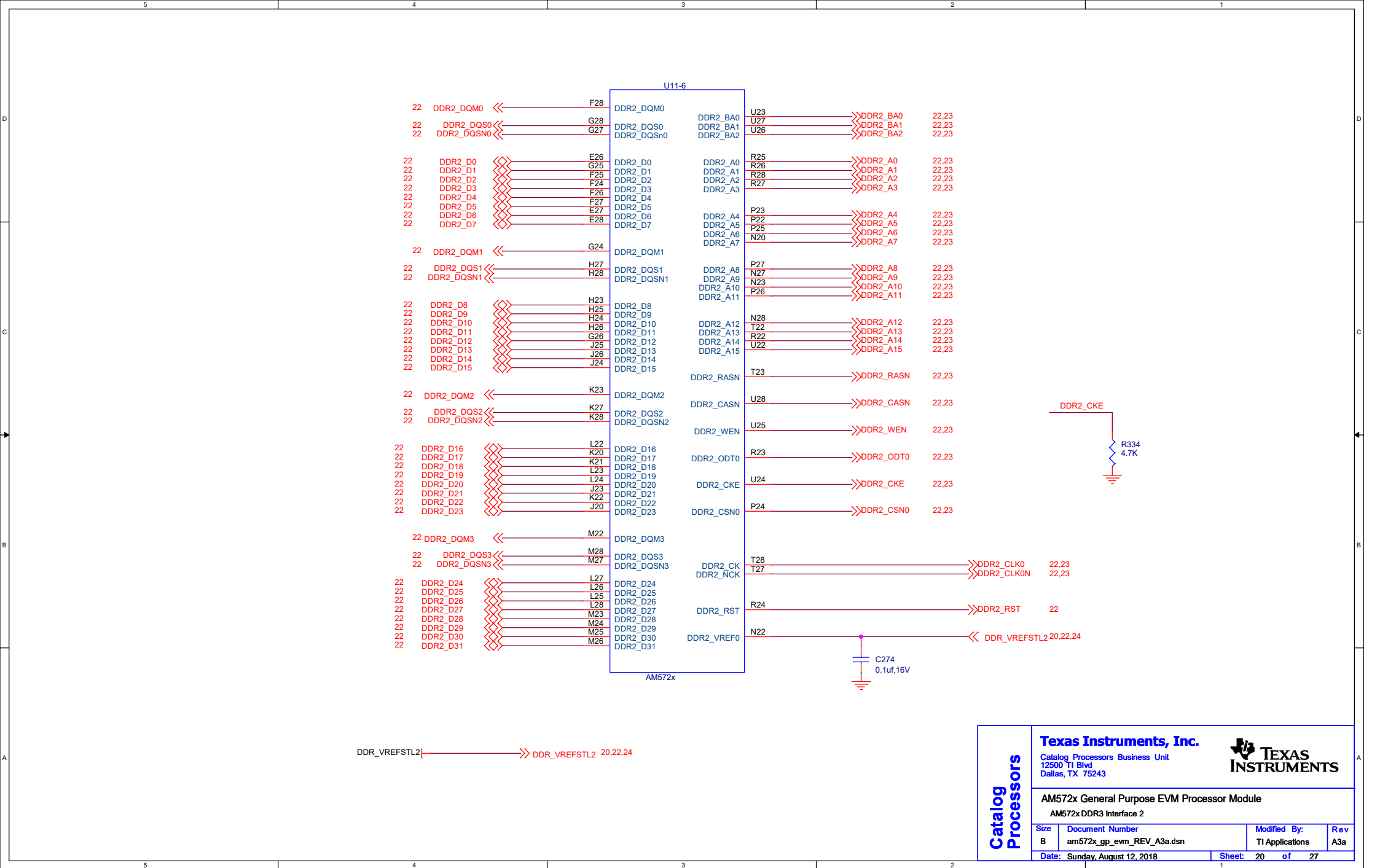


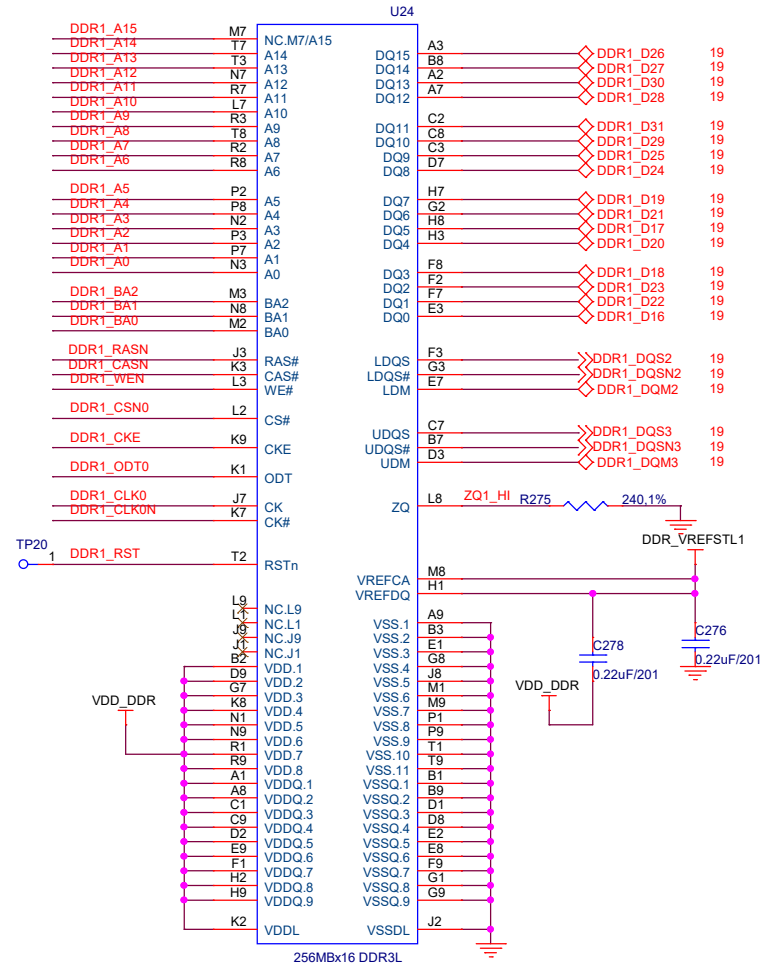
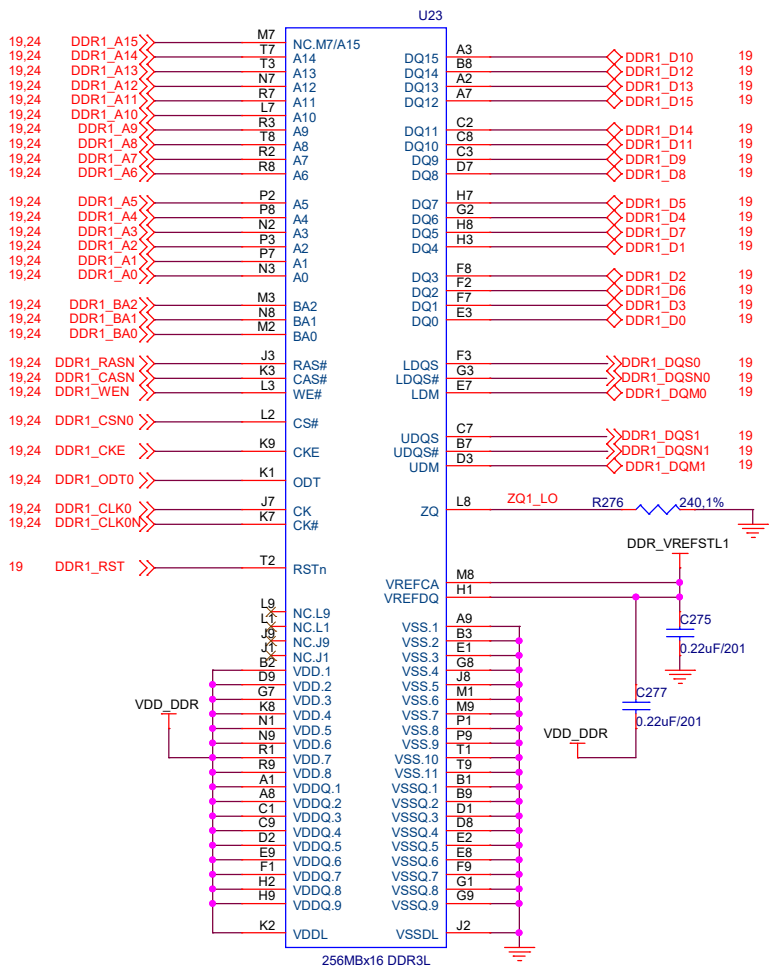
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AM572x General Purpose EVM Processor Module
AM572xDDR3 Interface 1

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DDR_VREFSTL1 >> DDR_VREFSTL1 19,24
VDD_DDR <> VDD_DDR 5,10,19,22,23,24

Catalog Processors

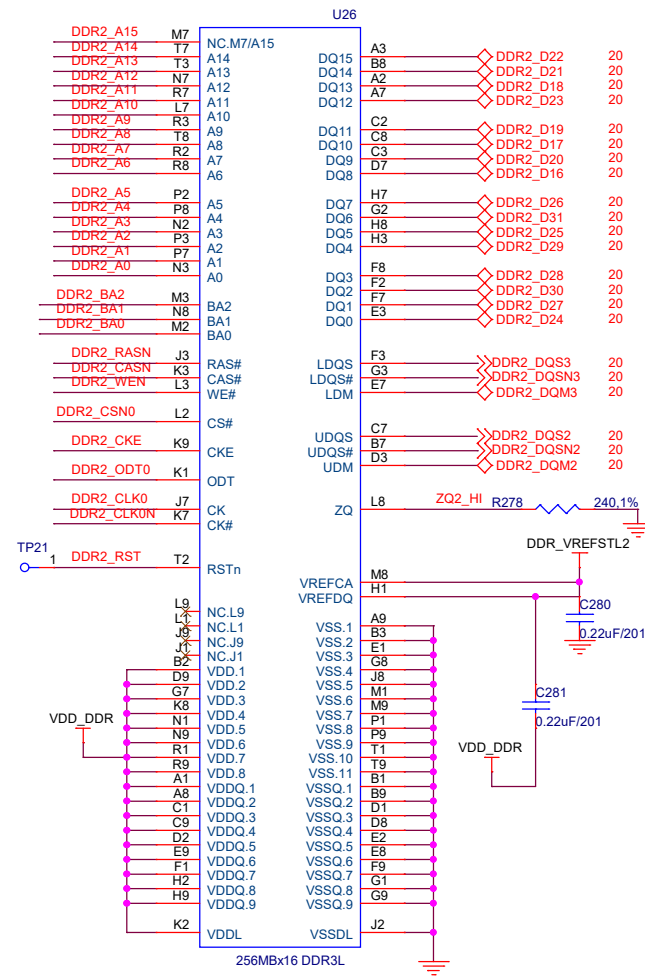
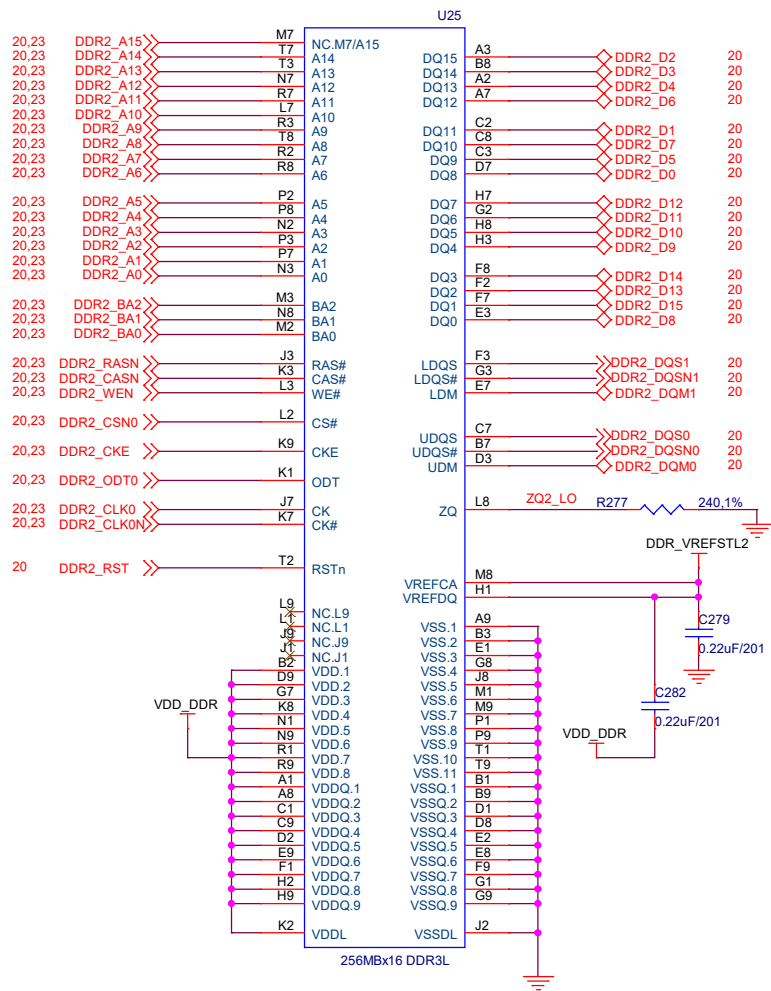
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AM572x General Purpose EVM Processor Module
DDR3 Bank 1

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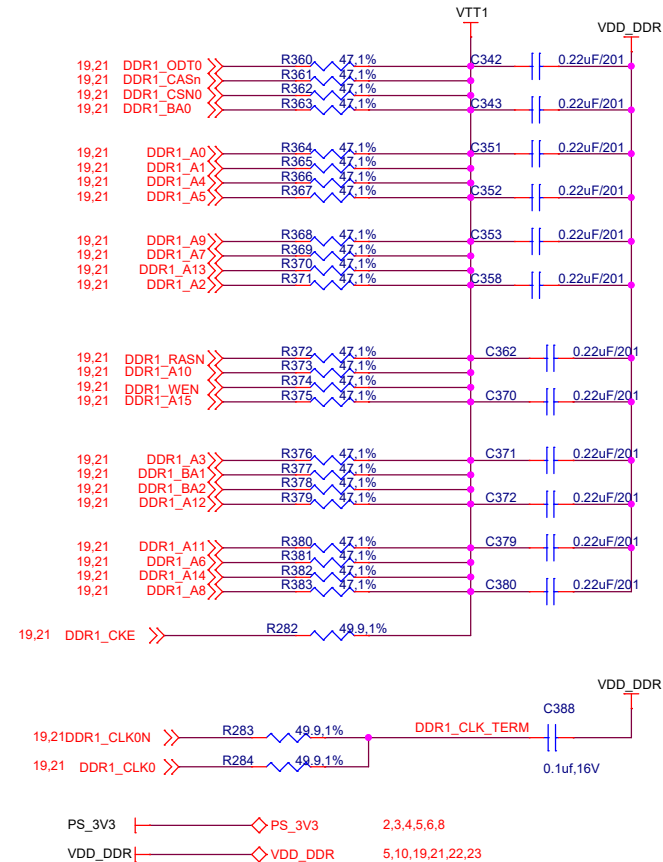
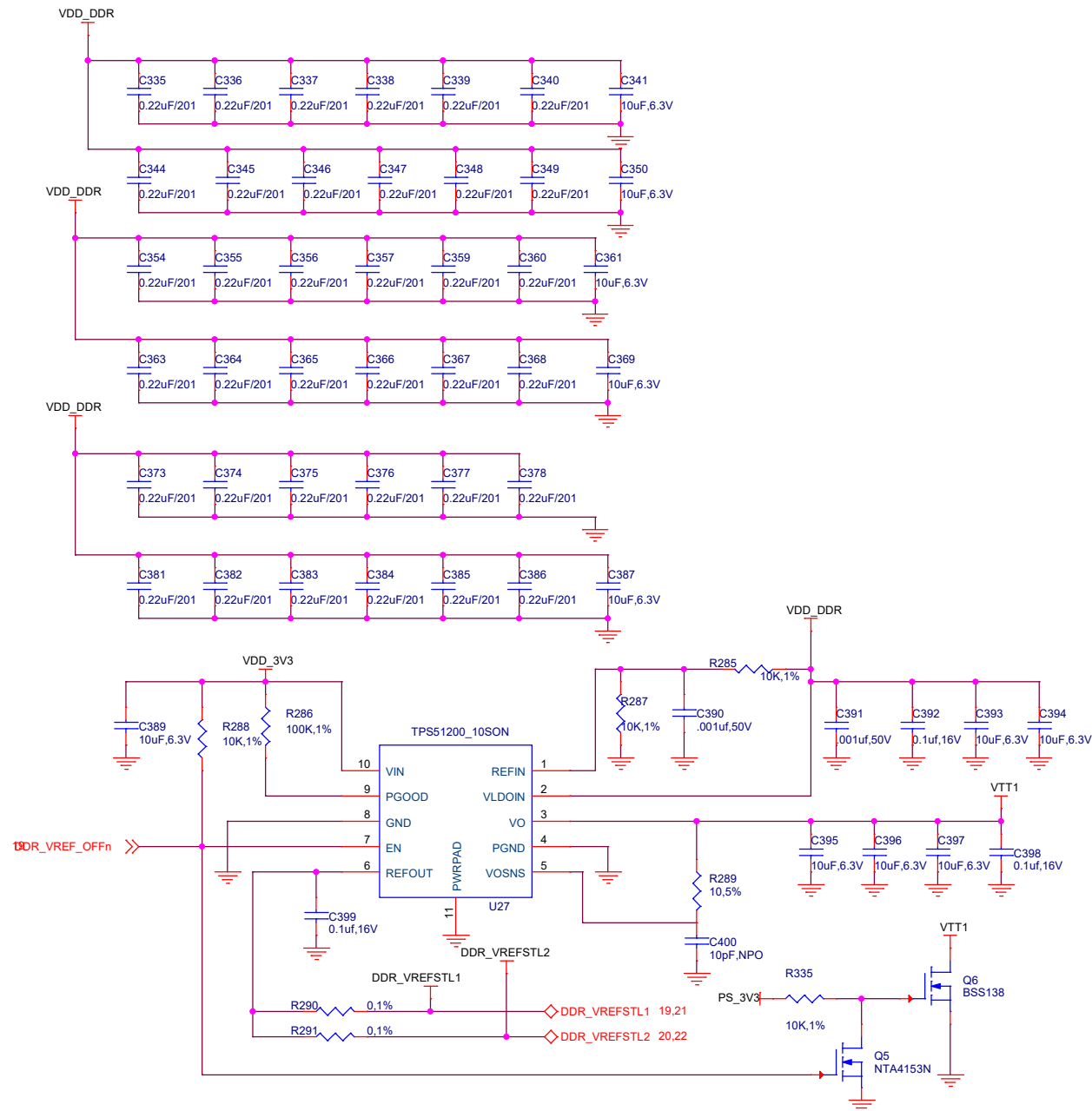
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VDD_DDR <--> VDD_DDR 5,10,19,21,23,24

DDR_VREFSTL2 <--> DDR_VREFSTL2 20,24

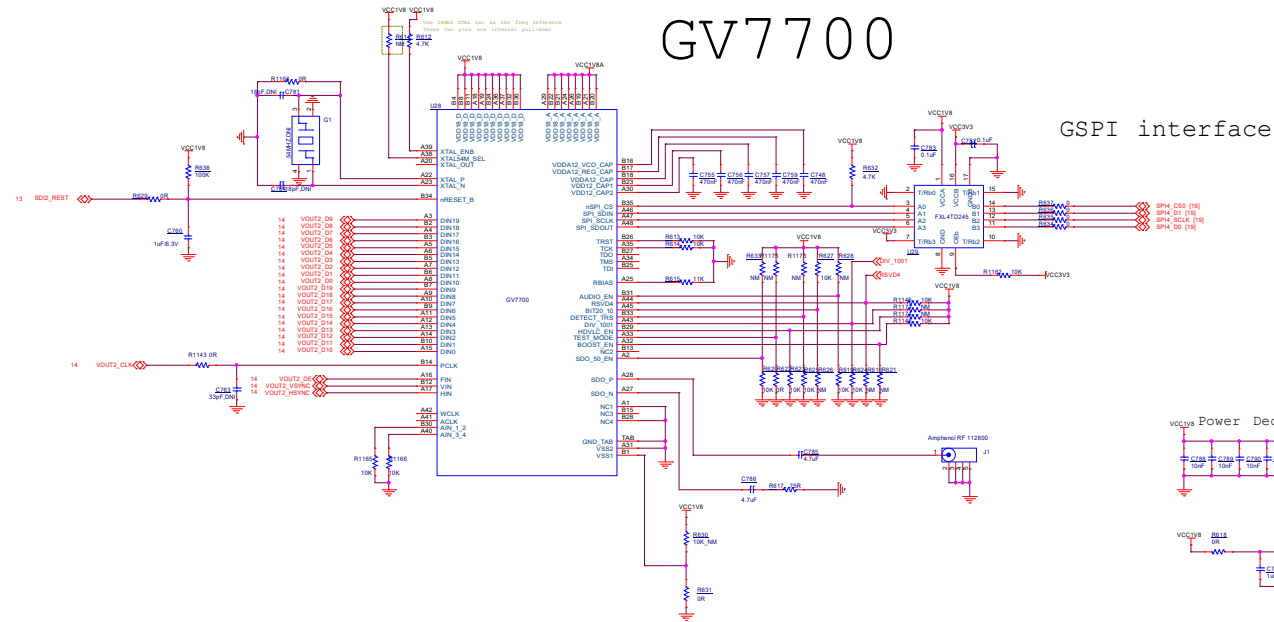
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	Size	Document Number	Modified By:		Rev
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PS_3V3 | ◇ PS_3V3 2,3,4,5,6,8
VDD_DDR | ◇ VDD_DDR 5,10,19,21,22,23

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	AM5728 Processor Module DDR3 BANK 1 CAPS & TERM				
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GV7700



GV7700

