

Signals

CC3120BP	AMIC110 ICE	
P1_1.5 (nHIB)	J4_Pin11 (GPIO2_27)	nHIB
P1_1.7 (CLK)	J4_Pin13 (SPI1_CLK)	SPI_CLK
P2_2.3 (CS#)	J4_Pin15 (SPI1_CS0)	SPI_CS
P2_2.2 (IRQ)	J5_Pin6 (GPIO1_1)	IRQ
P2_2.6 (MOSI)	J5_Pin12 (SPI1_D0)	SPI_MOSI
P2_2.7 (MISO)	J5_Pin14 (SPI1_D1)	SPI_MISO

Note: To enable J4 and J5 on AMIC110 ICE. GPIO2_21(M16) needs to set to low

Power

CC3120BP	AMIC110 ICE	
P1_1.1 (3.3V)	J4_Pin1	3.3V
P3_3.2 (GND)	J4_Pin4	GND