[Result of the test]

(1)Slave Ratio of 0x1C did not cause errors with 8 DSPs

(2)It was confirmed that the number of errors varied with the change of Slave Ratio.

(3)When changing DDR_CLK frequency from 1333MHz to 1066MHz, the error occurs only at Board#2

NG : Test Failed
OK : Test Passed
Not Tested : due to high possibility of error

[The result of adjusting DDR_CONFIG_REG23(Slave Ratio)]

		DDR_CONFIG_REG23(bit[7:0]) SLAVE RATIO										
Test Board	DSPs on the board	0x14	0x1C	0x24	0x2C	0x30	0x34 (Default Value)	0x38	0x3C	0x44	0x4C	0x54
Board#1	DSP1	NG	OK	OK	OK	NG	NG	NG	NG	NG	NG	NG
	DSP2(OK device)	NG	OK	OK	OK	OK	OK	OK	OK	OK	OK	NG
Board#2	DSP1	NG	OK	OK	NG	NG	NG	NG	NG	NG	NG	NG
	DSP2(OK device)	NG	OK	OK	OK	OK	OK	OK	OK	OK	OK	OK
Board#3	DSP1(OK device)	NG	OK	OK	OK	OK	OK	OK	OK	OK	OK	OK
	DSP2	NG	OK	NG	NG	NG	NG	Not Tested	Not Tested	Not Tested	Not Tested	Not Tested
Board#4	DSP1	NG	OK	OK	OK	OK	NG	NG	NG	NG	NG	NG
	DSP2(OK device)	NG	OK	OK	OK	OK	OK	OK	OK	OK	OK	OK

[Number of error occurrences]

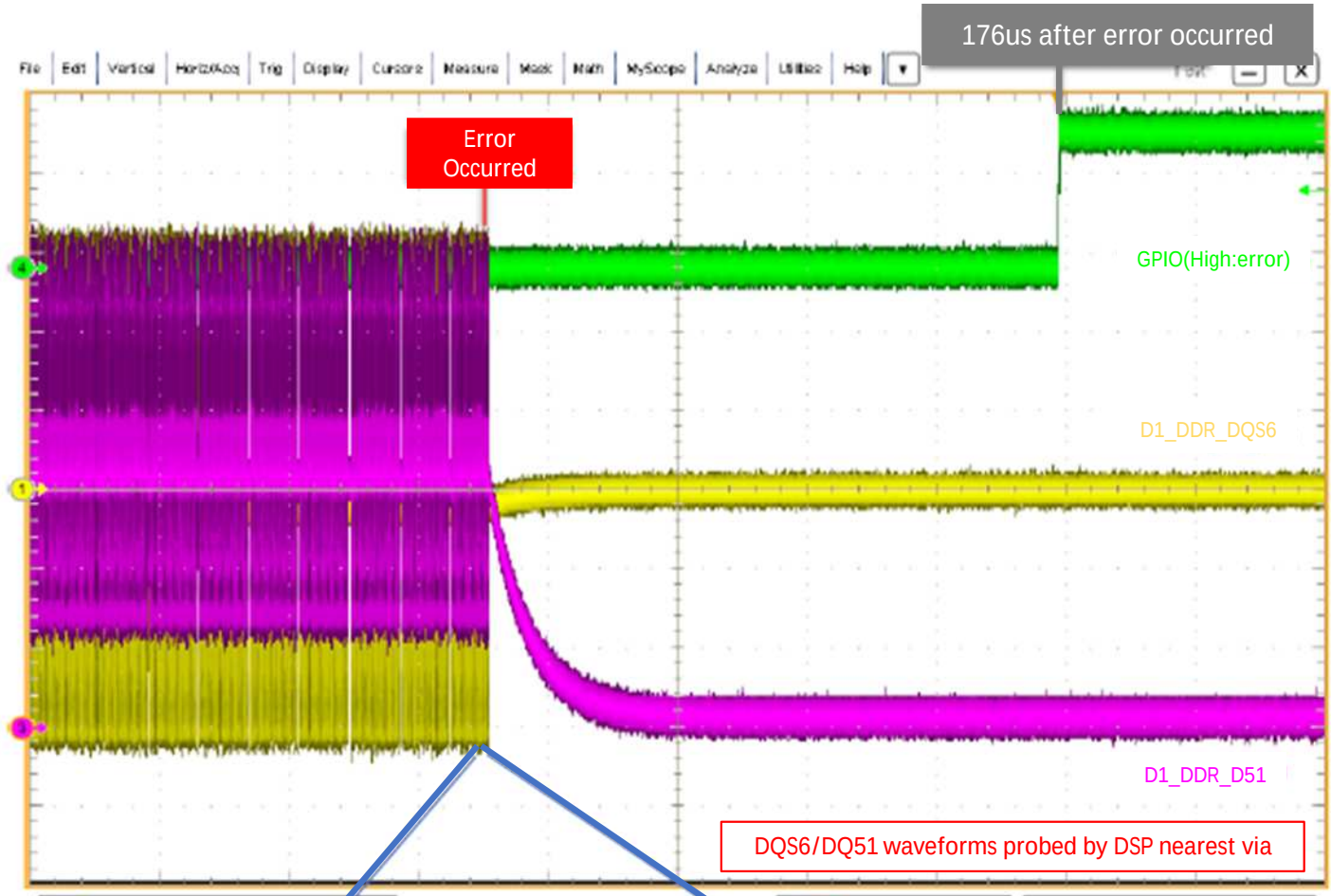
		DDR_CONFIG_REG23(bit[7:0]) SLAVE RATIO										
Test Board	DSPs on the board	0x14	0x1C	0x24	0x2C	0x30	0x34 (Default Value)	0x38	0x3C	0x44	0x4C	0x54
Board#1	DSP1	3	0	0	0	6	24	312	869	6201	21890	37873
	DSP2(OK device)	850250	0	0	0	0	0	0	0	0	0	2
Board#2	DSP1	415980818	0	0	2	47	789	53930	387126	18190312	204622326	648076538
	DSP2(OK device)	956411944	0	0	0	0	0	0	0	0	0	0
Board#3	DSP1(OK device)	9966	0	0	0	0	0	0	0	0	0	0
	DSP2	1720055311	0	6	11536	183505	181606	Not Tested	Not Tested	Not Tested	Not Tested	Not Tested
Board#4	DSP1	666554428	0	0	0	0	6	3871	89395	9876984	742706578	778917805
	DSP2(OK device)	223808818	0	0	0	0	0	0	0	0	0	0
Number of test comparison		27355195392										

[The result of changing DDR_CLK frequency]

Slave Ratio : 0x34(default value) is used		DDR_CLK 1333MHz	DDR_CLK 1066MHz
Board#1	DSP1	NG	OK
	DSP2(OK device)	OK	OK
Board#2	DSP1(OK device)	NG	NG
	DSP2	OK	OK
Board#4	DSP1	NG	OK
	DSP2(OK device)	OK	OK

Not tested at Board#3

[DQS/data waveform at the time the error occurred]



Waveform magnified at error occurrence

Address: 0x8100_E6D8
Result value : 0xFFF7_FFFF_FFFF_FFFF
Expected value : 0xFFFF_FFFF_FFFF_FFFF
At this time, the burst data in Bit51 is 0,0,0,0,1,0,0,0,0,0, and the data is output from the memory as expected.