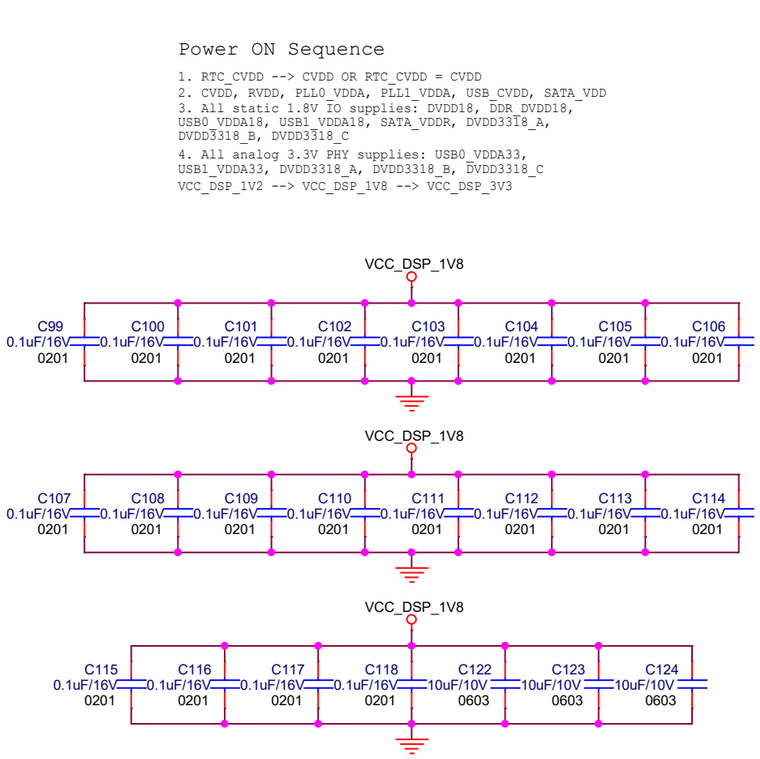
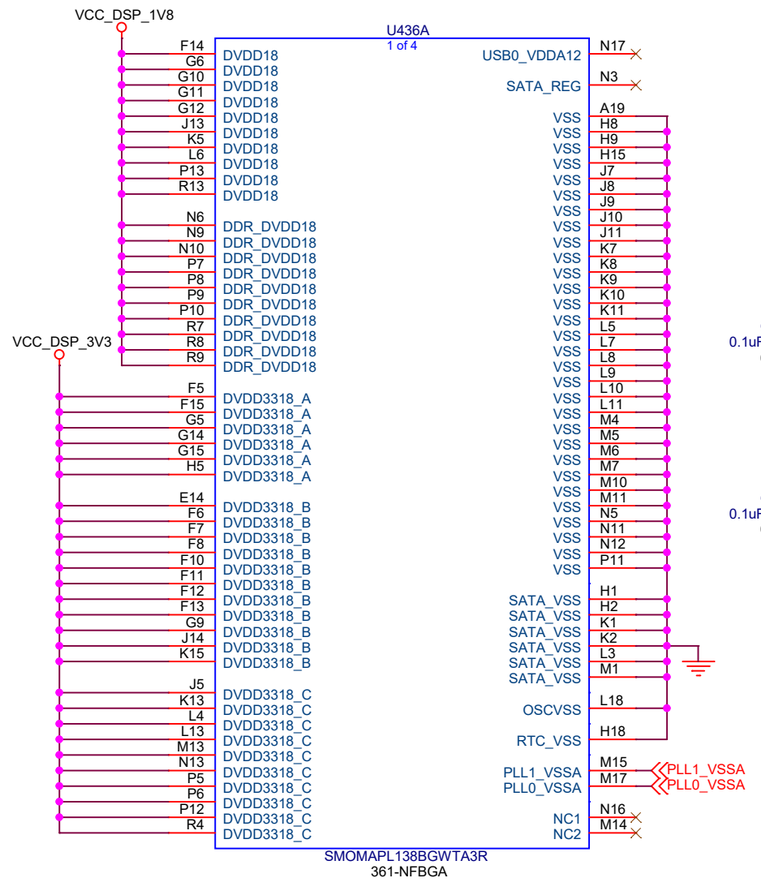
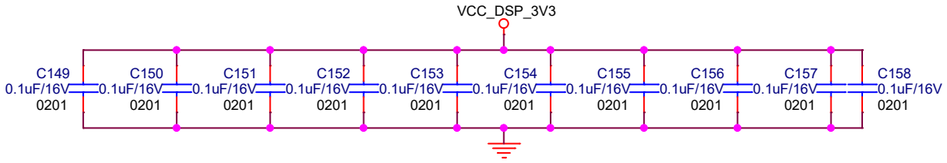
Power ON Sequence

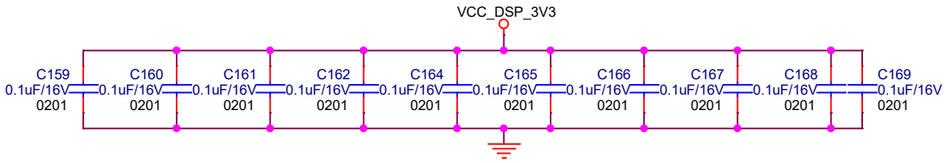
1. RTC_CVDD --> CVDD OR RTC_CVDD = CVDD
2. CVDD, RVDD, PLL0_VDDA, PLL1_VDDA, USB_CVDD, SATA_VDD
3. All static 1.8V IO supplies: DVDD18, DDR_DVDD18, USB0_VDDA18, USB1_VDDA18, SATA_VDDR, DVDD3318_A, DVDD3318_B, DVDD3318_C
4. All analog 3.3V PHY supplies: USB0_VDDA33, USB1_VDDA33, DVDD3318_A, DVDD3318_B, DVDD3318_C

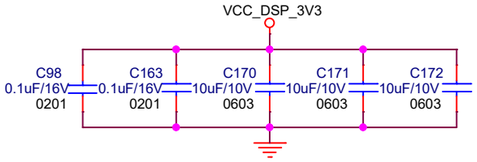
VCC_DSP_1V2 --> VCC_DSP_1V8 --> VCC_DSP_3V3

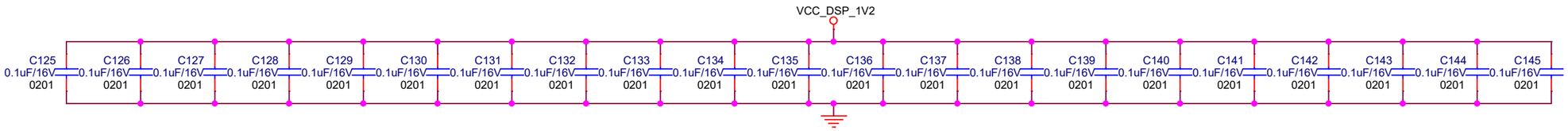


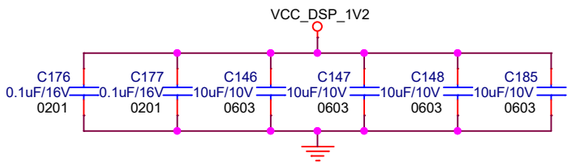












LAYOUT NOTE:
Place all 0201 package capacitors near to respectice power pin below BGA package processor.

CONFIGURATION BOARD	NAME	SIGNATURE	DATE
DRAWN:	Vighnesh		
DESIGNED:	Vighnesh		
PEER REVIEWER:	Karthik		
QA REVIEWER:			
CHECKED BY:			
APPROVED BY:			

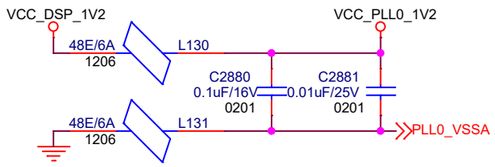
CSIR - NAL

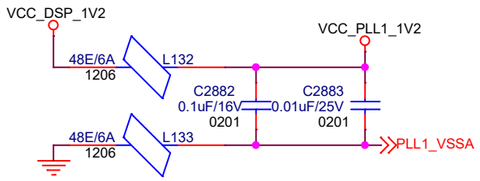
PB 1779, Old Airport Road,
Bengaluru Karnataka 560 017

DRAWING TITLE: COM BASE CARD
DSP COM POWER

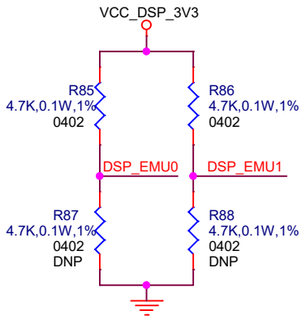
SIZE	DRAWING NUMBER	REV
A3	DR-2007	-

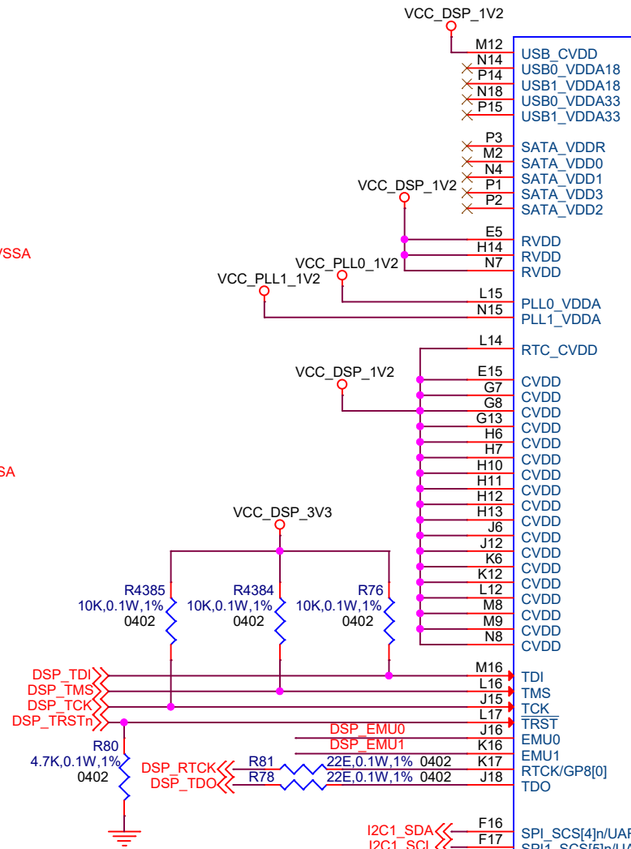
THIRD ANGLE PROJ	SCALE	SHEET No / of SHEETS
	1:1	3 / 25

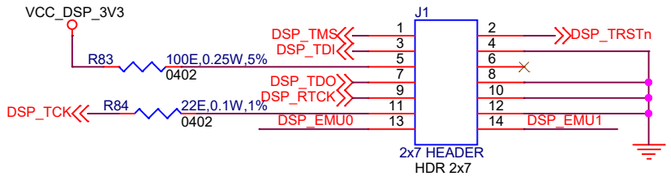


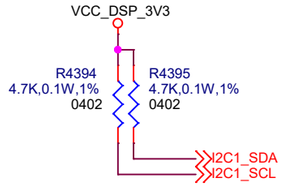


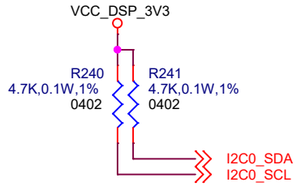
LAYOUT NOTE:
Place all 0201 package capacitors near to respectice power pin below BGA package processor.











CONFIGURATION BOARD	NAME	SIGNATURE	DATE
DRAWN:	Vighnesh		
DESIGNED:	Vighnesh		
PEER REVIEWER:	Karthik		
QA REVIEWER:			
CHECKED BY:			
APPROVED BY:			

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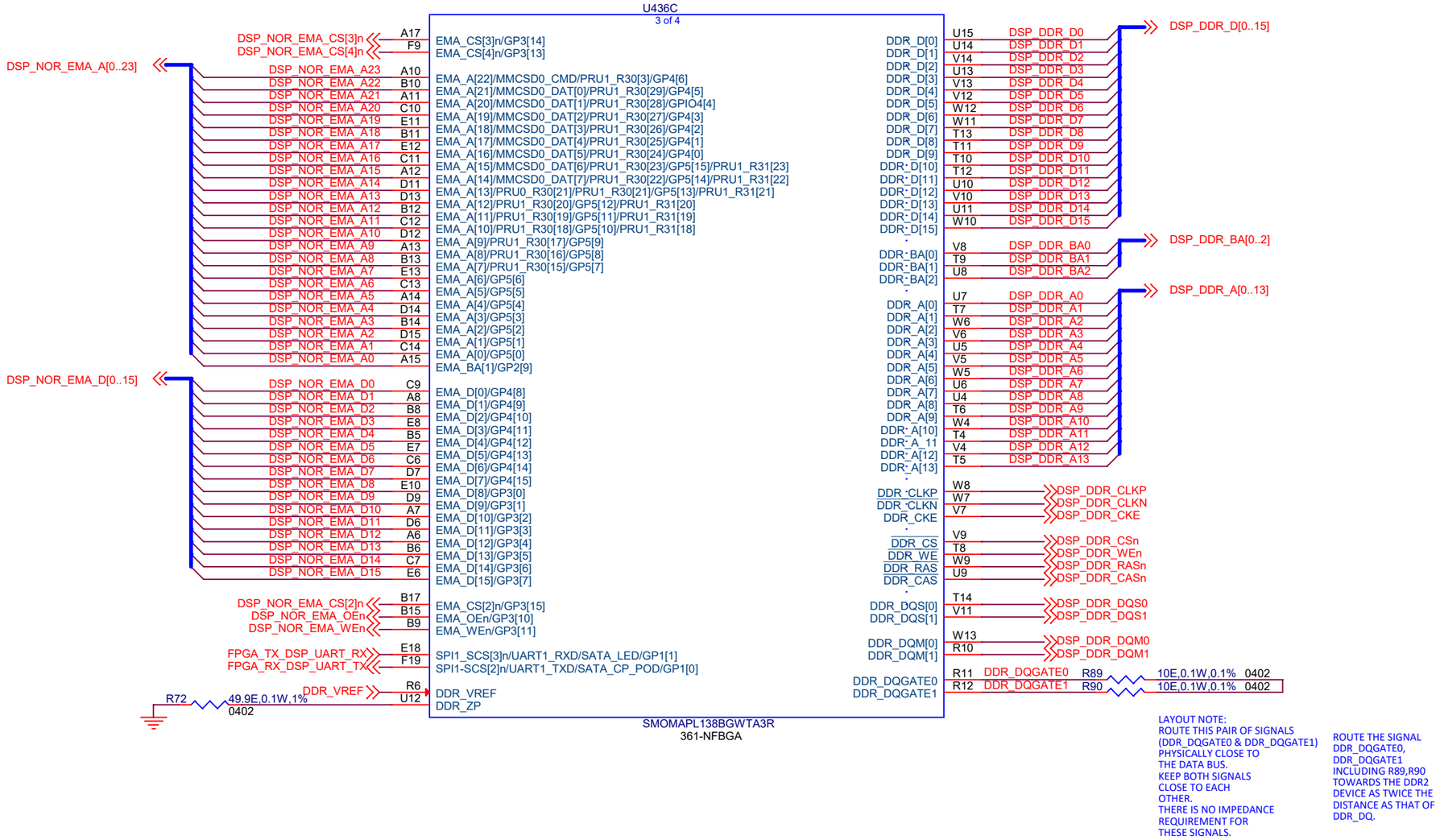
PB 1779, Old Airport Road,
Bengaluru Karnataka 560 017

DRAWING TITLE: COM BASE CARD
DSP CLOCK AND JTAG

SIZE	DRAWING NUMBER	REV
A3	DR-2007	-

THIRD ANGLE PROJ	SCALE	SHEET No	4
	1:1	of SHEETS	25



CONFIGURATION BOARD	NAME	SIGNATURE	DATE
DRAWN:	Vighnesh		
DESIGNED:	Vighnesh		
PEER REVIEWER:	Karthik		
QA REVIEWER:			
CHECKED BY:			
APPROVED BY:			

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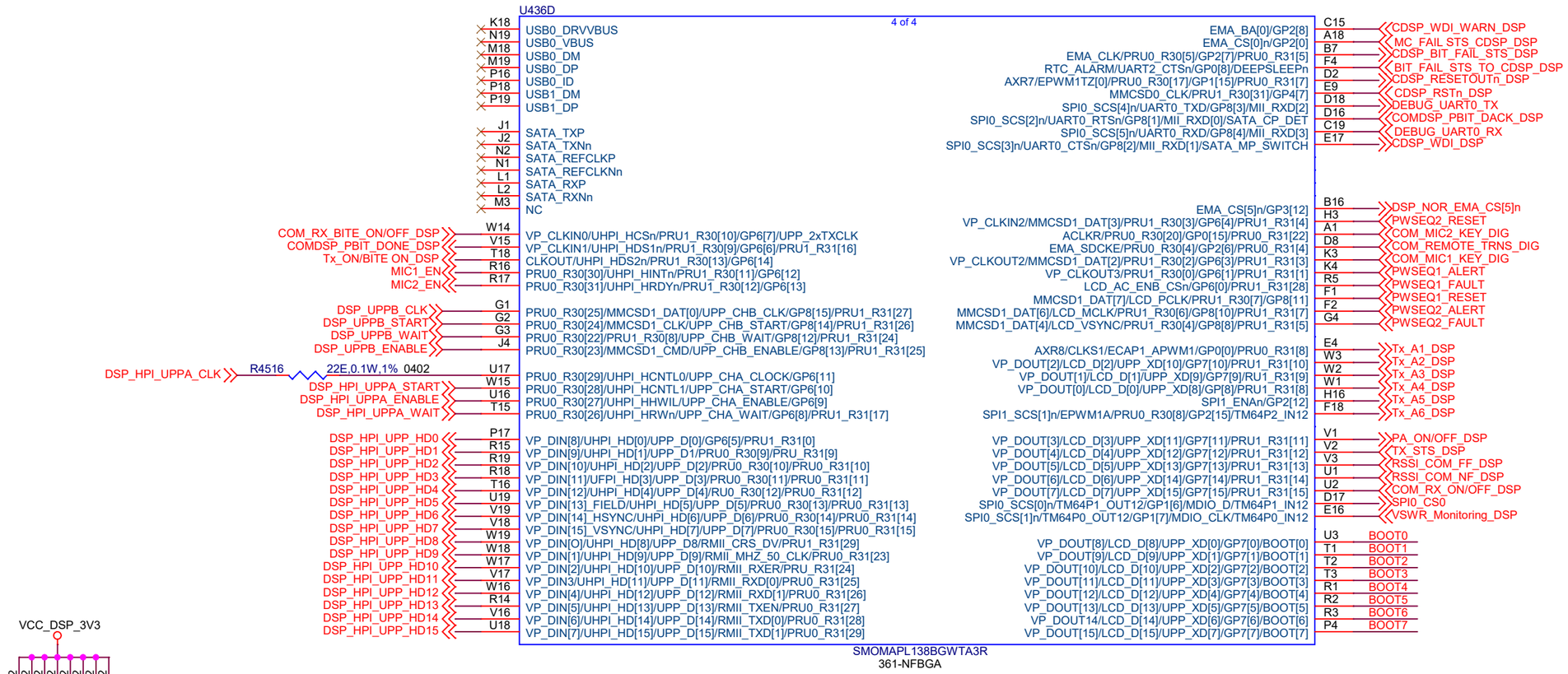
Bengaluru Karnataka 560 017

DRAWING TITLE: COM BASE CARD
DSP NOR DDR2 BLOCK

SIZE	DRAWING NUMBER	REV
A3	DR-2007	-

THIRD ANGLE PROJ	SCALE	SHEET No / of SHEETS
	1:1	5 / 25



Use below mentioned jumper for shorting connector pin to set BOOT mode
Part number : QPC02SXGN-RC

CONFIGURATION BOARD	NAME	SIGNATURE	DATE
DRAWN:	Vighnesh		
DESIGNED:	Vighnesh		
PEER REVIEWER:	Karthik		
QA REVIEWER:			
CHECKED BY:			
APPROVED BY:			

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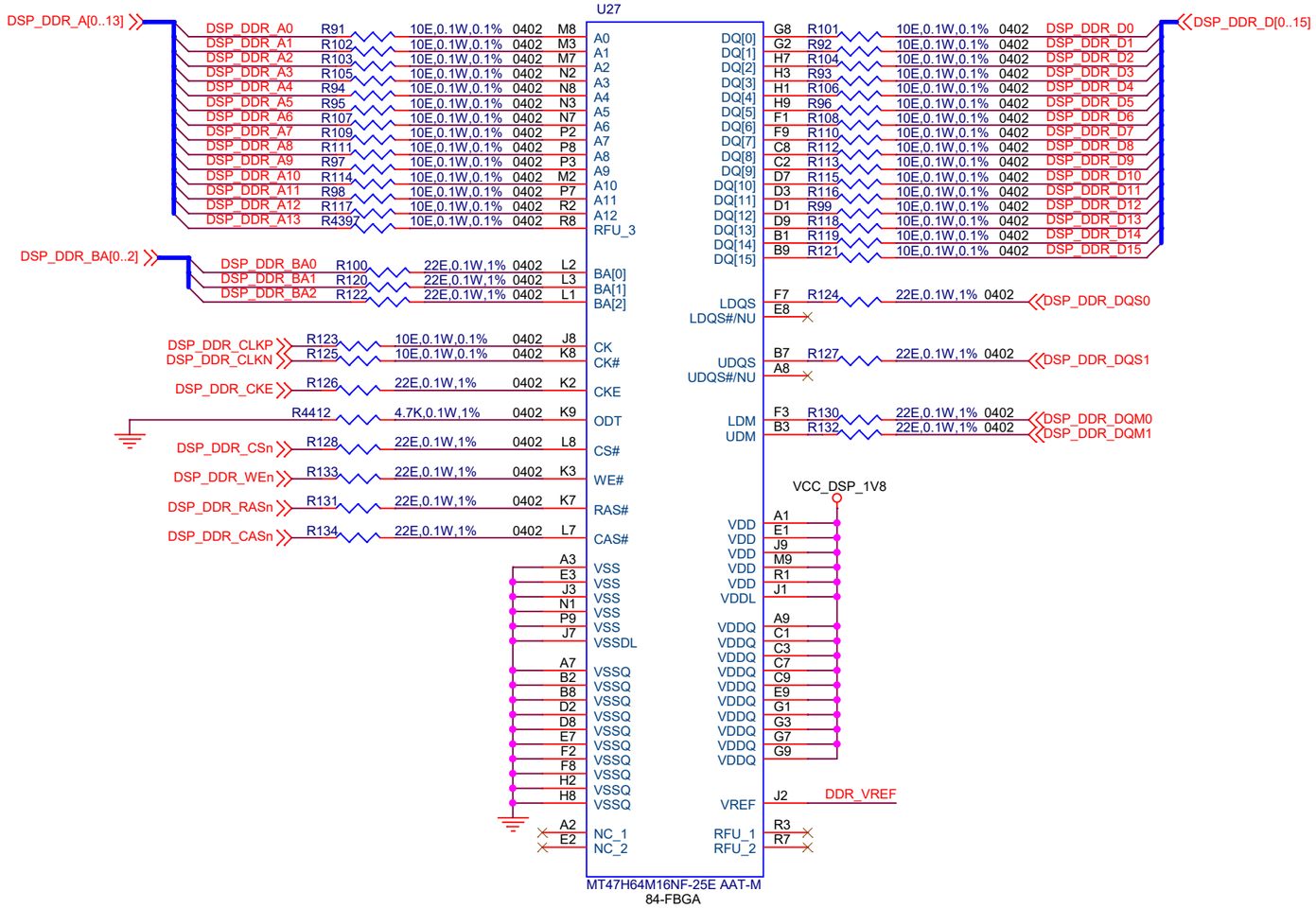
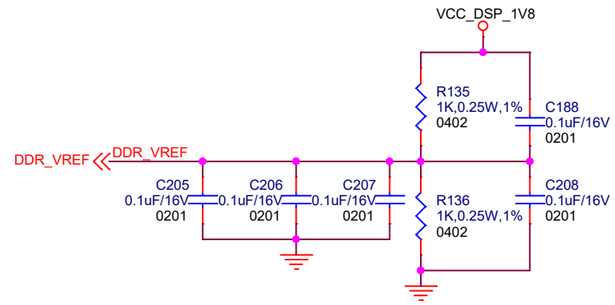
Bengaluru Karnataka 560 017

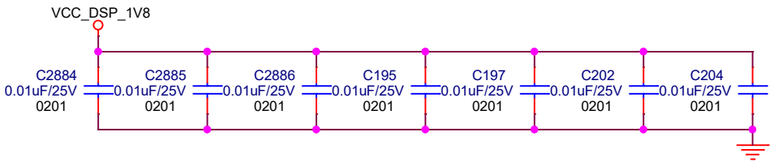
DRAWING TITLE: COM BASE CARD
DSP HPI UPP BOOT

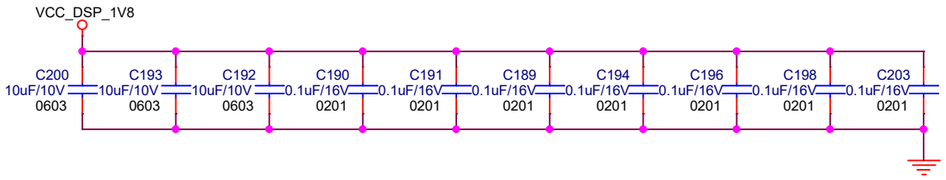
SIZE	DRAWING NUMBER	REV
A3	DR-2007	-

THIRD ANGLE PROJ	SCALE	SHEET No / of SHEETS
	1:1	6 / 25









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QA REVIEWER:			
CHECKED BY:			
APPROVED BY:			

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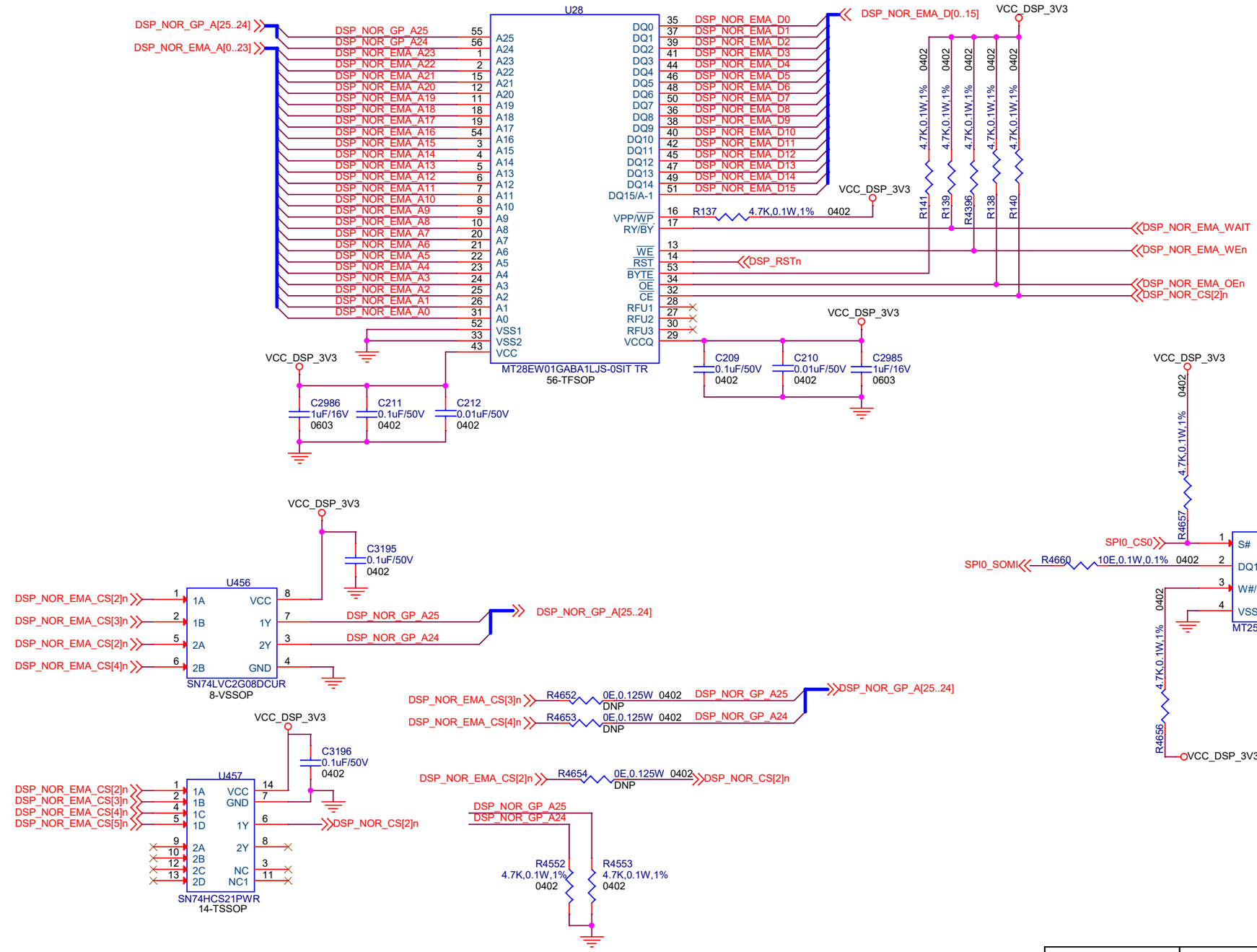
Bengaluru Karnataka 560 017

DRAWING TITLE: COM BASE CARD
DDR2 INTERFACE

SIZE	DRAWING NUMBER	REV
A3	DR-2007	-

THIRD ANGLE PROJ	SCALE	SHEET No 7
	1:1	of SHEETS 25



CONFIGURATION BOARD	NAME	SIGNATURE	DATE
DRAWN:	Vighnesh		
DESIGNED:	Vighnesh		
PEER REVIEWER:	Karthik		
QA REVIEWER:			
CHECKED BY:			
APPROVED BY:			

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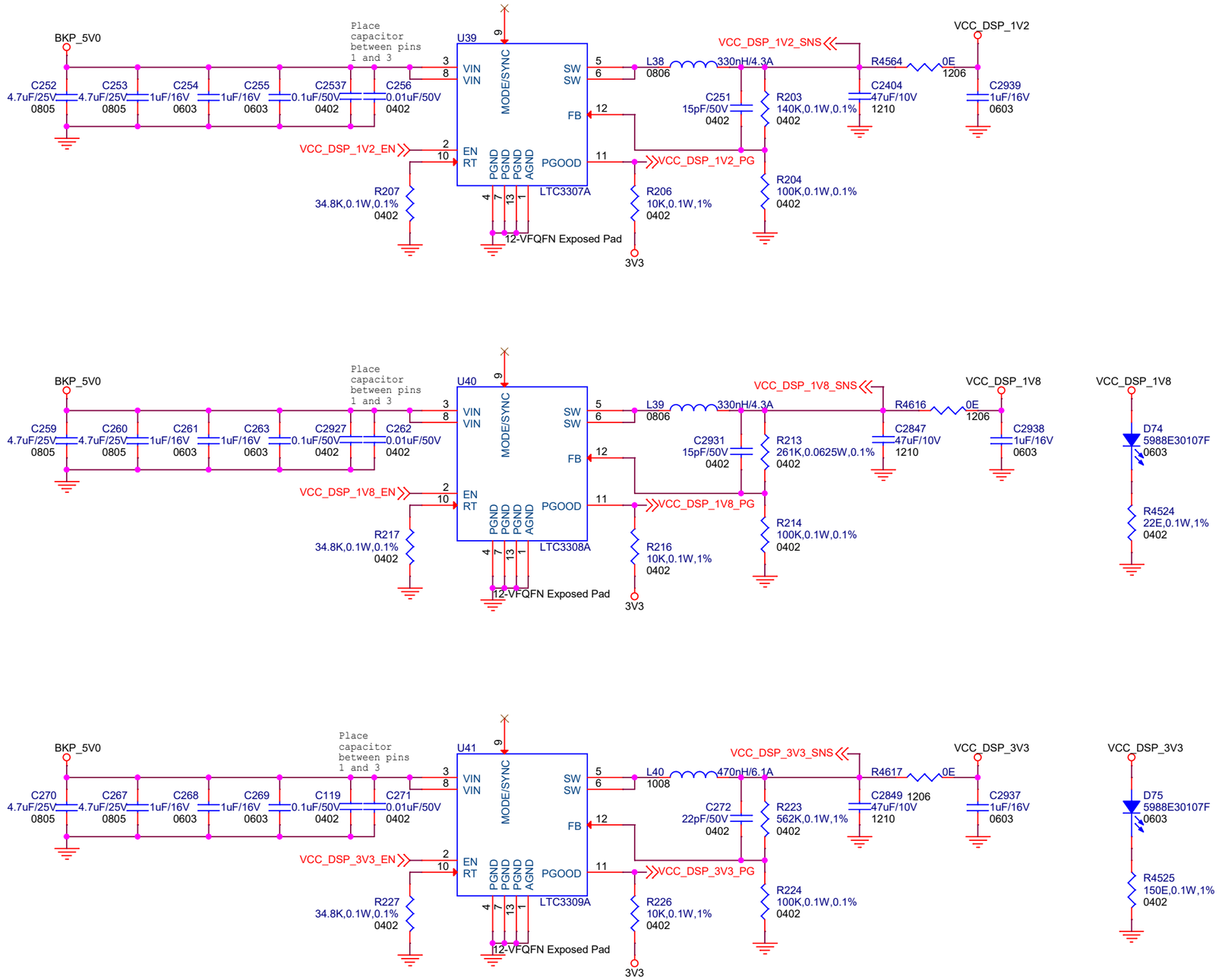
Bengaluru Karnataka 560 017

DRAWING TITLE: COM BASE CARD
NOR FLASH INTERFACE

SIZE	DRAWING NUMBER	REV
A3	DR-2007	-

THIRD ANGLE PROJ	SCALE	SHEET No / of SHEETS
	1:1	8 / 25



CONFIGURATION BOARD	NAME	SIGNATURE	DATE
DRAWN:	Vighnesh		
DESIGNED:	Vighnesh		
PEER REVIEWER:	Karthik		
QA REVIEWER:			
CHECKED BY:			
APPROVED BY:			

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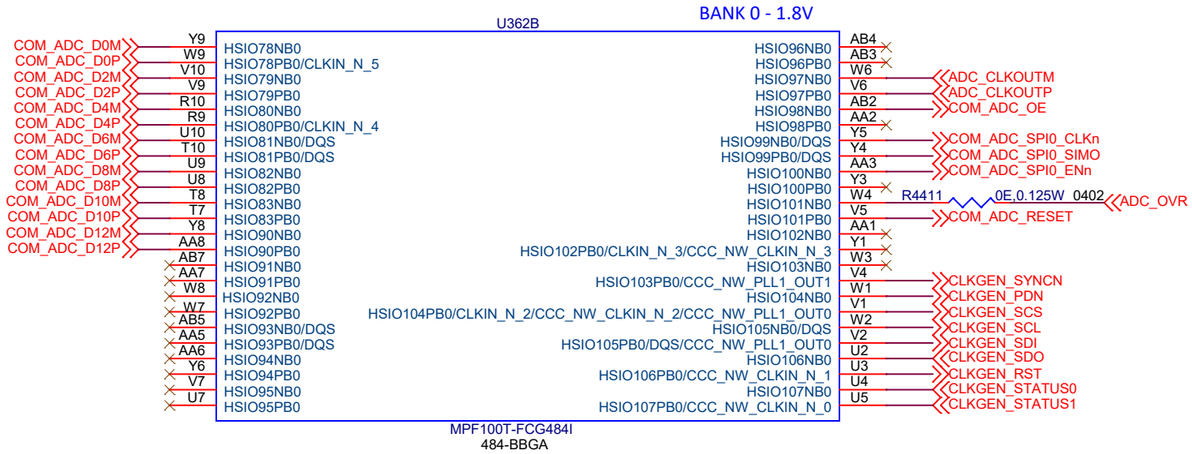
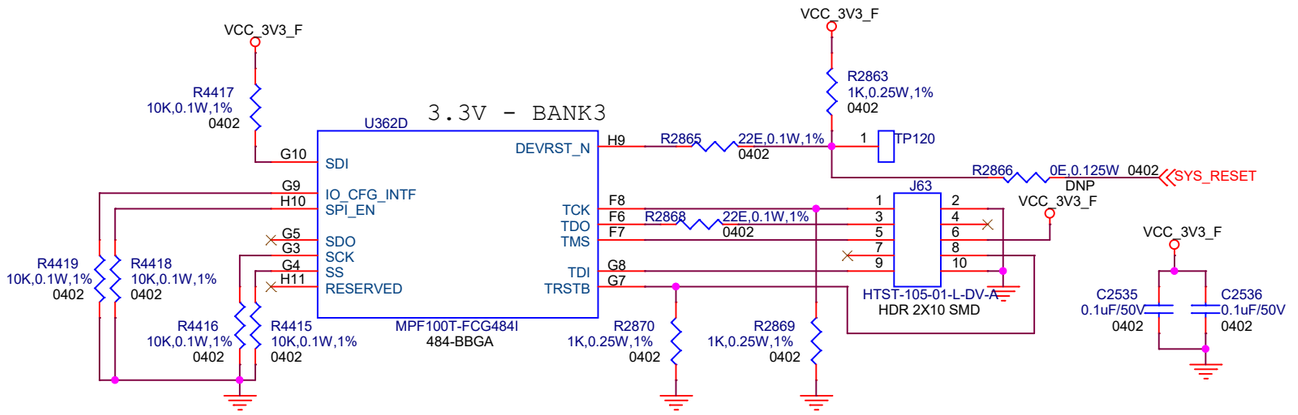
PB 1779, Old Airport Road,
Bengaluru Karnataka 560 017

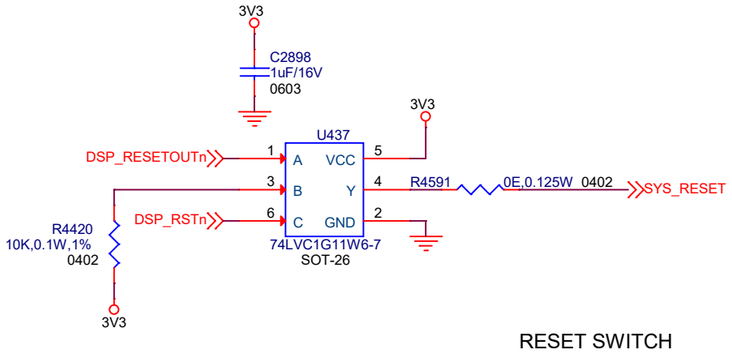
DRAWING TITLE: COM BASE CARD
DSP POWER SUPPLY

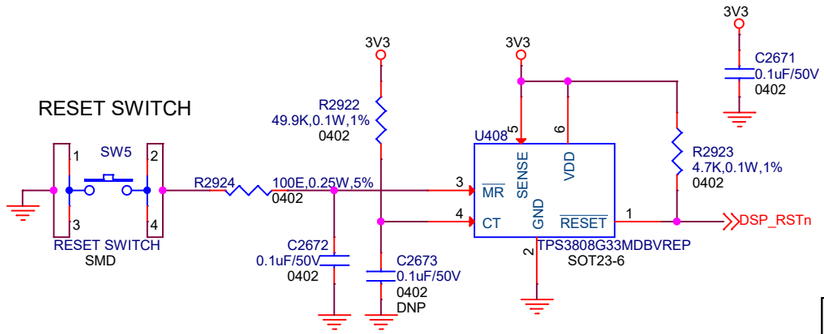
SIZE	DRAWING NUMBER	REV
A3	DR-2007	-

THIRD ANGLE PROJ	SCALE	SHEET No / of SHEETS
	1:1	13 / 25









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PEER REVIEWER:	Karthik		
QA REVIEWER:			
CHECKED BY:			
APPROVED BY:			

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Bengaluru Karnataka 560 017

DRAWING TITLE: COM BASE CARD
FPGA BANK 0 AND 3

SIZE	DRAWING NUMBER	REV
A3	DR-2007	-

THIRD ANGLE PROJ	SCALE	SHEET No / of SHEETS
	1:1	20 / 25