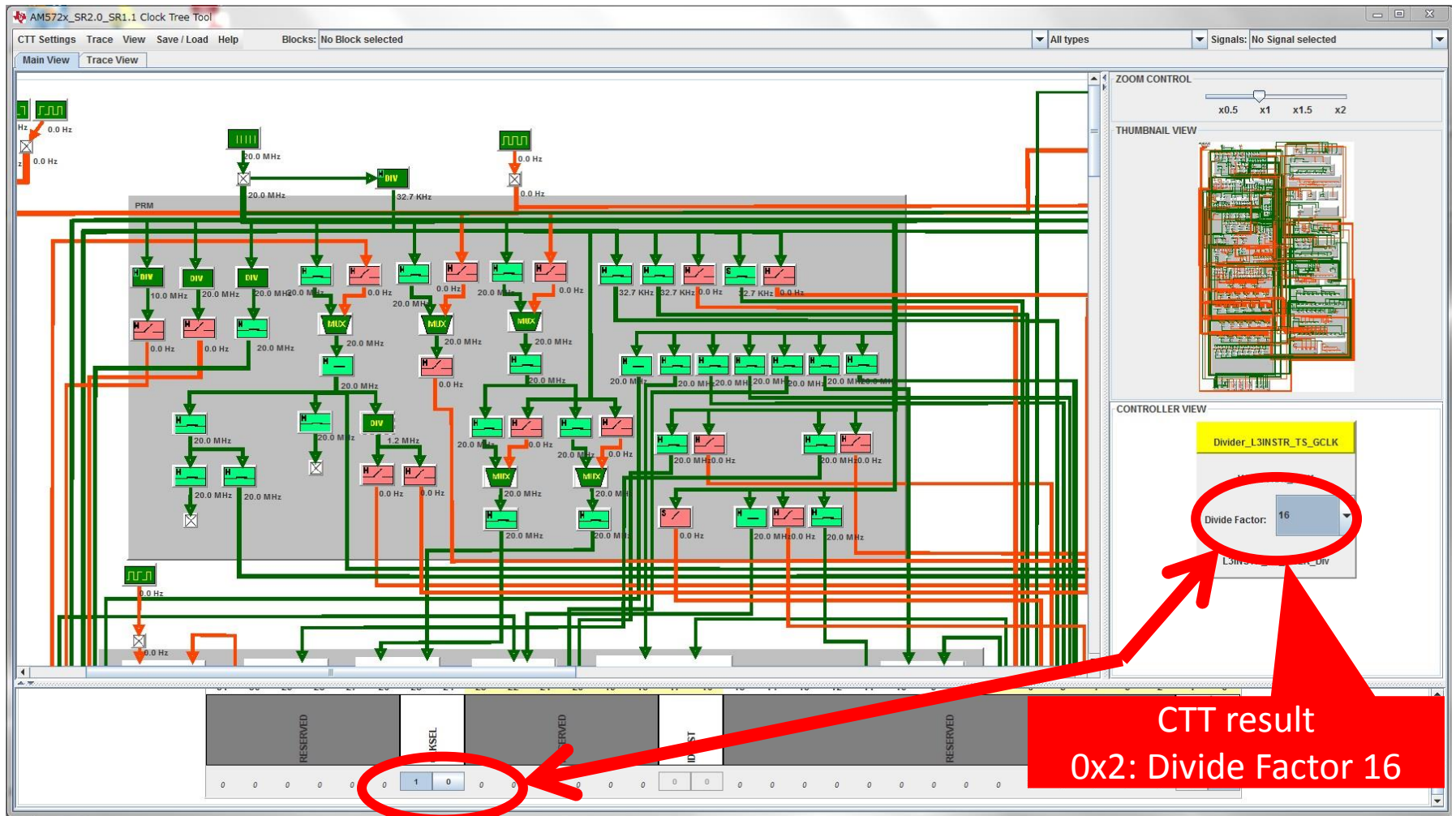




25:24 CLKSEL

Selects the divider value for generating the Thermal Sensor clock from WKUPAON_ICLK source. The divider has to be selected so as to guarantee a frequency between 1MHz and 2MHz.

RW

0x2

TRM said
0x2: Divide 32

0x0: Divide by 8

0x1: Divide by 16

0x2: Divide by 32

0x3: Reserved