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	<u>DRA744</u> (http://www.ti.com/product/DRA744)	<u>DRA746</u> (http://www.ti.com/product/DRA746)	<u>DRA75P</u> (http://www.ti.com/product/DRA75P)
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	Design & development (http://www.ti.com/product/DRA744/toolssoftware)	Design & development (http://www.ti.com/product/DRA746/toolssoftware)	Design & development (http://www.ti.com/product/DRA75P/toolssoftware)
Arm CPU	2 ARM Cortex-A15	2 ARM Cortex-A15	2 ARM Cortex-A15
Arm MHz (Max.)	1000	1500	1500
DSP	1 C66x	1 C66x	2 C66x
DSP MHz (Max)	750	750	750
Graphics acceleration	1 2D 2 3D	1 2D 2 3D	1 2D 2 3D
Co-processor(s)	2 Dual ARM Cortex-M4	2 Dual ARM Cortex-M4	2 Dual ARM Cortex-M4
DRAM	DDR2-800 DDR3-1066 DDR3L-1066	DDR2-800 DDR3-1066 DDR3L-1066	DDR2-800 DDR3-1333 DDR3L-1333
Hardware accelerators	1 Image Video Accelerator 2 Viterbi Decoder Audio Tracking	1 Image Video Accelerator 2 Viterbi Decoder Audio Tracking	1 Image Video Accelerator, 1 Image Subsystem Processor, 2 Viterbi Decoder, Audio Tracking
EMIF	2 32-bit	2 32-bit	2 32-bit

Other on-chip memory	512 KB	512 KB	2.5 MB
Ethernet MAC	10/100/1000 2-port 1Gb switch	10/100/1000 2-port 1Gb switch	10/100/1000 2-port 1Gb switch
Parallel video input ports	6	6	8
Display	1 HDMI OUT 3 LCD OUT	1 HDMI OUT 3 LCD OUT	1 HDMI OUT 3 LCD OUT
Serial I/O	CAN I2C SPI UART USB	CAN I2C SPI UART USB	CAN CAN-FD I2C QSPI SPI UART
Storage interface	1x SDIO 4b 1x SDIO 8b 1x UHSI 4b 1x eMMC 8b	1x SDIO 4b 1x SDIO 8b 1x UHSI 4b 1x eMMC 8b	1x SDIO 4b 1x SDIO 8b 1x UHSI 4b 1x eMMC 8b
PCIe	1 PCIe Gen2	1 PCIe Gen2	2 PCIe Gen2
McASP	8	8	8
TI functional safety category		Functional safety-compliant	