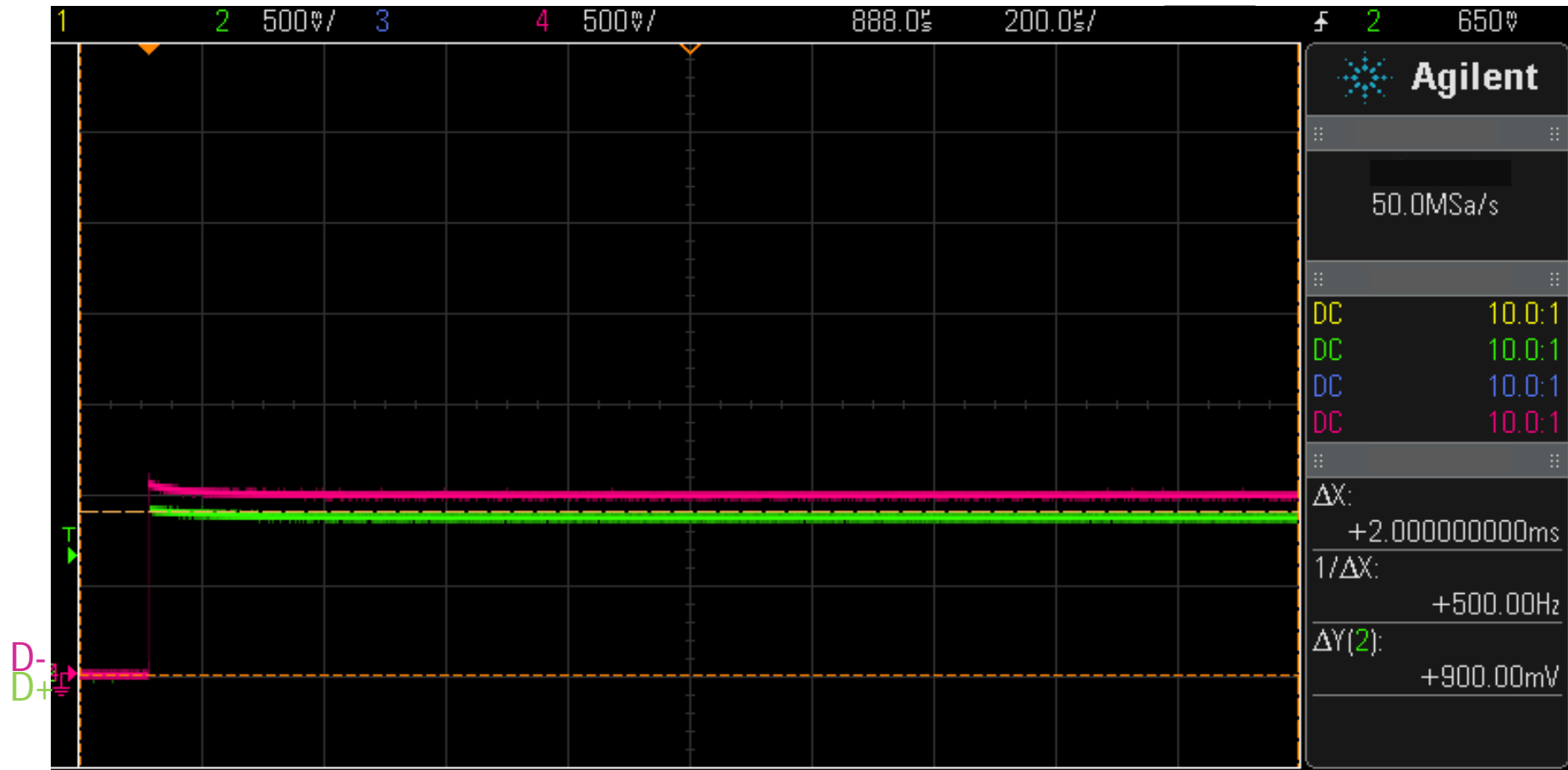
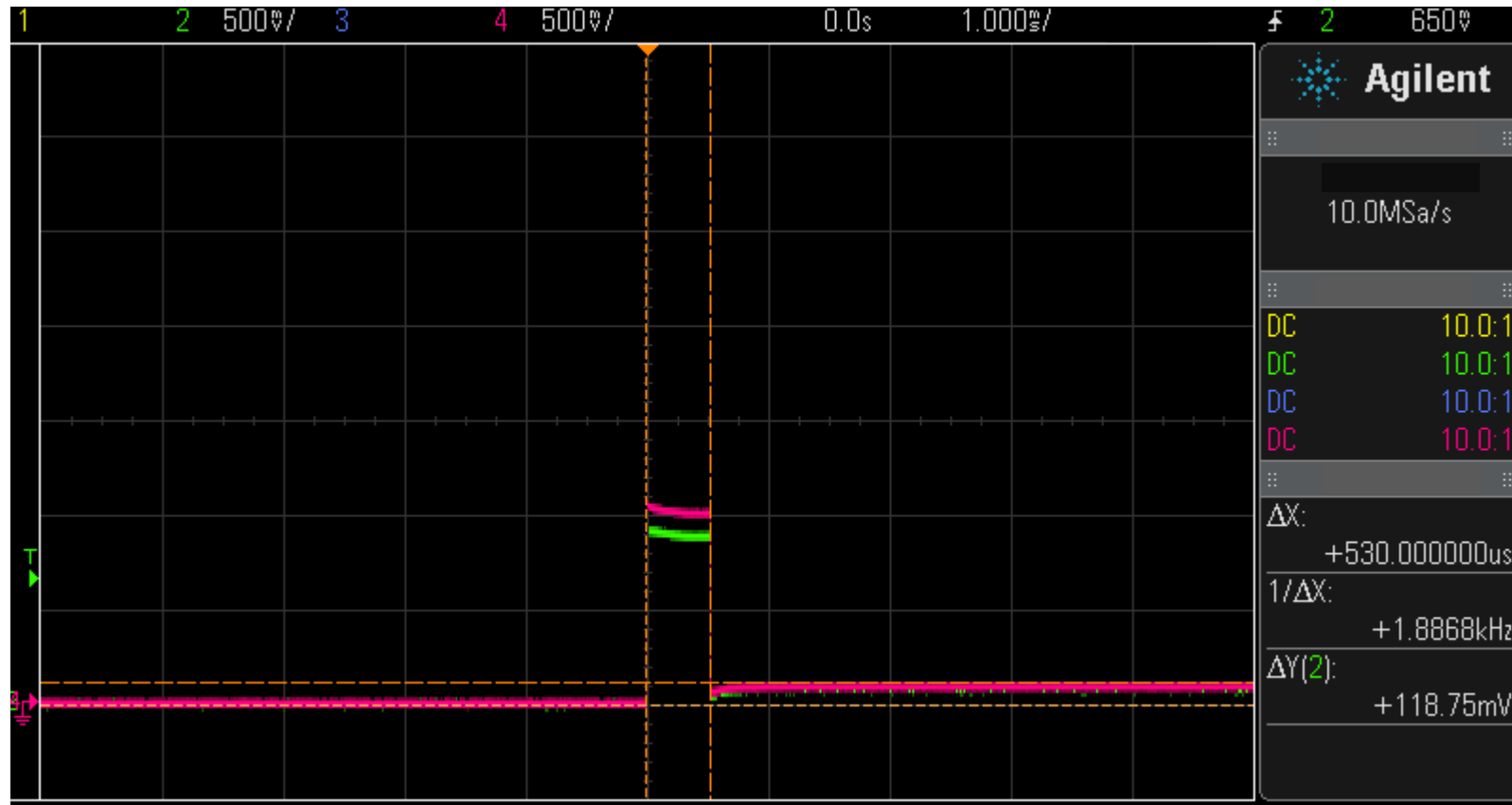


Set USBPHY_CTL bit 6,7 to 1, Set bit 2 to 1 then Clear Bit0 to 0
Result : D+ and D- goes up to about 1V(or3V) and never drop down to 0V



Additional Information:

- 1.PowerON DM6446EVM
 - 2.Connect DM6446EVM to CCS
 - 3.GEL for DM6446EVM runs
 - 4.Set USBPHY_CTL Register bit 6, 7 to 1 , clear bit2 to zero
 - 5.Clear USBPHY_CTL Register bit0 to zero
- D+/D- line goes up to 1V(or 3V) for about 530us and then drop down to around 120mV as follow:



After this, I set the bit 2 and 0 of USBPHY_CTL to one again (PHY Oscillator and PHY power off). Then tried from step4 and step5 of above sequence. Please see the next page for the result.

D+/D- goes up to 1V(or 3V) for 17us(shorter than first time) and then drop down to around 130mV. This signal is what I have seen previously and attached to the E2E Forum. So, I guess when I poweron USBPHY first time , D+/D- goes up to 1V(or 3V) and keep the voltage level longer than powering on the USBPHY second time. But I don't know why.

