

BOOTMODE[2:0] - b101 - I2C Boot Device

Device boot mode
 0 = EMIF16 / No Boot
 1 = Serial Rapid I/O
 2 = Ethernet (SGMII) (PASS PLL configuration assumes input rate same as CORECLK(PIN); BOOTMODE[12:10] values drive the PASS PLL configuration during boot)
 3 = Ethernet (SGMII) (PASS PLL configuration assumes input rate same as SRIOSGMICLK(PIN); BOOTMODE[9:8] values drive the PASS PLL configuration during boot)
 4 = PCIe
 5 = I2C
 6 = SPI
 7 = HyperLink

BOOTMODE[7:3] - b00000 - Parameter Table Index

Specifies which parameter table is loaded from I2C EEPROM. The boot ROM reads the parameter table (each table is 0x80 bytes) from the I2C EEPROM starting at I2C address (0x50 + parameter index). This value can range from 0 to 31.

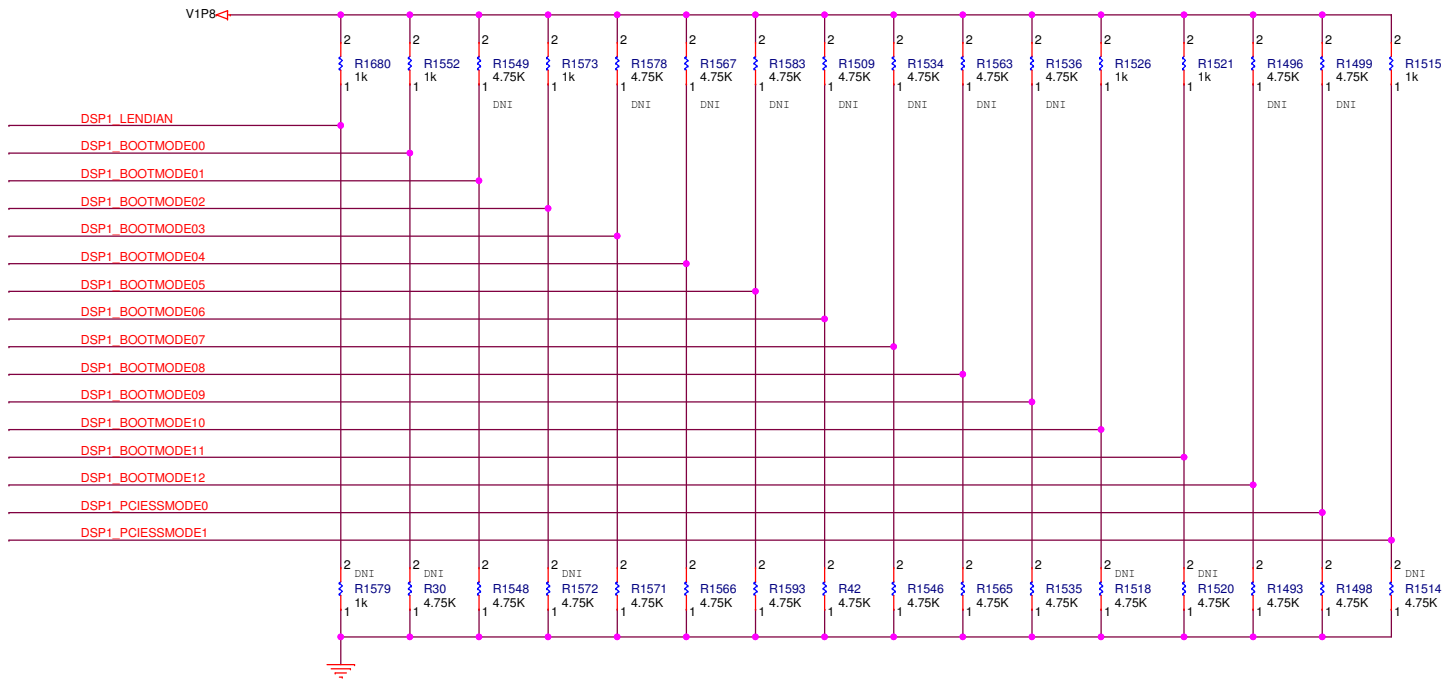
BOOTMODE[9:8] - b00 - I2C Operation Mode - Master mode
 0 = Master mode
 3 = Passive mode (see section 2.5.2.5.2 "I2C Passive Mode")
 Others = Reserved

BOOTMODE[10] - b1 - I2C address
 0 = Boot from I2C EEPROM at I2C bus address 0x50
 1 = Boot from I2C EEPROM at I2C bus address 0x51

BOOTMODE[11] - b1 - I2C data rate configuration
 0 = I2C slow mode. Initial data rate is CORECLK/5000 until PLLs and clocks are programmed
 1 = I2C fast mode. Initial data rate is CORECLK/250 until PLLs and clocks are programmed

BOOTMODE[12] - reserved

PCIESSMODE[1:0] - b00 - PCIe Subsystem mode selection.
 00 = PCIe in end point mode
 01 = PCIe legacy end point (support for legacy INTx)
 10 = PCIe in root complex mode
 11 = Reserved



<Variant Name>



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Schematic Title: Crescendo (DSPM2)			
Page Description: DSP1 CLOCKS/RESETS/BOOT			
Size: C	Author: Joseph Willis	Part Number: 64-006236-1SC_B	Rev.: B0
Last Modified: Tuesday, October 10, 2017 11:54:24		Sheet: 29 Of 103	