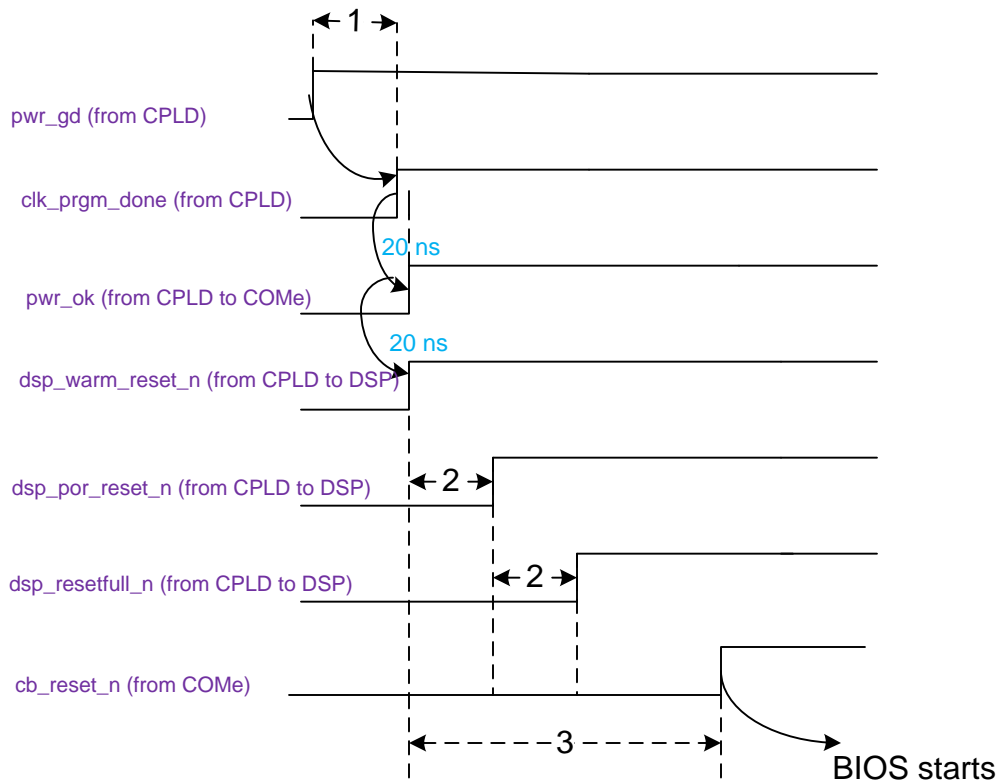


DSP Reset Timing



1 = ? (need to measure. Note: clk programming is done using i2c slow bus)

2 = 5ms

3 = ? (need to measure)