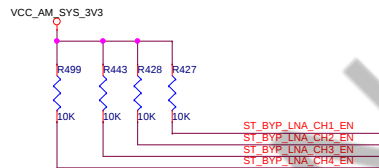
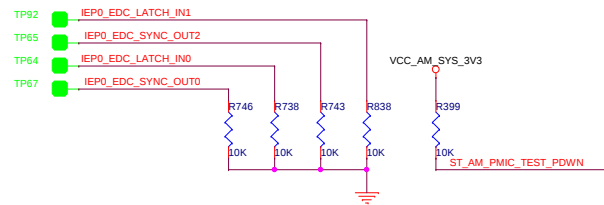
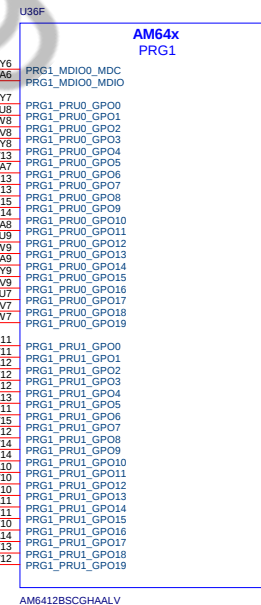
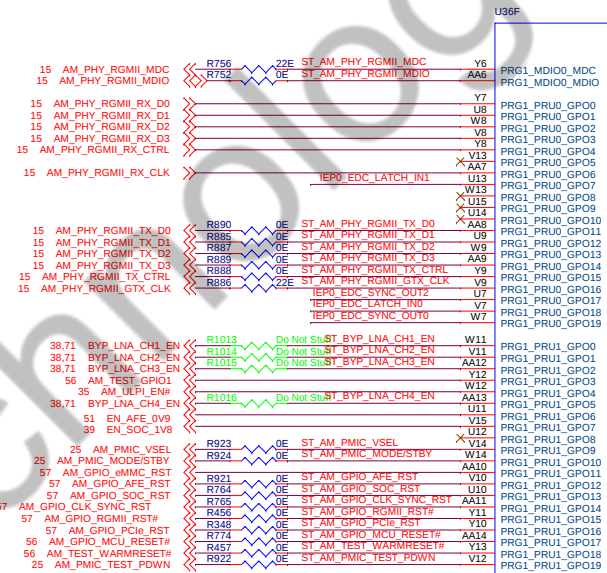


AM6412_ETHERNET_PHY_INTERFACE



Demount the pull up resistors while using the control signals.

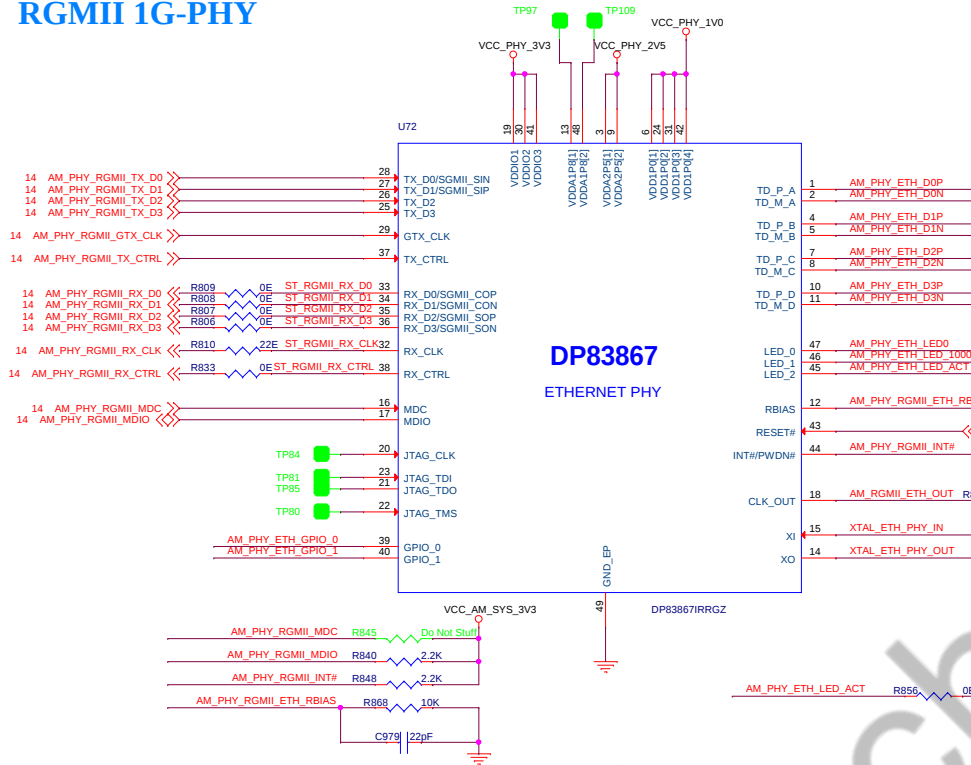


ROOM : AM6412_ETH_PHY

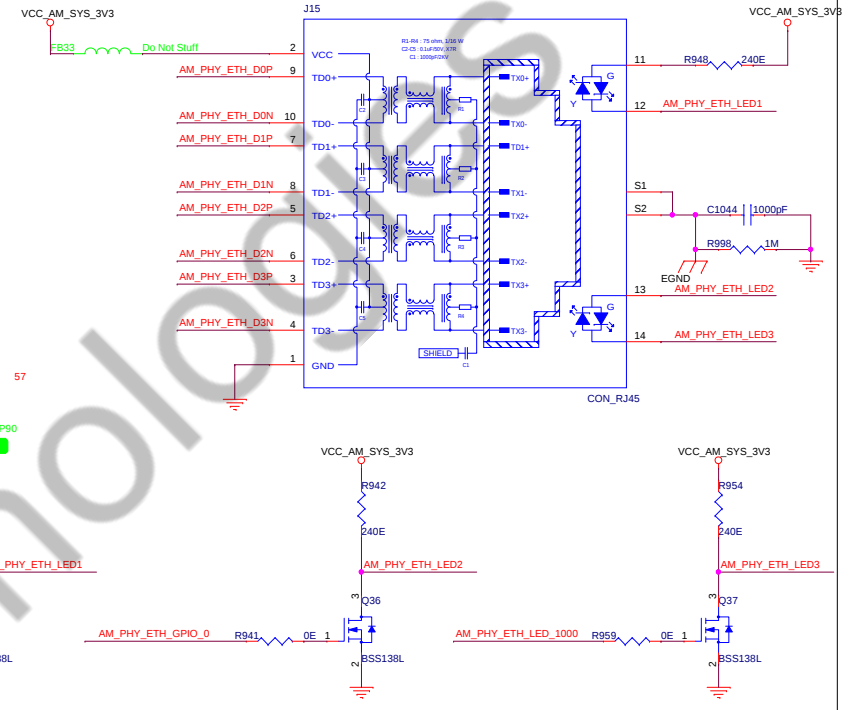
TbX5WRUBOARD

	A3	Title : AM64_ETH_PHY	
	Fab No : 501-1-02577		Rev: A2
	Asy No : 701-1-03251		Sheet 14 of 72

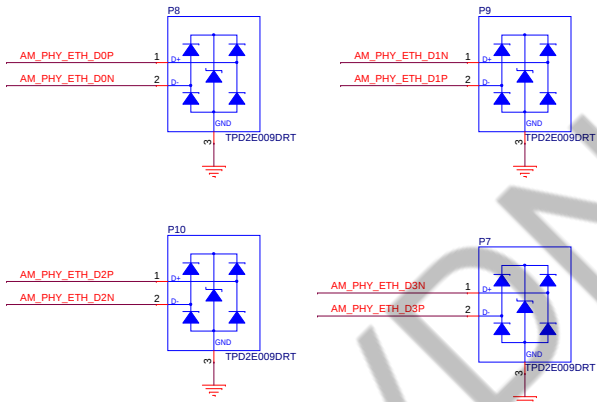
RGMII 1G-PHY



RJ45 CONNECTOR

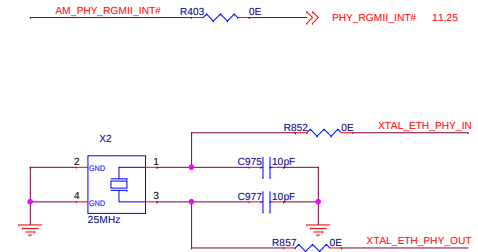
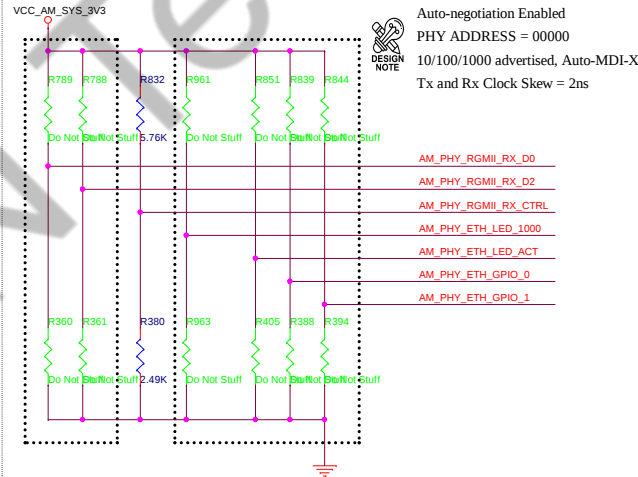


PROTECTIONS



ROOM : 1GE_PHY

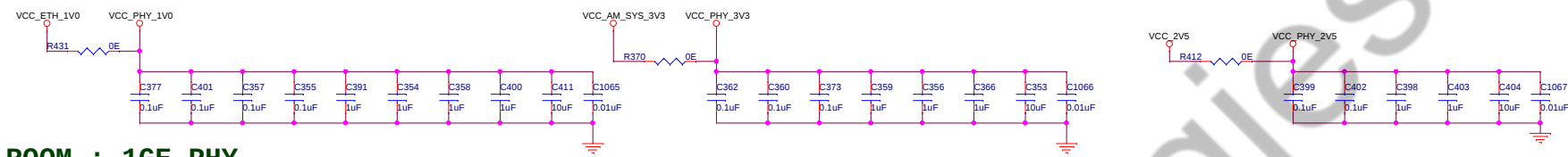
HW-STRAP RESISTORS



TXUSWRUBOARD

	A3	Title : 1GE_ETH_PHY	
	Fab No : 501-02577		Rev: A2
	Asy No : 701-03251		Sheet 15 of 72

1G-PHY DE CAPS



ROOM : 1GE_PHY

TxM5WRUBOARD			
	A3	Title : 1GE_PHY_DECAP&JTAG_CON	
	Fab No : 501-1-02577		Rev: A2
	Asy No : 701-1-03251		Sheet 16 of 72