

Error Report K2L – EVM:

Error occurrence:

6. SoC Power Failure issue

This Issue is random and analyzed in very few boards

Workaround:

Please re-flash latest UCD file from EVM webpage.

Official documentation from:

https://www.einfochips.com/images/texas_instrument/K2LEVM/TCIEVMK2LX_Known%20Issues.pdf

Experienced:

While working on programs, using both ARM and DSP, the Voltage (provided by a regulated power supply) had to be reapplied.

The Board began afterwards to show the in “Error I” described behavior.

Error switches sporadic between:

Error I:

- Fan does not work
- LCD-Display without illumination or text.
- Following LEDs are permanent on: D2,D9,D10 (green)
- all other LEDs remain off.
- No boot-up
- Board is not reachable via Serial connectors J6 or J1

Error II:

- Fan does work
- LCD-Display is illuminated
 - Displayed text is :MAIN POWER FAILURE
 - text changes to BMC-version and other Board information.
- LEDs : D2,D9,D10 (green) and also D3 (red) and D5 (blue) are permanently ON.
- J1 is not reachable via serial connection.
- BMC is reachable via J6 with 115200 baud, printing the following

```
BMC VERSION 1.1.0.6
BUILT Jan 22 2015 22:33:33
-----
[00:00:00] BMC Init Begin
[00:00:00] BMC Version 1.1.0.6
[00:00:00] UCD9xxx Reset Done..
[00:00:00] EVMK2L
[00:00:00] 1.0.3
[00:00:00] S/N: 14510204
[00:00:00] BMC Init Complete
[00:00:00] Main PWR Start Begin
[00:00:00] EVM setup has encountered the following error: The signal MAIN_POWER_GOOD has failed to assert
[00:00:00] Main PWR Stop Begin
[00:00:01] Main PWR Stop Complete
[00:00:01] HWDBG is set to ON, evm setup will now halt.
[00:00:01] To alter this behavior, please use the hwdbg command.
BMC>
```

bug-fixing: (only possible while Error II)

- BMC update using the TI- LM-Flash-Programmer → Programmer finishes with: „xxxxx Byte programmed“. (updated to BMC 1.1.0.6)
- UCD update using eInfochips' python bmc_tool „UCDgogo“ → finishes with „SUCCESS“. (updated to UCD 4.0)
- Updating Uboot as well as flashing the Linux image is not possible because the ARM is not starting and it is not possible to communicate using J1.