Issue

Handler (GPIO_TIMER_2_5) is called twice.

Detail

They periodically signal the GPIO of AM 5746 from the FPGA.
(500µs -> 1500µs -> 3500µs -> 500µs…)
They used Hwi.addHookSet to check Hwi's time. (“Time(µs)” is measured by FPGA)
As soon as HwiHdl is finished, the same HwiHdl is executed.

No		Time(us)	EndTime(us	Event	Event start	Task name	コアID	優先度
1		38144206	38145339	Event_dispatch	start	TaskApsBase	0	23
2	HwiHdl=0x800604D4 Interrupt	38144566		EVT_HWI-Begin		0x800604D4	0	
3		38144570	0	GPIO_TIMER_2_5	start	0x0	0	0
4		38144573	0	GPIO_TIMER_3	start	0x0	0	0
5		38144588		EVT_HWI-End		0x800604D4	0	
6	HwiHdl=0x800604D4 Interrupt	38144590		EVT_HWI-Begin		0x800604D4	0	
7		38144594	0	GPIO_TIMER_2_5	start	0x0	0	0
8		38144598		EVT_HWI-End		0x800604D4	0	
9		38144912		EVT_HWI-Begin		ti_sysbios_family_arm_systimer	0	

GPIO setting

```
GPIO_PinConfig gpioPinConfigs[] = {
  //  Timer2_5
  GPIO_DEVICE_CONFIG( HAL_GPIO_GPIO_PORT5 ,17  ) |  GPIO_CFG_IN_INT_HIGH |  GPIO_CFG_INPUT,
} ;
```

Question

Why does HwiHdr execute twice in a single interrupt?