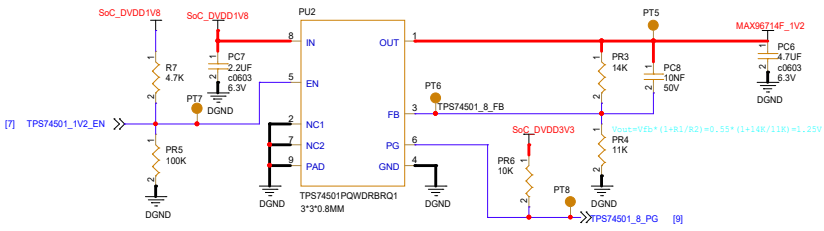
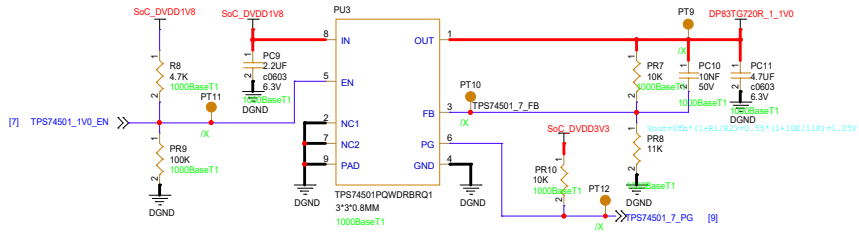


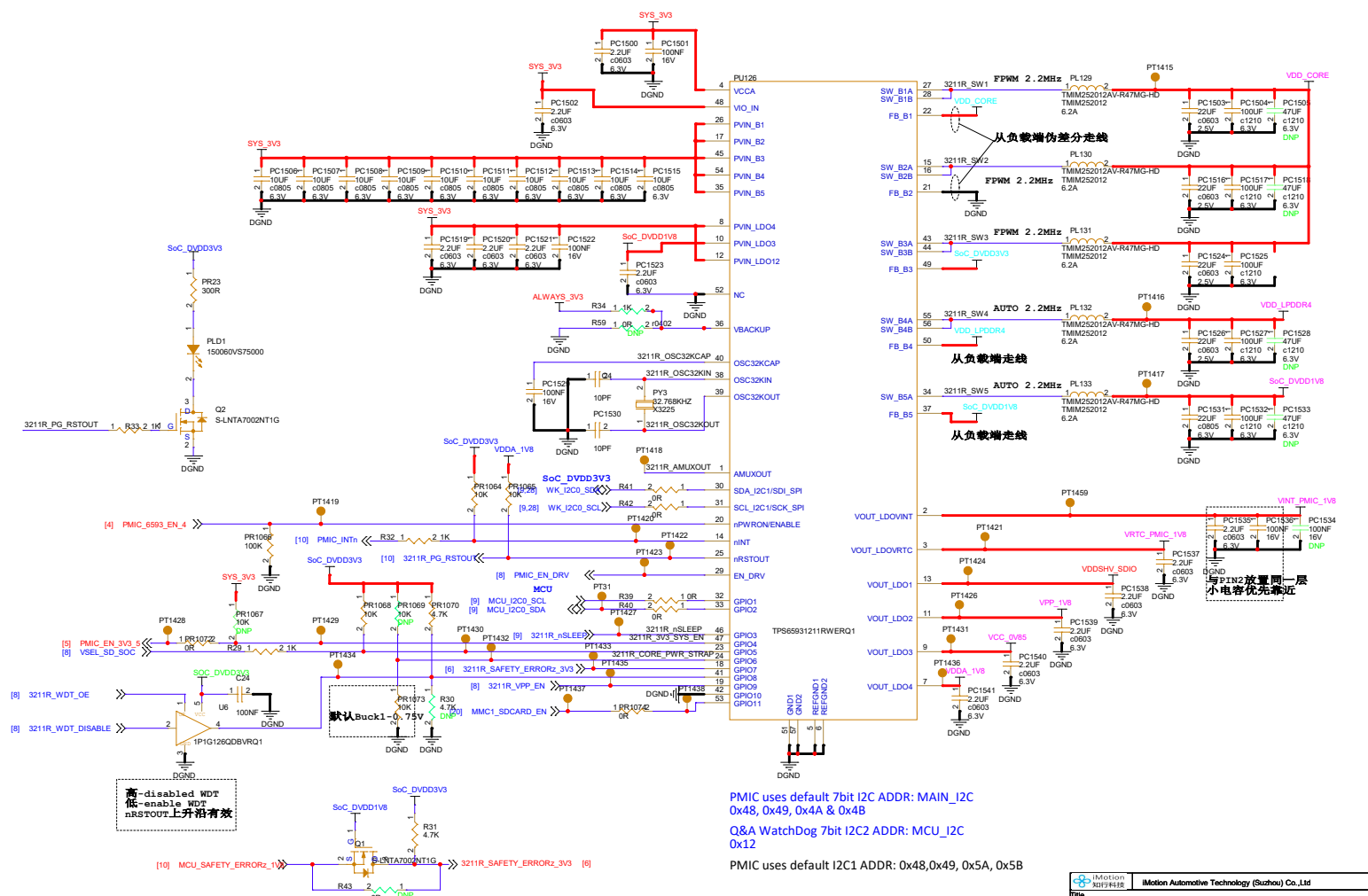
output: 1.25V@0.5A



output: 1.05V@0.5A



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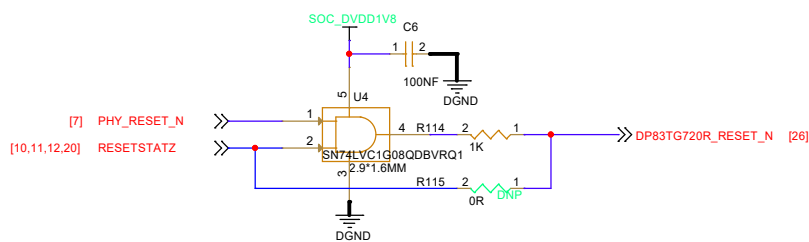
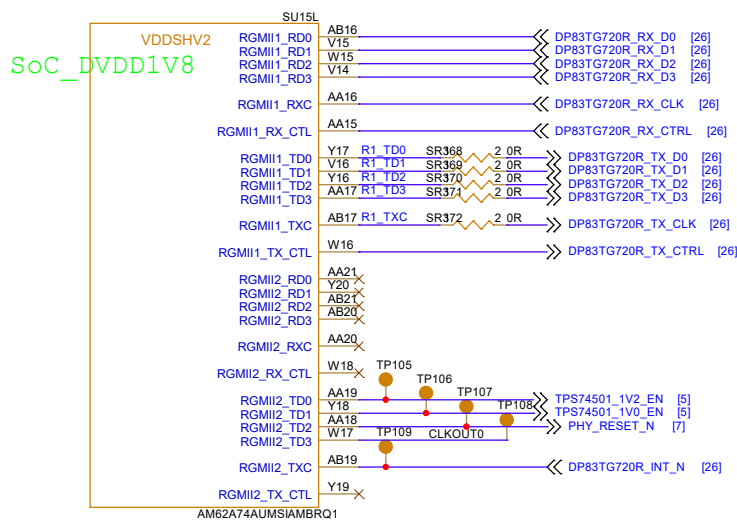


PMIC uses default 7bit I2C ADDR: MAIN_I2C
0x48, 0x49, 0x4A & 0x4B

Q&A WatchDog 7bit I2C2 ADDR: MCU_I2C
0x12

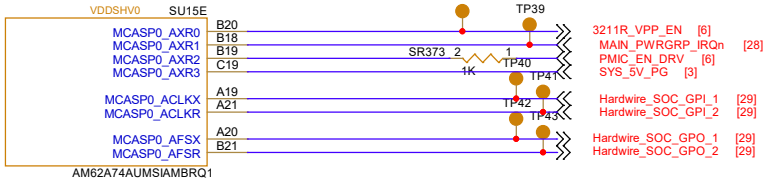
PMIC uses default I2C1 ADDR: 0x48, 0x49, 0x5A, 0x5B

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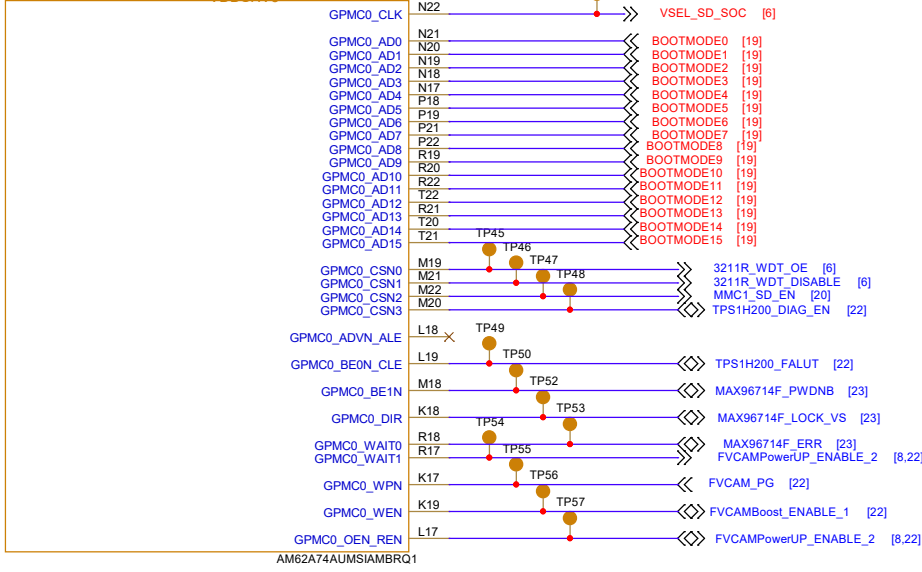


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SOC_DVDD3V3

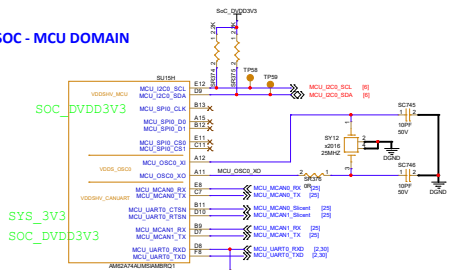


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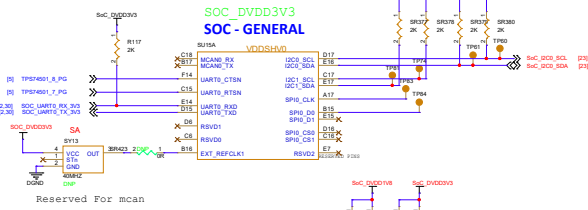


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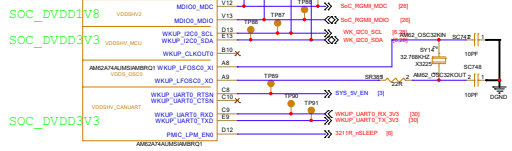
SOC - MCU DOMAIN



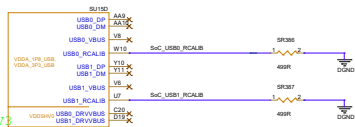
**SOC_DVDD3V3
SOC - GENERAL**



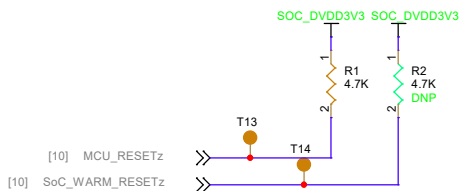
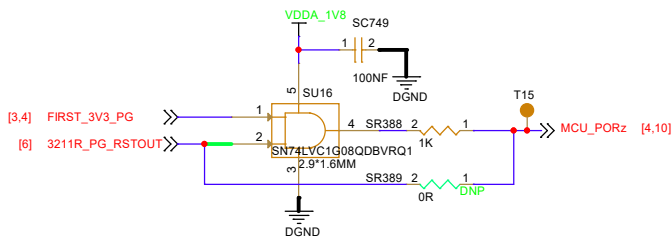
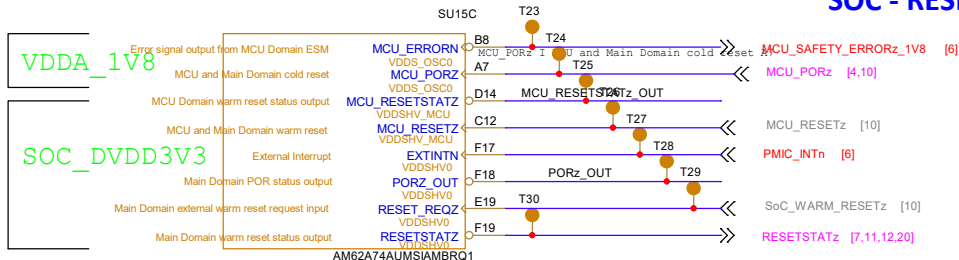
SoC SPIO FET SWITCH



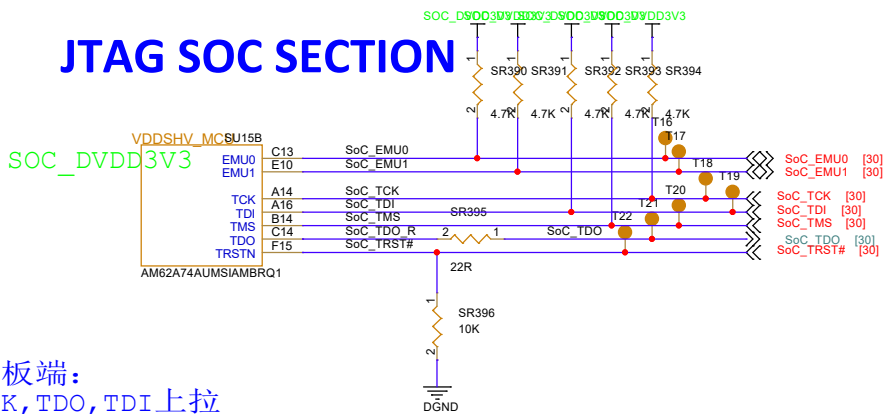
SOC_DVDD3V3



SOC - RESET



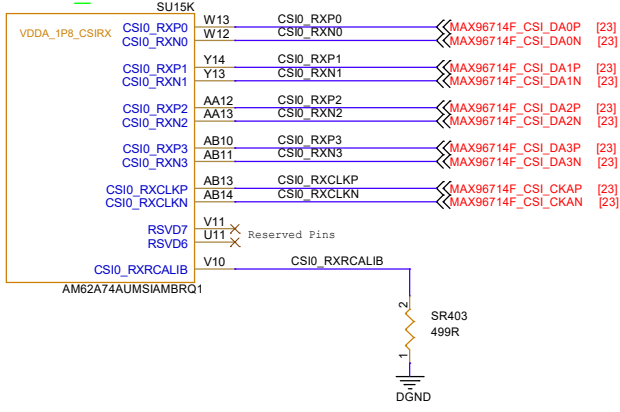
JTAG SOC SECTION



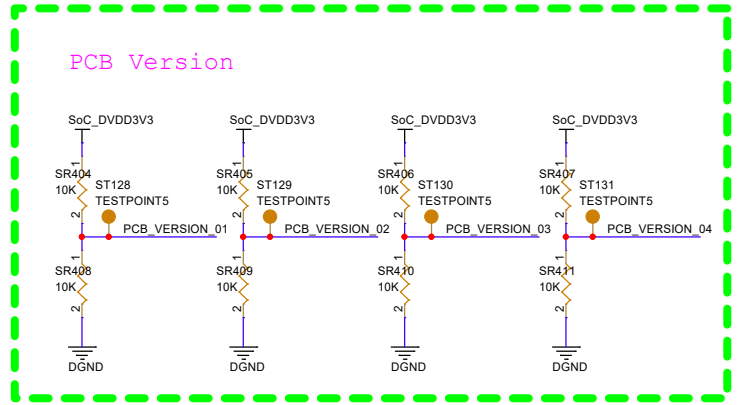
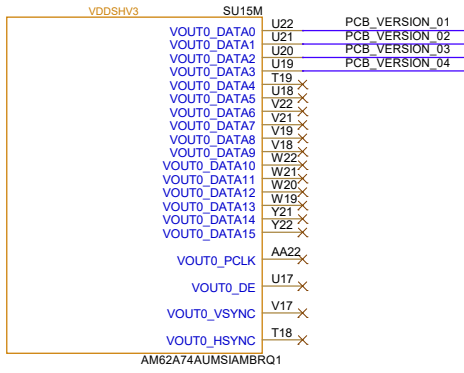
JTAG 主板端:
 TMS, TCK, TDO, TDI 上拉
 TRSTn 下拉至 DGND
 VTREF 上拉至 D1.8V (DV)

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VDDA_1V8

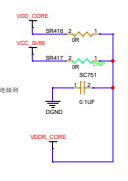
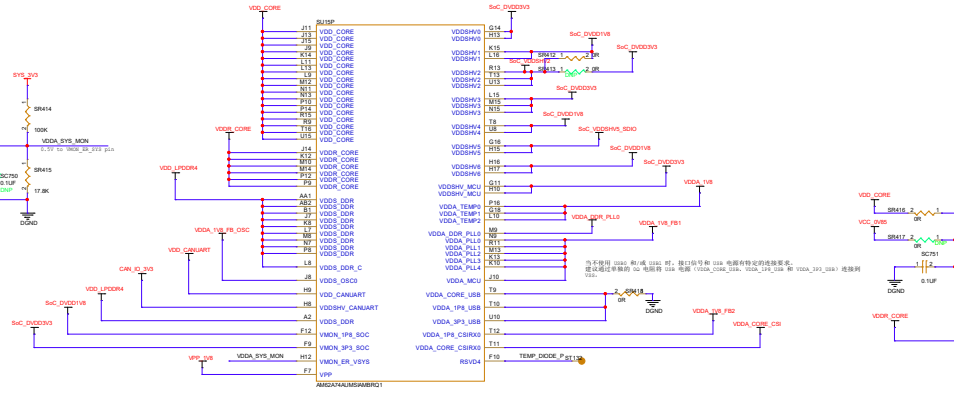


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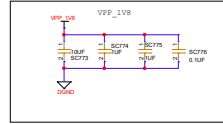
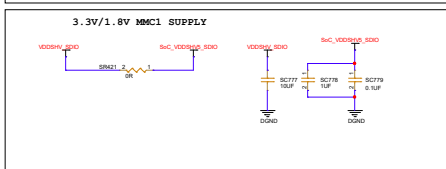
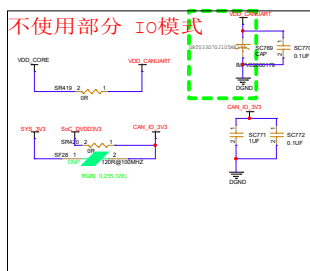
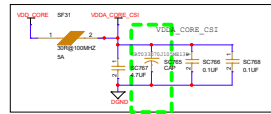
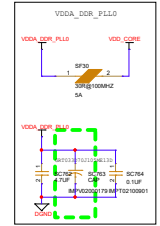
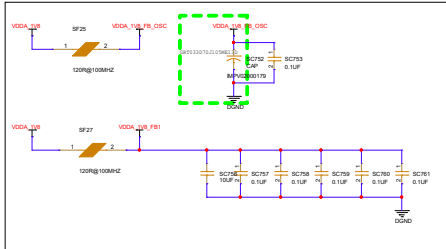
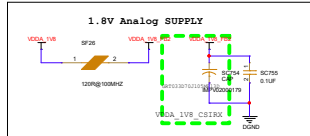


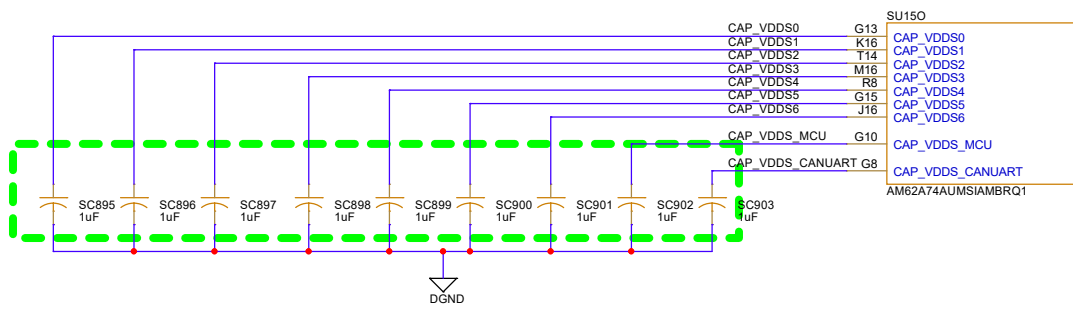
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SOC POWER



CORE SUPPLY





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C


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B

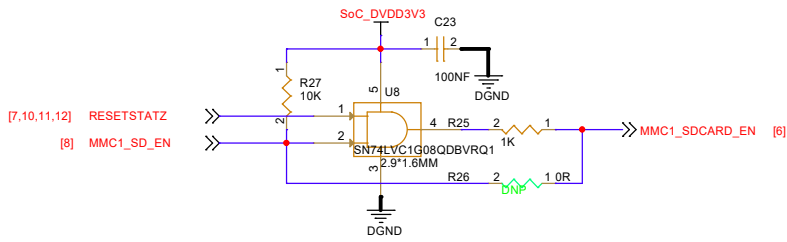
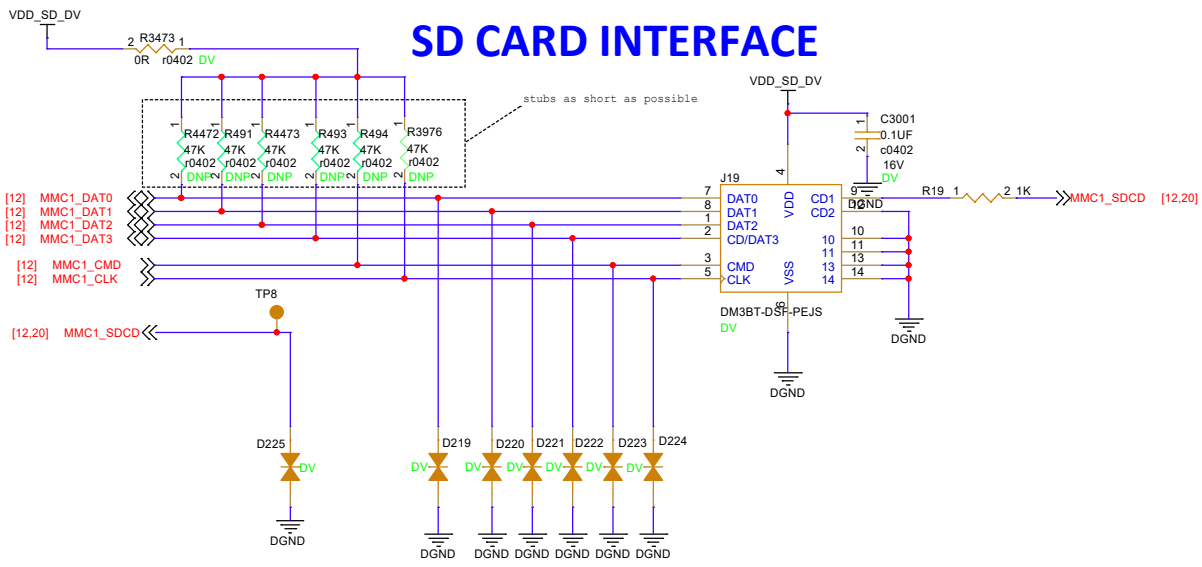
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SD CARD INTERFACE



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