

JPEG Baseline Profile Encoder (v02.02.03) on C66x

User's Guide



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Read This First

About This Manual

This document describes how to install and work with Texas Instruments' (TI) JPEG Baseline Profile Encoder implementation on the C64x+ platform. It also provides a detailed Application Programming Interface (API) reference and information on the sample application that accompanies this component.

TI's codec implementations are based on the eXpressDSP Digital Media (XDM) standard. XDM is an extension of the eXpressDSP Algorithm Interface Standard (XDAIS).

Intended Audience

This document is intended for system engineers who want to integrate TI's codecs with other software to build a multimedia system based on the C64x+ platform.

This document assumes that you are fluent in the C language, have a good working knowledge of Digital Signal Processing (DSP), digital signal processors, and DSP applications. Good knowledge of eXpressDSP Algorithm Interface Standard (XDAIS) and eXpressDSP Digital Media (XDM) standard will be helpful.

How to Use This Manual

This document includes the following chapters:

- ❑ **Chapter 1 - Introduction**, provides a brief introduction to the XDAIS and XDM standards. It also provides an overview of the codec and lists its supported features.
- ❑ **Chapter 2 - Installation Overview**, describes how to install, build, and run the codec.
- ❑ **Chapter 3 - Sample Usage**, describes the sample usage of the codec.
- ❑ **Chapter 4 - API Reference**, describes the data structures and interface functions used in the codec.
- ❑ **Appendix A – Standard Quantization Table**, provides an example for initialization of a structure type `IDMJPEG_TIGEM_CustomQuantTables` using the Standard Quantization Table.

Related Documentation From Texas Instruments

The following documents describe TI's DSP algorithm standards such as, XDAIS and XDM. To obtain a copy of any of these TI documents, visit the Texas Instruments website at www.ti.com.

- ❑ *TMS320 DSP Algorithm Standard Rules and Guidelines* (literature number SPRU352) defines a set of requirements for DSP algorithms that, if followed, allow system integrators to quickly assemble production-quality systems from one or more such algorithms.
- ❑ *TMS320 DSP Algorithm Standard API Reference* (literature number SPRU360) describes all the APIs that are defined by the TMS320 DSP Algorithm Interface Standard (also known as XDAIS) specification.
- ❑ *Technical Overview of eXpressDSP - Compliant Algorithms for DSP Software Producers* (literature number SPRA579) describes how to make algorithms compliant with the TMS320 DSP Algorithm Standard which is part of TI's eXpressDSP technology initiative.
- ❑ *Using the TMS320 DSP Algorithm Standard in a Static DSP System* (literature number SPRA577) describes how an eXpressDSP-compliant algorithm may be used effectively in a static system with limited memory.
- ❑ *DMA Guide for eXpressDSP-Compliant Algorithm Producers and Consumers* (literature number SPRA445) describes the DMA architecture specified by the TMS320 DSP Algorithm Standard (XDAIS). It also describes two sets of APIs used for accessing DMA resources: the IDMA2 abstract interface and the ACPY2 library.
- ❑ *eXpressDSP Digital Media (XDM) Standard API Reference* (literature number SPRUEC8)

The following documents describe TMS320 devices and related support tools:

- ❑ *Design and Implementation of an eXpressDSP-Compliant DMA Manager for C6X1X* (literature number SPRA789) describes a C6x1x-optimized (C6211, C6711) ACPY2 library implementation and DMA Resource Manager.
- ❑ *TMS320C64x+ Megamodule* (literature number SPRAA68) describes the enhancements made to the internal memory and describes the new features which have been added to support the internal memory architecture's performance and protection.
- ❑ *TMS320C64x+ DSP Megamodule Reference Guide* (literature number SPRU871) describes the C64x+ megamodule peripherals.
- ❑ *TMS320C64x to TMS320C64x+ CPU Migration Guide* (literature number SPRAA84) describes migration from the Texas Instruments TMS320C64x™ digital signal processor (DSP) to the TMS320C64x+™ DSP.
- ❑ *TMS320C6000 Optimizing Compiler v 6.0 Beta User's Guide* (literature number SPRU187N) explains how to use compiler tools

such as compiler, assembly optimizer, standalone simulator, library-build utility, and C++ name demangler.

- ❑ *TMS320C64x/C64x+ DSP CPU and Instruction Set Reference Guide* (literature number SPRU732) describes the CPU architecture, pipeline, instruction set, and interrupts of the C64x and C64x+ DSPs.

Related Documentation

You can use the following documents to supplement this user guide:

- ❑ *ISO/IEC IS 10918-1 Information Technology - Digital Compression and Coding of Continuous-Tone Still Images -- Part 1: Requirements and Guidelines | CCITT Recommendation T.81*

Abbreviations

The following abbreviations are used in this document.

Table 1-1. List of Abbreviations

Abbreviation	Description
ABI	Application Binary Interface
COFF	Common Object File Format
CSL	Chip Support Library
DCT	Discrete Cosine Transform
DHT	Define Huffman Table
DQT	Define Quantization Table
DRI	Define Restart Interval
DSP	Digital Signal Processing
ELF	Executable and Linkable Format
EVM	Evaluation Module
IJG	Independent JPEG Group
JFIF	JPEG File Interchange Format
JPEG	Joint Photographic Experts Group
MCU	Minimum Coded Unit
QDMA	Quick Direct Memory Access
SOF	Start of Frame

Abbreviation	Description
SOI	Start of Image
SOS	Start of Scan
VLD	Variable Length Decoding
XDAIS	eXpressDSP Algorithm Interface Standard
XDM	eXpressDSP Digital Media

Text Conventions

The following conventions are used in this document:

- Text inside back-quotes (“”) represents pseudo-code.
- Program source code, function and macro names, parameters, and command line commands are shown in a `mono-spaced` font.

Product Support

When contacting TI for support on this codec, quote the product name (JPEG Baseline Profile Encoder on C64x+) and version number. The version number of the codec is included in the Title of the Release Notes that accompanies this codec.

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Introduction

This chapter provides a brief introduction to XDAIS and XDM. It also provides an overview of TI's implementation of the JPEG Encoder on the C64x+ platform and its supported features.

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1.1 Overview of XDAIS and XDM

TI's multimedia codec implementations are based on the eXpressDSP Digital Media (XDM) standard. XDM is an extension of the eXpressDSP Algorithm Interface Standard (XDAIS).

1.1.1 XDAIS Overview

An eXpressDSP-compliant algorithm is a module that implements the abstract interface IALG. The IALG API takes the memory management function away from the algorithm and places it in the hosting framework. Thus, an interaction occurs between the algorithm and the framework. This interaction allows the client application to allocate memory for the algorithm and also share memory between algorithms. It also allows the memory to be moved around while an algorithm is operating in the system. In order to facilitate these functionalities, the IALG interface defines the following APIs:

- ❑ `algAlloc()`
- ❑ `algInit()`
- ❑ `algActivate()`
- ❑ `algDeactivate()`
- ❑ `algFree()`

The `algAlloc()` API allows the algorithm to communicate its memory requirements to the client application. The `algInit()` API allows the algorithm to initialize the memory allocated by the client application. The `algFree()` API allows the algorithm to communicate the memory to be freed when an instance is no longer required.

Once an algorithm instance object is created, it can be used to process data in real-time. The `algActivate()` API provides a notification to the algorithm instance that one or more algorithm processing methods is about to be run zero or more times in succession. After the processing methods have been run, the client application calls the `algDeactivate()` API prior to reusing any of the instance's scratch memory.

The IALG interface also defines three more optional APIs `algControl()`, `algNumAlloc()`, and `algMoved()`. For more details on these APIs, see *TMS320 DSP Algorithm Standard API Reference* (literature number SPRU360).

1.1.2 XDM Overview

In the multimedia application space, you have the choice of integrating any codec into your multimedia system. For example, if you are building a video decoder system, you can use any of the available video decoders (such as MPEG4, H.263, or H.264) in your system. To enable easy integration with the client application, it is important that all codecs with similar functionality use similar APIs. XDM was primarily defined as an extension to XDAIS to ensure uniformity across different classes of codecs

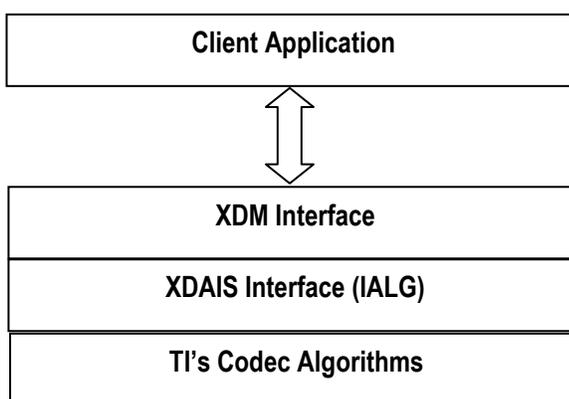
(For example audio, video, image, and speech). The XDM standard defines the following two APIs:

- ❑ `control()`
- ❑ `process()`

The `control()` API provides a standard way to control an algorithm instance and receive status information from the algorithm in real-time. The `control()` API replaces the `algControl()` API defined as part of the IALG interface. The `process()` API does the basic processing (encode/decode) of data.

Apart from defining standardized APIs for multimedia codecs, XDM also standardizes the generic parameters that the client application must pass to these APIs. The client application can define additional implementation specific parameters using extended data structures.

The following figure depicts the XDM interface to the client application.



As depicted in the figure, XDM is an extension to XDAIS and forms an interface between the client application and the codec component. XDM insulates the client application from component-level changes. Since TI's multimedia algorithms are XDM-compliant, it provides you with the flexibility to use any TI algorithm without changing the client application code. For example, if you have developed a client application using an XDM-compliant MPEG4 video decoder, then you can easily replace MPEG4 with another XDM-compliant video decoder, say H.263, with minimal changes to the client application.

For more details, see *eXpressDSP Digital Media (XDM) Standard API Reference* (literature number SPRUEC8).

1.2 Overview of JPEG Baseline Profile Encoder

The JPEG Baseline Profile Encoder accepts planar image data in YUV4:2:0, YUV4:1:1, YUV4:2:2, and YUV4:4:4 formats. It accepts interleaved image data in YUV4:2:2 format and accepts grayscale input.

From this point onwards, all references to JPEG Encoder means JPEG Baseline Profile Encoder only.

1.3 Supported Services and Features

This user guide accompanies TI's implementation of JPEG Encoder on the C64x+ platform.

This version of the codec has the following supported features:

- ❑ Supports baseline sequential mode for interleaved data formats (single scan)
- ❑ Supports multiple scans for planar formats YUV420, YUV411, YUV422, and YUV444
- ❑ Supports arbitrary image size
- ❑ Supports a maximum of three scans
- ❑ Supports comment insertion into the JPEG header
- ❑ Supports frame-based mode encoding
- ❑ Includes a standard JPEG header and also supports JFIF style header
- ❑ Huffman tables are hard-coded and built into the application at compile-time
- ❑ Supports custom quantization tables
- ❑ Supports DRI marker insertions in the compressed bit-stream
- ❑ Quantization tables are fixed with a quality factor (1-100) adjusting the quantization level
- ❑ eXpressDSP Digital Media (XDM 1.0 IIMGENC1) compliant
- ❑ Does not support encoding of images with pixel resolution more than 8 bits per pixel
- ❑ Supports thumbnail
- ❑ Supports insertion of application data APP0, APP1, and APP13
- ❑ Supports ELF ABI format

Installation Overview

This chapter provides a brief description on the system requirements and instructions for installing the codec component. It also provides information on building and running the sample test application.

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2.1 System Requirements

This section describes the hardware and software requirements for the normal functioning of the codec component.

2.1.1 Hardware

This codec has been built and tested on the TMS320C6678 EVM with on board USB emulation XDS100.

The following are the software requirements for the normal functioning of the codec:

- ❑ **Development Environment:** This project is developed using Code Composer Studio version 5.2.1.00018
- ❑ **Code Generation Tools:** This project is compiled, assembled, archived and linked using the code generation tools version 7.4.0.
- ❑ **Cygnus Cygwin V.B20**, which includes unix utilities and the 'make' program.

2.2 Installing the Component

The codec component is released as a installer executable. To install the codec, run the installer executable & follow the installer guide. This will create "C66x_jpegenc_02_02_03_00_ELF" folder onto your hard disk. Default location will be "C:\Program Files\Texas Instruments\Codecs\C66x_jpegenc_02_02_03_00_ELF".

Figure 2-1 shows the sub-directories created in the C66x_jpegenc_02_02_03_00_ELF directory.

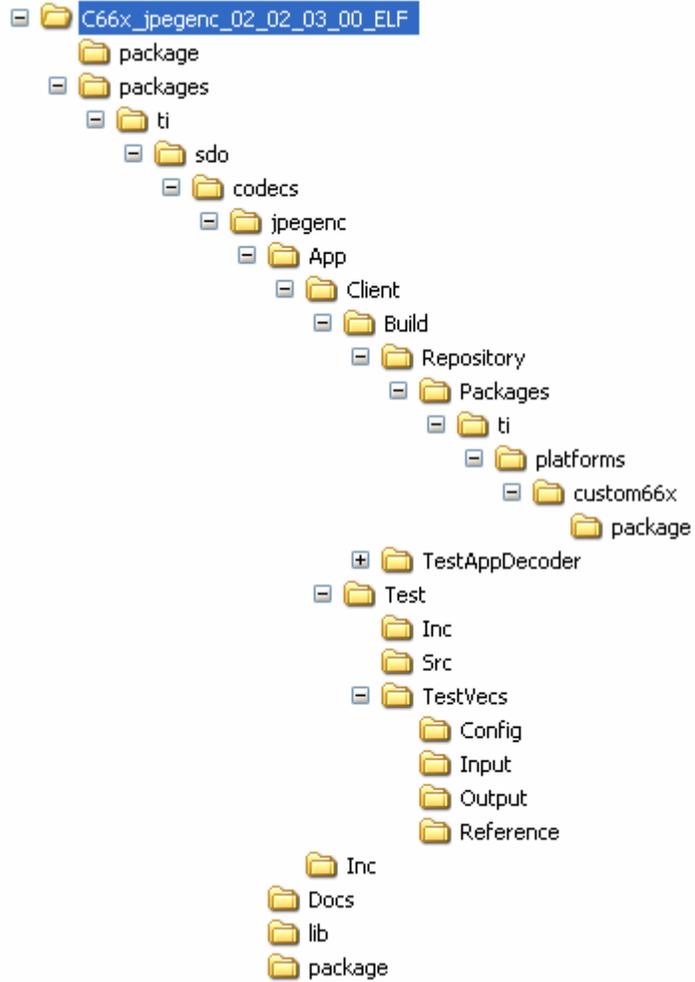


Figure 2-1. Component Directory Structure

Table 2-1 provides a description of the sub-directories created in the C66x_jpegenc_02_02_03_00_ELF\packages\ti\sdo\codecs\jpegenc directory.

Table 2-1. Component Directories

Sub-Directory	Description
\App\Inc	Contains XDM related header files which allow interface to the codec library
\lib	Contains the codec library file and map file.
\Docs	Contains user guide and datasheet
\App\Client\Build\TestAppEncoder	Contains the sample test application project, related .cmd & .cfg files.
\App\Client\Build\Repository	Contains test application settings
\App\Client\Test Src	Contains application C files

Sub-Directory	Description
\App\Client\Test\Inc	Contains header files needed for the application code
\App\Client\Test\TestVecs\Input	Contains input test vectors
\App\Client\Test\TestVecs\Output	Contains output generated by the codec
\App\Client\Test\TestVecs\Reference	Contains read-only reference output to be used for verifying codec output
\App\Client\Test\TestVecs\Config	Contains configuration parameter files

2.3 Before Building the Sample Test Application

This codec is accompanied by a sample test application. To run the sample test application, you need SYS/BIOS, Bios IPC, XDC Tools, XDAIS Tools and TI Framework Components (FC).

This version of the codec has been validated with

SYS/BIOS version 6.33.06.50,

Bios IPC version 1.24.03.32,

XDC Tools version 3.23.04.60,

XDAIS Tools version 7.23.00.06 and

Framework Component (FC) version 3.23.02.16.

2.3.1 To build and run on CCSv5 IDE

Open CCSv5 IDE. Create new workspace

2.3.2 Installing SYS/BIOS

You can download SYS/BIOS from the TI external website:

http://software-dl.ti.com/dsps/dsps_public_sw/sdo_sb/targetcontent/sysbios/index.html

Install SYS/BIOS at the same location where you have installed Code Composer Studio. For example:

<install directory>\CCStudio_5.2.1.00018

2.3.3 Installing Framework Component (FC)

You can download FC from the TI external website:

http://software-dl.ti.com/dsps/dsps_public_sw/sdo_sb/targetcontent/fc/index.html

Extract the FC zip file to the same location where you have installed Code Composer Studio. For example:

<install directory>\CCStudio_5.2.1.00018

2.3.4 Installing XDC Tools

You can download XDC Tools from the TI external website:

http://software-dl.ti.com/dsps/dsps_public_sw/sdo_sb/targetcontent/rtsc/index.html

Install XDC Tools at the same location where you have installed Code Composer Studio. For example:

<install directory>\CCStudio_5.2.1.00018

2.3.5 Installing BIOS IPC Tools

You can download BIOS IPC Tools from the TI external website:

http://software-dl.ti.com/dsps/dsps_public_sw/sdo_sb/targetcontent/ipc/index.html

Install BIOS IPC Tools at the same location where you have installed Code Composer Studio. For example:

<install directory>\CCStudio_5.2.1.00018

2.3.6 Installing XDAIS Tools

You can download XDAIS Tools from the TI external website:

http://software-dl.ti.com/dsps/dsps_public_sw/sdo_sb/targetcontent/xdais/index.html

Install XDAIS Tools at the same location where you have installed Code Composer Studio. For example:

<install directory>\CCStudio_5.2.1.00018

2.4 Building and Running the Sample Test Application

The sample test application that accompanies this codec will run in TI's Code Composer Studio development environment. To build and run the sample test application in Code Composer Studio, follow these steps:

- 1) Import the application project in workspace. Go to Project-> Import existing CCS\CCE Eclipse project. Browse the root directory of application project Ex: "C:\Program Files\Texas Instruments\Codecs\C66x_jpegenc_02_02_03_00_ELF\packages\ti\sd0\codecs\jpegenc\Ap\p\Client\Build\TestAppEncoder".

Project file will detect automatically, click on finish.

- 2) Set the Build properties -> Macros for test application project for BIOS_CG_ROOT, XDC_CG_ROOT and FRMWRK_ROOT, e.g.,
BIOS_CG_ROOT - C:\CCStudio_5.2.1.00018\bios_6_33_06_50

FRMWRK_ROOT -
C:\CCStudio_5.2.1.00018\framework_components_3_23_02_16

XDC_CG_ROOT - C:\CCStudio_5.2.1.00018\xdctools_3_23_04_60
- 3) Verify that the codec object library, jpegalg_ti.le66 exists in the \lib sub-directory.
- 4) Build the project. When build is completed, TestAppEncoder.out and TestAppEncoder.map can be found at
C66x_jpegenc_02_02_03_00_ELF\packages\ti\sdo\codecs\jpegenc\Ap
p\Client\Build\TestAppEncoder\Debug.
- 5) Create a new configuration - Go to Target -> New Target Configuration and click "finish". After this step, select the appropriate target for execution namely simulator or emulator.
- 6) Go to View->Target Configuration. Right click on the selected target configuration and choose "Launch Selected Configuration".
- 7) Load the DSP executable (TestAppEncoder.out).
- 8) Select Target -> Run to execute the sample test application. The sample test application takes the input files stored in the \App\Client\Test\Testvecs\Input sub-directory, runs the codec, and uses the reference files stored in the \App\Client\Test\Testvecs\Reference sub-directory to verify that the codec is functioning as expected.
- 9) On successful completion, the application displays one of the following messages for each frame:
 - o "Encoder compliance test passed/failed"(for compliance check mode)
 - o "Encoder output dump completed" (for output dump mode)

2.5 Configuration Files

This codec is shipped along with:

- Generic configuration file (Testvecs.cfg) - specifies input and reference files for the sample test application.
- Encoder configuration file (Testparams.cfg) - specifies the configuration parameters used by the test application to configure the Encoder.

2.5.1 Generic Configuration File

The sample test application shipped along with the codec uses the configuration file, Testvecs.cfg for determining the input and reference files for running the codec and checking for compliance. The Testvecs.cfg file is available in the \App\Client\Test\TestVecs\Config sub-directory.

The format of the Testvecs.cfg file is:

```
X
Config
Input
Output/Reference
```

where:

- x may be set as:
 - 1 - for compliance checking, no output file is created
 - 0 - for writing the output to the output file
- Config is the Encoder configuration file. For details, see Section 2.5.2.
- Input is the input file name (use complete path).
- Output/Reference is the output file name (if x is 0) or reference file name (if x is 1).

A sample Testvecs.cfg file is as shown:

```
1
..\..\..\Test\TestVecs\Config\Testparams.cfg
..\..\..\Test\TestVecs\Input\Input_422.yuv
..\..\..\Test\TestVecs\Reference\Output_422.jpg
0
..\..\..\Test\TestVecs\Config\Testparams.cfg
..\..\..\Test\TestVecs\Input\Input_422.yuv
..\..\..\Test\TestVecs\Output\Output_422.jpg
```

2.5.2 Encoder Configuration File

The encoder configuration file, Testparams.cfg contains the configuration parameters required for the encoder. The Testparams.cfg file is available in the \App\Client\Test\TestVecs\Config sub-directory.

A sample Testparams.cfg file is as shown.

```
# Input File Format is as follows
# <ParameterName> = <ParameterValue> # Comment
#
#####
Parameters
#####

MaxImageWidth      = 768 # Image width in Pels
MaxImageHeight     = 512 # Image height in Pels
IPImageWidth       = 768 # Image width in Pels
IPImageHeight      = 512 # Image height in Pels
Scan                = 0  # Maximum number of
                        scans, default value is 0
ForceChromaFormat  = 2  # 0 =>default
                        1 => XDM_YUV_420P,
                        2 => XDM_YUV_422P,
                        3 => XDM_YUV_422IBE,
                        4 => XDM_YUV_422ILE,
```

```

inputChromaFormat    = 4    # 5 => XDM_YUV_444P,
                        # 6 => XDM_YUV_411P
                        # 7 => XDM_GRAY,
                        # 8 => XDM_RGB
                        # 1 => XDM_YUV_420P,
                        # 2 => XDM_YUV_422P,
                        # 3 => XDM_YUV_422IBE,
                        # 4 => XDM_YUV_422ILE,
                        # 5 => XDM_YUV_444P,
                        # 6 => XDM_YUV_411P
                        # 7 => XDM_GRAY,
                        # 8 => XDM_RGB
QP                   = 73   # 0 => Min
                        # 100=> Max
numMCU               = 0    # 0 for Frame based encoding
                        # else slice based encoding

captureWidth         = 0    # Capture width in Pels, must be
                        # equal to 0 or equal to
                        # Inputwidth
captureHeight        = 512  # Capture Height must be
                        # equal to 0 or equal to actual
                        # Inputheight

DRI_Interval         = 0    # 0 - DRI Marker is not inserted
                        # other than 0 - will add DRI
                        # marker at regular interval

```

Any field in the `IIMGENC1_Params` structure (see Section 4.2.1.5) can be set in the `Testparams.cfg` file using the syntax shown above. If you specify additional fields in the `Testparams.cfg` file, ensure to modify the test application appropriately to handle these fields.

2.6 Standards Conformance and User-Defined Inputs

To check the conformance of the codec for the default input file shipped along with the codec, follow the steps as described in Section 2.4.

To check the conformance of the codec for other input files of your choice, follow these steps:

- 1) Copy the input files to the `\App\Client\Test\TestVecs\Inputs` sub-directory.
- 2) Copy the reference files to the `\App\Client\Test\TestVecs\Reference` sub-directory.
- 3) Edit the configuration file, `Testvecs.cfg` available in the `\App\Client\Test\TestVecs\Config` sub-directory. For details on the format of the `Testvecs.cfg` file, see Section 2.5.1.
- 4) Execute the sample test application. On successful completion, the application displays one of the following messages for each frame:
 - o "Encoder compliance test passed/failed" (if `x` is 1)
 - o "Encoder output dump completed" (if `x` is 0)

If you have chosen the option to write to an output file (`x` is 0), you can use any standard file comparison utility to compare the codec output with the reference output and check for conformance.

2.7 Uninstalling the Component

To uninstall the component, execute the “uninstall.exe” executable at the codec install directory. Ex : “C:\Program Files\Texas Instruments\Codex\C66x_jpegenc_02_02_03_00_ELF\uninstall.exe”.

\package and \packages directory shall get deleted after succesfull un-installation.

2.8 Evaluation Version

If you are using an evaluation version of this codec, a Texas Instruments logo will be visible in the output.

Sample Usage

This chapter provides a detailed description of the sample test application that accompanies this codec component.

3.1 Overview of the Test Application

The test application exercises the `IIMGENC1` base class of the JPEG Encoder library. The main test application files are `TestAppEncoder.c` and `TestAppEncoder.h`. These files are available in the `\Client\Test\Src` and `\Client\Test\Inc` sub-directories respectively.

Figure 3-1 depicts the sequence of APIs exercised in the sample test application.

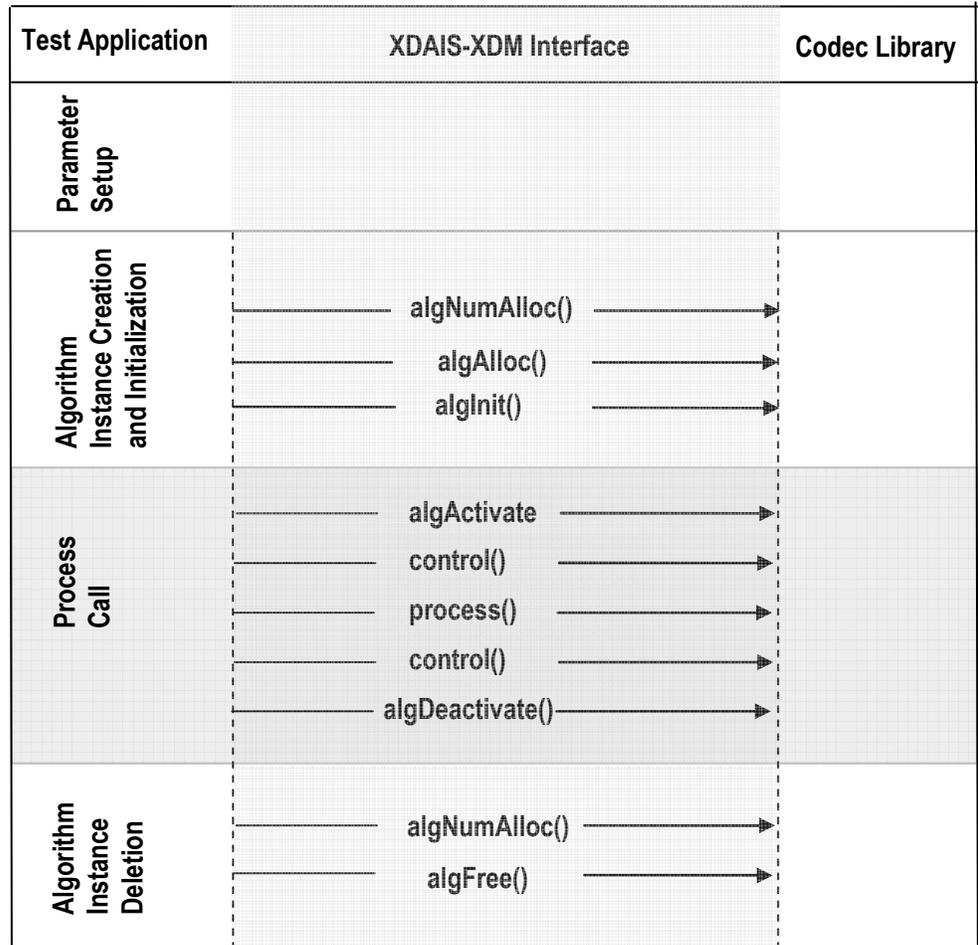


Figure 3-1. Test Application Sample Implementation

The test application is divided into four logical blocks:

- Parameter setup
- Algorithm instance creation and initialization
- Process call
- Algorithm instance deletion

3.1.1 Parameter Setup

Each codec component requires various codec configuration parameters to be set at initialization. For example, a video codec requires parameters such as video height, video width, and so on. The test application obtains the required parameters from the Encoder configuration files.

In this logical block, the test application performs the following:

- 1) Opens the generic configuration file, `Testvecs.cfg` and reads the compliance checking parameter, Encoder configuration file name (`Testparams.cfg`), input file name, and output/reference file name.
- 2) Opens the Encoder configuration file, (`Testparams.cfg`) and reads the various configuration parameters required for the algorithm.

For more details on the configuration files, see Section 0.

- 3) Sets the `IIMGENC1_Params` structure based on the values it reads from the `Testparams.cfg` file.
- 4) Reads the input bit-stream into the application input buffer.

After successful completion of the above steps, the test application performs the algorithm instance creation and initialization.

3.1.2 Algorithm Instance Creation and Initialization

In this logical block, the test application accepts the various initialization parameters and returns an algorithm instance pointer. The following APIs are called in sequence:

- 1) `algNumAlloc()` - To query the algorithm about the number of memory records it requires.
- 2) `algAlloc()` - To query the algorithm about the memory requirement to be filled in the memory records.
- 3) `algInit()` - To initialize the algorithm with the memory structures provided by the application.

A sample implementation of the create function that calls `algNumAlloc()`, `algAlloc()`, and `algInit()` in sequence is provided in the `ALG_create()` function implemented in the `alg_create.c` file.

3.1.3 Process Call

After algorithm instance creation and initialization, the test application performs the following:

- 1) Sets the dynamic parameters (if they change during run-time) by calling the `control()` function with the `XDM_SETPARAMS` command.
- 2) Sets the input and output buffer descriptors required for the `process()` function call. The input and output buffer descriptors are obtained by calling the `control()` function with the `XDM_GETBUFINFO` command.

- 3) Calls the `process()` function to encode/decode a single frame of data. The behavior of the algorithm can be controlled using various dynamic parameters (see Section 4.2.1.6). The inputs to the process function are input and output buffer descriptors, pointer to the `IIMGENC1_InArgs` and `IIMGENC1_OutArgs` structures.

The `control()` and `process()` functions should be called only within the scope of the `algActivate()` and `algDeactivate()` XDAIS functions which activate and deactivate the algorithm instance respectively. Once an algorithm is activated, there could be any ordering of `control()` and `process()` functions. The following APIs are called in sequence:

- 1) `algActivate()` - To activate the algorithm instance.
- 2) `control()` (optional) - To query the algorithm on status or setting of dynamic parameters and so on, using the six available control commands.
- 3) `process()` - To call the Encoder with appropriate input/output buffer and arguments information.
- 4) `control()` (optional) - To query the algorithm on status or setting of dynamic parameters and so on, using the six available control commands.
- 5) `algDeactivate()` - To deactivate the algorithm instance.

The do-while loop encapsulates frame level `process()` call and updates the input buffer pointer every time before the next call. The do-while loop breaks off either when an error condition occurs or when the input buffer exhausts. It also protects the `process()` call from file operations by placing appropriate calls for cache operations as well. The test application does a cache invalidate for the valid input buffers before `process()` and a cache write back invalidate for output buffers after `process()`.

In the sample test application, after calling `algDeactivate()`, the output data is either dumped to a file or compared with a reference file.

3.1.4 Algorithm Instance Deletion

Once encoding/decoding is complete, the test application must delete the current algorithm instance. The following APIs are called in sequence:

- 1) `algNumAlloc()` - To query the algorithm about the number of memory records it used.
- 2) `algFree()` - To query the algorithm to get the memory record information and then free them up for the application.

A sample implementation of the delete function that calls `algNumAlloc()` and `algFree()` in sequence is provided in the `ALG_delete()` function implemented in the `alg_create.c` file.

API Reference

This chapter provides a detailed description of the data structures and interfaces functions used in the codec component.

Topic	Page
4.1 Symbolic Constants and Enumerated Data Types	4-2
4.2 Data Structures	4-5
4.3 Interface Functions	4-19

4.1 Symbolic Constants and Enumerated Data Types

This section summarizes all the symbolic constants specified as either #define macros and/or enumerated C data types. For each symbolic constant, the semantics or interpretation of the same is also provided.

Table 4-1. List of Enumerated Data Types

Group or Enumeration Class	Symbolic Constant Name	Description or Evaluation
XDM_DataFormat	XDM_BYTE	Big endian stream.
	XDM_LE_16	16-bit little endian stream.
	XDM_LE_32	32-bit little endian stream.
XDM_ChromaFormat	XDM_YUV_420P	YUV 4:2:0 planar
	XDM_YUV_422P	YUV 4:2:2 planar
	XDM_YUV_422IBE	YUV 4:2:2 interleaved (big endian). Not supported in this version of JPEG Encoder.
	XDM_YUV_422ILE	YUV 4:2:2 interleaved (little endian)
	XDM_YUV_444P	YUV 4:4:4 planar
	XDM_YUV_411P	YUV 4:1:1 planar
	XDM_GRAY	Gray format
	XDM_RGB	RGB color format. Not supported in this version of JPEG Encoder.
XDM_CmdId	XDM_DEFAULT	Default value
	XDM_GETSTATUS	Query algorithm instance to fill the Status structure
	XDM_SETPARAMS	Set run-time dynamic parameters via the DynamicParams structure
	XDM_RESET	Reset the algorithm
	XDM_SETDEFAULT	Initialize all fields in Params structure to default values specified in the library
	XDM_FLUSH	Handle end of stream conditions. This command forces algorithm instance to output data without additional input.
	XDM_GETBUFINFO	Query algorithm instance regarding the properties of input and output buffers

Group or Enumeration Class	Symbolic Constant Name	Description or Evaluation
	XDM_SETIMGPREPROCESSOR	Image pipe is not supported in this version of JPEG Encoder.
	XDM_INSERTCOMMENT	Pass a BYTE array to insert into the JPEG header as a comment.
	XDM_GETVERSION	Retrieve a pointer to a CHAR array containing a string with the current library version.
XDM_EncMode	XDM_ENCODE_AU	Encode entire access unit
	XDM_GENERATE_HEADER	Encode only header
XDM_ErrorBit	XDM_APPLIEDCONCEALMENT	Bit 9 <input type="checkbox"/> 1 - Applied concealment <input type="checkbox"/> 0 - Ignore Not applicable for JPEG Encoder.
	XDM_INSUFFICIENTDATA	Bit 10 <input type="checkbox"/> 1 - Insufficient data <input type="checkbox"/> 0 - Ignore
	XDM_CORRUPTEDDATA	Bit 11 <input type="checkbox"/> 1 - Data problem/corruption <input type="checkbox"/> 0 - Ignore
	XDM_CORRUPTEDHEADER	Bit 12 <input type="checkbox"/> 1 - Header problem/corruption <input type="checkbox"/> 0 - Ignore Not applicable for JPEG Encoder.
	XDM_UNSUPPORTEDINPUT	Bit 13 <input type="checkbox"/> 1 - Unsupported feature/parameter in input <input type="checkbox"/> 0 - Ignore Not applicable for JPEG Encoder.
	XDM_UNSUPPORTEDPARAM	Bit 14 <input type="checkbox"/> 1 - Unsupported input parameter or configuration <input type="checkbox"/> 0 - Ignore This bit is set by the JPEG Encoder when an input parameter is not valid. For example, this can occur when the input data format is not supported.
	XDM_FATALERROR	Bit 15 <input type="checkbox"/> 1 - Fatal error (stop encoding) <input type="checkbox"/> 0 - Recoverable error This bit is set when a fatal error occurs. The JPEG Encoder cannot be used for an encode process in this state.

Note:

The remaining bits that are not mentioned in `XDM_ErrorBit` are interpreted as:

- ❑ Bit 16-32 : Reserved
- ❑ Bit 8 : Reserved
- ❑ Bit 0-7 : Codec and implementation specific (see Table 4-2)

The algorithm can set multiple bits to 1 depending on the error condition.

- ❑ Comment insertion is supported through Application buffer but not through `XDM_INSERTCOMMENT` control command
- ❑ `XDM_GETVERSION` is not supported in this version of JPEG Encoder. However, version can be extracted through `vers.exe`

The JPEG Encoder specific error status messages are listed in Table 4-2. The value column indicates the decimal value of the last 8-bits reserved for codec specific error statuses.

Table 4-2. JPEG Encoder Error Statuses

Group or Enumeration Class	Symbolic Constant Name	Value	Description or Evaluation
<code>IJPEGENC_ErrorStatus</code>	<code>JPEGENC_SUCCESS</code>	0	Successful encoding.
	<code>JPEGENC_OUTPUT_BUFF_SIZE</code>	1	Set an error, if output buffer size is less than required.
	<code>JPEGENC_THUMB_RESOLUTION</code>	2	Set an error, if <code>Thumbnail_resolutions</code> is greater than <code>Image_resolutions</code> .

4.2 Data Structures

This section describes the XDM defined data structures that are common across codec classes. These XDM data structures can be extended to define any implementation specific parameters for a codec component.

4.2.1 Common XDM Data Structures

This section includes the following common XDM data structures:

- ❑ XDM_BufDesc
- ❑ XDM1_BufDesc
- ❑ XDM_AlgBufInfo
- ❑ IIMGENC1_Fxns
- ❑ IIMGENC1_Params
- ❑ IIMGENC1_DynamicParams
- ❑ IIMGENC1_InArgs
- ❑ IIMGENC1_Status
- ❑ IIMGENC1_OutArgs

4.2.1.1 XDM_BufDesc

|| Description

This structure defines the buffer descriptor for input and output buffers.

|| Fields

Field	Datatype	Input/ Output	Description
**bufs	XDAS_Int8	Input	Pointer to the vector containing buffer addresses
numBufs	XDAS_Int32	Input	Number of buffers
*bufSizes	XDAS_Int32	Input	Size of each buffer in bytes

4.2.1.2 XDM1_BufDesc

|| Description

This structure defines the single buffer descriptor for input and output buffers.

|| Fields

Field	Datatype	Input/ Output	Description
numBufs	XDAS_Int32	Input	Number of buffers
descs[XDM_MAX_IO_BUFFERS]	XDM1_SingleBufDesc	Input	Array of Buffer descriptors

4.2.1.3 XDM_AlgBufInfo

|| Description

This structure defines the buffer information descriptor for input and output buffers. This structure is filled when you invoke the `control()` function with the `XDM_GETBUFINFO` command.

|| Fields

Field	Datatype	Input/ Output	Description
minNumInBufs	XDAS_Int32	Output	Number of input buffers
minNumOutBufs	XDAS_Int32	Output	Number of output buffers
minInBufSize[XDM_MAX_IO_BUFFERS]	XDAS_Int32	Output	Size in bytes required for each input buffer

Field	Datatype	Input/ Output	Description
minOutBufSize[XDM_MAX_IO_BUFFERS]	XDAS_Int32	Output	Size in bytes required for each output buffer

Note:

The JPEG Encoder has the following buffer information:

- ❑ control() function must be called with XDM_CmdId set to XDM_SETPARAMS before calling with XDM_CmdId set to XDM_GETBUFINFO.
- ❑ Number of input buffers required are 1 for XDM_YUV_422ILE and 3 for planar formats.
- ❑ Number of output buffer required is 1.
- ❑ Set maxWidth/inputWidth and maxHeight/inputHeight as multiples of the number specified in the following table based on the input chromaformat.

Input Format	Multiplication Factor	
	Width	Height
XDM_YUV_420P	16	16
XDM_YUV_422P	16	8
XDM_YUV_422ILE	16	8
XDM_YUV_444P	8	8
XDM_YUV_411P	32	8
XDM_GRAY	8	8

- ❑ By default, each input buffer size is equal to $(\text{maxHeight} * \text{maxWidth} * 2)$ for XDM_YUV_422ILE and $(\text{maxHeight} * \text{maxWidth} * 3)$ for planar formats. See IIMGENC1_Params data structure for details.
- ❑ Output buffer size can be estimated as $\text{Height} * \text{Width} * \text{Quality Factor} / 100$
- ❑ For interleaved input format, output will have only one scan irrespective of the output format
- ❑ If the output format is set to XDM_GRAY, output will have only one scan irrespective of the input format.

4.2.1.4 IIMGENC1_Fxns

|| Description

This structure contains pointers to all the XDAIS and XDM interface functions.

|| Fields

Field	Datatype	Input/ Output	Description
ialg	IALG_Fxns	Input	Structure containing pointers to all the XDAIS interface functions. For more details, see <i>TMS320 DSP Algorithm Standard API Reference</i> (SPRU360).
*process	XDAS_Int32	Input	Pointer to the <code>process()</code> function.
*control	XDAS_Int32	Input	Pointer to the <code>control()</code> function.

4.2.1.5 IIMGENC1_Params

|| Description

This structure defines the creation parameters for algorithm instance object. Set this data structure to `NULL`, if you are not sure of the values to be specified for these parameters.

|| Fields

Field	Datatype	Input/ Output	Description
size	XDAS_Int32	Input	Size of the basic or extended (if being used) data structure in bytes. The default value is <code>sizeof (IDMJPEG_TIGEM_Params)</code>
maxHeight	XDAS_Int32	Input	Maximum image height. The default value is 2000.
maxWidth	XDAS_Int32	Input	Maximum image width. The default value is 2000.
maxScans	XDAS_Int32	Input	Maximum number of scans. <code>maxScans</code> should be set to <code>XDM_DEFAULT</code> .
dataEndianness	XDAS_Int32	Input	Endianness of input data. The JPEG Encoder implementation supports only <code>XDM_BYTE</code> format.

Field	Datatype	Input/Output	Description
forceChromaFormat	XDAS_Int32	Input	Output color format for the encoder. See XDM_ChromaFormat enumeration for details. If set to XDM_DEFAULT, the output color format matches the input color format. Planar input formats can be converted to XDM_GRAY by ignoring the color planes. The default value is XDM_YUV_422P.

Note:

If the input is non-interleaved, then the output is non-interleaved. If the input is interleaved, then the output also is interleaved.

The output formats supported for each input format are as follows:

Input Format	Supported Output Formats
XDM_YUV_420P	<input type="checkbox"/> XDM_YUV_420P <input type="checkbox"/> XDM_GRAY
XDM_YUV_422P	<input type="checkbox"/> XDM_YUV422P <input type="checkbox"/> XDM_GRAY
XDM_YUV_422ILE	<input type="checkbox"/> XDM_YUV_422P <input type="checkbox"/> XDM_YUV_420P <input type="checkbox"/> XDM_GRAY
XDM_YUV_444P	<input type="checkbox"/> XDM_YUV_444P <input type="checkbox"/> XDM_GRAY
XDM_YUV_411P	<input type="checkbox"/> XDM_YUV_411P <input type="checkbox"/> XDM_GRAY
XDM_GRAY	<input type="checkbox"/> XDM_GRAY

4.2.1.6 IIMGENC1_DynamicParams**|| Description**

This structure defines the run-time parameters for algorithm instance object. Set this data structure to NULL, if you are not sure of the values to be specified for these parameters.

|| Fields

Field	Datatype	Input/Output	Description
size	XDAS_Int32	Input	Size of the basic or extended (if being used) data structure in bytes. Default value: sizeof (IDMJPGE_TIGEM_DynamicParams)

Field	Datatype	Input/ Output	Description
numAU	XDAS_Int32	Input	Number of access units to encode. Setting this field to <code>XDM_DEFAULT</code> encodes the complete frame. Any other value will encode the specified number of access units. Default value: <code>XDM_DEFAULT</code>
inputChromaFormat	XDAS_Int32	Input	Color format of the input data. See <code>XDM_ChromaFormat</code> enumeration for details. Default value: <code>XDM_YUV_422P</code>
inputHeight	XDAS_Int32	Input	Input height of the image Default value: 480
inputWidth	XDAS_Int32	Input	Input width of the image Default value: 720
captureWidth	XDAS_Int32	Input	Total buffer width of the input data. This must be equal to or greater than <code>inputWidth</code> . Default value: <code>XDM_DEFAULT</code>
generateHeader	XDAS_Int32	Input	<input type="checkbox"/> <code>XDM_ENCODE_AU</code> - Encode entire access unit <input type="checkbox"/> <code>XDM_GENERATE_HEADER</code> - Encode only header Default value: <code>XDM_ENCODE_AU</code>
qValue	XDAS_Int32	Input	Q value for encoder. Set to a value between 1 and 100 to define the quantization parameter. <input type="checkbox"/> 1 - lowest quality <input type="checkbox"/> 100 - highest quality Default value: 86

Note:

- To support smaller output buffer, dump the output after every process call, else should increment the output buffer by bytes generated after every process call. To support slice based encoding, set the `NumAu` to the required value.
- For efficient encoding, that is good quality and comparatively good compression, can be achieved by having the quality factor (`qValue`) in the range of 20 to 80.
- Set the appropriate output buffer size to insert application markers.
- To encode the portion of an image, set the `inputWidth` and `inputHeight` to the expected value. Set the `captureWidth` and `captureHeight` to the actual width and height of the image. Setting the `captureWidth` and `captureHeight` other than the actual value, will result in visual artifacts.

4.2.1.7 IIMGENC1_InArgs

|| Description

This structure defines the run-time input arguments for algorithm instance object.

|| Fields

Field	Datatype	Input/ Output	Description
size	XDAS_Int32	Input	Size of the basic or extended (if being used) data structure in bytes.

4.2.1.8 IIMGENC1_Status

|| Description

This structure defines parameters that describe the status of the algorithm instance object.

|| Fields

Field	Datatype	Input/ Output	Description
size	XDAS_Int32	Input	Size of the basic or extended (if being used) data structure in bytes.
extendedError	XDAS_Int32	Output	Extended error enumeration. See <code>XDM_ErrorBit</code> enumeration for details.
totalAU	XDAS_Int32	Output	Total number of iterations for the encoder to encode the current image frame.
bufInfo	XDM_AlgBufInfo	Output	Input and output buffer information. See <code>XDM_AlgBufInfo</code> data structure for details.

4.2.1.9 IIMGENC1_OutArgs

|| Description

This structure defines the run-time output arguments for the algorithm instance object.

|| Fields

Field	Datatype	Input/ Output	Description
size	XDAS_Int32	Input	Size of the basic or extended (if being used) data structure in bytes.
extendedError	XDAS_Int32	Output	Extended error enumeration. See <code>XDM_ErrorBit</code> enumeration for details.
bytesGenerated	XDAS_Int32	Output	Number of bytes generated in the current bit-stream.
currentAU	XDAS_Int32	Output	The current MCU number that the encoder has completed. This can be used to track the encoder status in the case of slice-based encoding.

4.2.2 JPEG Encoder Data Structures

This section includes the following JPEG Encoder specific extended data structures:

- ❑ `IDMJPGE_TIGEM_Params`
- ❑ `IDMJPGE_TIGEM_DynamicParams`
- ❑ `IDMJPGE_TIGEM_InArgs`
- ❑ `IDMJPGE_TIGEM_Status`
- ❑ `IDMJPGE_TIGEM_OutArgs`
- ❑ `IDMJPGE_TIGEM_Fxns`

4.2.2.1 `IDMJPGE_TIGEM_Params`

|| Description

This structure defines the creation parameters and any other implementation specific parameters for the JPEG Encoder instance object. The creation parameters are defined in the XDM data structure, `IIMGENC1_Params`.

|| Fields

Field	Datatype	Input/ Output	Description
<code>params</code>	<code>IIMGENC1_Params</code>	Input	See <code>IIMGENC1_Params</code> data structure for details.
<code>MaxThumbnail_H_size_App0</code>	<code>XDAS_UInt32</code>	Input	Maximum horizontal resolution for APP0 thumbnail
<code>MaxThumbnail_V_size_App0</code>	<code>XDAS_UInt32</code>	Input	Maximum vertical resolution for APP0 thumbnail
<code>MaxThumbnail_H_size_App1</code>	<code>XDAS_UInt32</code>	Input	Maximum horizontal resolution for APP1 thumbnail
<code>MaxThumbnail_V_size_App1</code>	<code>XDAS_UInt32</code>	Input	Maximum vertical resolution for APP1 thumbnail
<code>MaxThumbnail_H_size_App13</code>	<code>XDAS_UInt32</code>	Input	Maximum horizontal resolution for APP13 thumbnail
<code>MaxThumbnail_V_size_App13</code>	<code>XDAS_UInt32</code>	Input	Maximum vertical resolution for APP13 thumbnail

4.2.2.2 *IDMJPGE_TIGEM_DynamicParams*

|| Description

This structure defines the run-time parameters and any other implementation specific parameters for a JPEG Encoder instance object. The run-time parameters are defined in the XDM data structure, `IIMGENC1_DynamicParams`.

|| Fields

Field	Datatype	Input/Output	Description
<code>params</code>	<code>IIMGENC1_DynamicParams</code>	Input	See <code>IIMGENC1_DynamicParams</code> data structure for details.
<code>captureHeight</code>	<code>XDAS_Int32</code>	Input	If set to 0 , input height is equal to capture height , else capture height should be set to actual image height.
<code>quantTable</code>	<code>IDMJPGE_TIGEM_CustomQuantTables *</code>	Input	To support custom Quantization Table
<code>DRI_Interval</code>	<code>XDAS_Int32</code>	Input	<input type="checkbox"/> 0 - DRI Marker is not inserted <input type="checkbox"/> Other than 0 -Insert DRI marker at regular interval in the compressed bit-stream.
<code>APPN0_numBufs</code>	<code>XDAS_UInt32</code>	Input	Number of APP0 buffers
<code>APPN1_numBufs</code>	<code>XDAS_UInt32</code>	Input	Number of APP1 buffers
<code>APPN13_numbufs</code>	<code>XDAS_UInt32</code>	Input	Number of APP13 buffers
<code>APPN0_startBuf</code>	<code>XDAS_UInt32</code>	Input	Start buffer number in inbufs for APP0 buffers
<code>APPN1_startBuf</code>	<code>XDAS_UInt32</code>	Input	Start buffer number in inbufs for APP1 buffers
<code>APPN13_startBuf</code>	<code>XDAS_UInt32</code>	Input	Start buffer number in inbufs for APP13 buffers
<code>COMMENT_startBuf</code>	<code>XDAS_UInt32</code>	Input	Start buffer number in inbufs for Comment buffer

Field	Datatype	Input/ Output	Description
COMMENT_insert	XDAS_UInt32	Input	Specifies whether to insert comment or not
Thumbnail_Index_Ap p0	XDAS_UInt32	Input	<input type="checkbox"/> 0 - Thumbnail not added in APP0 data <input type="checkbox"/> 1 - Thumbnail appended in 1st APP0 data <input type="checkbox"/> 2 - Thumbnail appended in 2nd APP0 data The maximum value is 16.
Thumbnail_Index_Ap p1	XDAS_UInt32	Input	<input type="checkbox"/> 0 - Thumbnail not added in APP1 data <input type="checkbox"/> 1 - Thumbnail appended in 1st APP1 data <input type="checkbox"/> 2 - Thumbnail appended in 2nd APP1 data The maximum value is 16.
Thumbnail_Index_Ap p13	XDAS_UInt32	Input	<input type="checkbox"/> 0- Thumbnail not added in APP13 data <input type="checkbox"/> 1 - Thumbnail appended in 1st APP13 data <input type="checkbox"/> 2 - Thumbnail appended in 2nd APP13 data The maximum value is 16.
Thumbnail_H_size_A pp0	XDAS_UInt32	Input	Horizontal resolution for APP0 thumbnail
Thumbnail_V_size_A pp0	XDAS_UInt32	Input	Vertical resolution for APP0 thumbnail
Thumbnail_H_size_A pp1	XDAS_UInt32	Input	Horizontal resolution for APP1 thumbnail
Thumbnail_V_size_A pp1	XDAS_UInt32	Input	Vertical resolution for APP1 thumbnail
Thumbnail_H_size_A pp13	XDAS_UInt32	Input	Horizontal resolution for APP13 thumbnail
Thumbnail_V_size_A pp13	XDAS_UInt32	Input	Vertical resolution for APP13 thumbnail

Note:

- Input Width, Input Height, Thumbnail Width, and Thumbnail Height should be in multiples of the value mentioned in the following table to have the sufficient data to encode. If not, data present in the padded buffer will be taken to encode and bit-exactness will not be assured for the same, as the data present in the padded buffer will

not be the same always.

Input Format	Multiplication Factor	
	Width	Height
XDM_YUV_420P	16	16
XDM_YUV_422P	16	8
XDM_YUV_422ILE	16	8
XDM_YUV_444P	8	8
XDM_YUV_411P	32	8
XDM_GRAY	8	8

- Thumbnail resolution should be less than Image resolution.
- Minimum resolution of thumbnail is 16x16.
- Odd thumbnail resolutions is not supported.

4.2.2.3 *IDMJPGE_TIGEM_CustomQuantTables*

|| Description

This structure defines the format for custom Quantization tables for both Luma and Chroma that needs to be passed to the JPEG Encoder and will be used in the Quantization Module. The format is as specified in RFC 2035 (RTP Payload Format for JPEG-compressed Video) and in Table K.1 and K.2 of JPEG Spec.

The `IDMJPGE_TIGEM_DynamicParams` structure contains a pointer to the structure type `IDMJPGE_TIGEM_CustomQuantTables`. This field is used to pass custom Quantization tables to the JPEG Encoder. The application has to initialize this field appropriately. However, if the application needs to use the Standard Quantization Tables, there are two ways of implementing this in JPEG Encoder:

- Using only the base class `IIMGENC1_DynamicParams` and not the extended class `IDMJPGE_TIGEM_DynamicParams`. In this case, the `size` field in the `DynamicParams` structure should be set to the size of `IIMGENC1_DynamicParams`.
- Using the extended class `IDMJPGE_TIGEM_DynamicParams`, setting the field `IDMJPGE_TIGEM_CustomQuantTables *quantTable` to `NULL`.

|| Fields

Field	Datatype	Input/Output	Description
<code>lum_quant_tab[64]</code>	<code>XDAS_UInt16</code>	Input	The array <code>lum_quant_tab</code> defines the quantization table for the luma component.
<code>chm_quant_tab[64]</code>	<code>XDAS_UInt16</code>	Input	The array <code>chm_quant_tab</code> defines the quantization table for the chroma component.

The Standard Quantization Table is provided in Appendix A as an example initialization of the above.

4.2.2.4 IDMJPGE_TIGEM_InArgs**|| Description**

This structure defines the run-time input arguments for JPEG Encoder instance object.

|| Fields

Field	Datatype	Input/ Output	Description
inArgs	IIMGENC1_In Args	Input	See IIMGENC1_InArgs data structure for details.

4.2.2.5 IDMJPGE_TIGEM_Status**|| Description**

This structure defines parameters that describe the status of the JPEG Encoder and any other implementation specific parameters. The status parameters are defined in the XDM data structure, IIMGENC1_Status.

|| Fields

Field	Datatype	Input/ Output	Description
status	IIMGENC1_Status	Output	See IIMGENC1_Status data structure for details.

4.2.2.6 IDMJPGE_TIGEM_OutArgs**|| Description**

This structure defines the run-time output arguments for the JPEG Encoder instance object.

|| Fields

Field	Datatype	Input/ Output	Description
outArgs	IIMGENC1_OutArg s	Output	See IIMGENC1_OutArgs data structure for details.

4.2.2.7 *IDMJPGE_TIGEM_Fxns*

|| Description

This structure defines all the operations on the JPEG Encoder instance object.

|| Fields

Field	Datatype	Input/ Output	Description
<code>iimgenc1</code>	<code>IIMGENC1_Fxns</code>	Input	See <code>IIMGENC1_Fxns</code> data structure for details.

4.3 Interface Functions

This section describes the Application Programming Interfaces (APIs) used in the JPEG Encoder. The APIs are logically grouped into the following categories:

- ❑ **Creation** – `algNumAlloc()`, `algAlloc()`
- ❑ **Initialization** – `algInit()`
- ❑ **Control** – `control()`
- ❑ **Data processing** – `algActivate()`, `process()`, `algDeactivate()`
- ❑ **Termination** – `algFree()`

You must call these APIs in the following sequence:

- 1) `algNumAlloc()`
- 2) `algAlloc()`
- 3) `algInit()`
- 4) `algActivate()`
- 5) `process()`
- 6) `algDeactivate()`
- 7) `algFree()`

`control()` can be called any time after calling the `algInit()` API.

`algNumAlloc()`, `algAlloc()`, `algInit()`, `algActivate()`, `algDeactivate()`, and `algFree()` are standard XDAIS APIs. This document includes only a brief description for the standard XDAIS APIs. For more details, see *TMS320 DSP Algorithm Standard API Reference* (literature number SPRU360).

4.3.1 Creation APIs

Creation APIs are used to create an instance of the component. The term creation could mean allocating system resources, typically memory.

|| Name

`algNumAlloc()` – determine the number of buffers that an algorithm requires

|| Synopsis

```
XDAS_Int32 algNumAlloc(Void);
```

|| Arguments

Void

|| Return Value

```
XDAS_Int32; /* number of buffers required */
```

|| Description

`algNumAlloc()` returns the number of buffers that the `algAlloc()` method requires. This operation allows you to allocate sufficient space to call the `algAlloc()` method.

`algNumAlloc()` may be called at any time and can be called repeatedly without any side effects. It always returns the same result. The `algNumAlloc()` API is optional.

For more details, see *TMS320 DSP Algorithm Standard API Reference* (literature number SPRU360).

|| See Also

`algAlloc()`

|| Name

`algAlloc()` – determine the attributes of all buffers that an algorithm requires

|| Synopsis

```
XDAS_Int32 algAlloc(const IALG_Params *params, IALG_Fxns
**parentFxns, IALG_MemRec memTab[]);
```

|| Arguments

```
IALG_Params *params; /* algorithm specific attributes */
```

```
IALG_Fxns **parentFxns; /* output parent algorithm
functions */
```

```
IALG_MemRec memTab[]; /* output array of memory records */
```

|| Return Value

```
XDAS_Int32 /* number of buffers required */
```

|| Description

`algAlloc()` returns a table of memory records that describe the size, alignment, type, and memory space of all buffers required by an algorithm. If successful, this function returns a positive non-zero value indicating the number of records initialized.

The first argument to `algAlloc()` is a pointer to a structure that defines the creation parameters. This pointer may be `NULL`; however, in this case, `algAlloc()` must assume default creation parameters and must not fail.

The second argument to `algAlloc()` is an output parameter. `algAlloc()` may return a pointer to its parent's IALG functions. If an algorithm does not require a parent object to be created, this pointer must be set to `NULL`.

The third argument is a pointer to a memory space of size `nbufs * sizeof(IALG_MemRec)` where, `nbufs` is the number of buffers returned by `algNumAlloc()` and `IALG_MemRec` is the buffer-descriptor structure defined in `ialg.h`.

After calling this function, `memTab[]` is filled up with the memory requirements of an algorithm.

For more details, see *TMS320 DSP Algorithm Standard API Reference* (literature number SPRU360).

|| See Also

```
algNumAlloc(), algFree()
```

4.3.2 Initialization API

Initialization API is used to initialize an instance of the algorithm. The initialization parameters are defined in the `Params` structure (see Data Structures section for details).

|| Name

algInit() – initialize an algorithm instance

|| Synopsis

```
XDAS_Int32 algInit(IALG_Handle handle, IALG_MemRec memTab[], IALG_Handle parent, IALG_Params *params);
```

|| Arguments

```
IALG_Handle handle; /* algorithm instance handle*/  
IALG_memRec memTab[]; /* array of allocated buffers */  
IALG_Handle parent; /* handle to the parent instance */  
IALG_Params *params; /* algorithm initialization parameters */
```

|| Return Value

```
IALG_EOK; /* status indicating success */  
IALG_EFAIL; /* status indicating failure */
```

|| Description

algInit() performs all initialization necessary to complete the run-time creation of an algorithm instance object. After a successful return from algInit(), the instance object is ready to be used to process data.

The first argument to algInit() is a handle to an algorithm instance. This value is initialized to the base field of memTab[0].

The second argument is a table of memory records that describe the base address, size, alignment, type, and memory space of all buffers allocated for an algorithm instance. The number of initialized records is identical to the number returned by a prior call to algAlloc().

The third argument is a handle to the parent instance object. If there is no parent object, this parameter must be set to NULL.

The last argument is a pointer to a structure that defines the algorithm initialization parameters.

For more details, see *TMS320 DSP Algorithm Standard API Reference* (literature number SPRU360).

|| See Also

algAlloc(), algMoved()

4.3.3 Control API

Control API is used for controlling the functioning of the algorithm instance during run-time. This is done by changing the status of the controllable parameters of the algorithm during run-time. These controllable parameters are defined in the Status data structure (see Data Structures section for details).

|| Name

`control()` – change run-time parameters and query the status

|| Synopsis

```
XDAS_Int32 (*control) (IIMGENC1_Handle handle,
IIMGENC1_Cmd id, IIMGENC1_DynamicParams *params,
IIMGENC1_Status *status);
```

|| Arguments

```
IIMGENC1_Handle handle; /* algorithm instance handle */
IIMGENC1_Cmd id; /* algorithm specific control commands*/
IIMGENC1_DynamicParams *params /* algorithm run-time parameters
*/
IIMGENC1_Status *status /* algorithm instance status parameters */
```

|| Return Value

```
IALG_EOK; /* status indicating success */
IALG_EFAIL; /* status indicating failure */
```

|| Description

This function changes the run-time parameters of an algorithm instance and queries the algorithm's status. `control()` must only be called after a successful call to `algInit()` and must never be called after a call to `algFree()`.

The first argument to `control()` is a handle to an algorithm instance.

The second argument is an algorithm specific control command. See `XDM_CmdId` enumeration for details.

The third and fourth arguments are pointers to the `IIMGENC1_DynamicParams` and `IIMGENC1_Status` data structures respectively.

Note:

If you are using extended data structures, the third and fourth arguments must be pointers to the extended `DynamicParams` and `Status` data structures respectively. Also, ensure that the `size` field is set to the size of the extended data structure. Depending on the value set for the `size` field, the algorithm uses either basic or extended parameters.

|| Preconditions

The following conditions must be true prior to calling this function; otherwise, its operation is undefined.

- ❑ `control()` can only be called after a successful return from `algInit()` and `algActivate()`.
- ❑ If algorithm uses DMA resources, `control()` can only be called after a successful return from `DMAN3_init()`.
- ❑ `handle` must be a valid handle for the algorithm's instance object.

|| Postconditions

The following conditions are true immediately after returning from this function.

- ❑ If the control operation is successful, the return value from this operation is equal to `IALG_EOK`; otherwise, it is equal to either `IALG_EFAIL` or an algorithm specific return value.
- ❑ If the control command is not recognized, the return value from this operation is not equal to `IALG_EOK`.

|| Example

See test application file, `TestAppEncoder.c` available in the `\Client\Test\Src` sub-directory.

|| See Also

`algInit()`, `algActivate()`, `process()`

4.3.4 Data Processing API

Name	Data processing API is used for processing the input data.
Synopsis	<code>algActivate()</code> – initialize scratch memory buffers prior to processing.
Arguments	<code>Void algActivate(IALG_Handle handle);</code>
Return Value	<code>IALG_Handle handle; /* algorithm instance handle */</code>
Description	<p><code>Void</code></p> <p><code>algActivate()</code> initializes any of the instance's scratch buffers using the persistent memory that is part of the algorithm's instance object.</p> <p>The first (and only) argument to <code>algActivate()</code> is an algorithm instance handle. This handle is used by the algorithm to identify various buffers that must be initialized prior to calling any of the algorithm's processing methods.</p> <p>For more details, see <i>TMS320 DSP Algorithm Standard API Reference</i>. (literature number SPRU360).</p>
See Also	<code>algDeactivate()</code>

|| Name

`process()` – basic encoding/decoding call

|| Synopsis

```
XDAS_Int32 (*process)(IIMGENC1_Handle handle, XDML_BufDesc
*inBufs, XDML_BufDesc *outBufs, IIMGENC1_InArgs *inargs,
IIMGENC1_OutArgs *outargs);
```

|| Arguments

```
IIMGENC1_Handle handle; /* algorithm instance handle */
XDML_BufDesc *inBufs; /* algorithm input buffer descriptor
*/
XDML_BufDesc *outBufs; /* algorithm output buffer
descriptor */
IIMGENC1_InArgs *inargs /* algorithm runtime input
arguments */
IIMGENC1_OutArgs *outargs /* algorithm runtime output
arguments */
```

|| Return Value

```
IALG_EOK; /* status indicating success */
IALG_EFAIL; /* status indicating failure */
```

|| Description

This function does the basic encoding/decoding. The first argument to `process()` is a handle to an algorithm instance.

The second and third arguments are pointers to the input and output buffer descriptor data structures respectively (see `XDML_BufDesc` data structure for details).

The fourth argument is a pointer to the `IIMGENC1_InArgs` data structure that defines the run-time input arguments for an algorithm instance object.

The last argument is a pointer to the `IIMGENC1_OutArgs` data structure that defines the run-time output arguments for an algorithm instance object.

Note:

If you are using extended data structures, the fourth and fifth arguments must be pointers to the extended `InArgs` and `OutArgs` data structures respectively. Also, ensure that the `size` field is set to the size of the extended data structure. Depending on the value set for the `size` field, the algorithm uses either basic or extended parameters.

|| Preconditions

The following conditions must be true prior to calling this function; otherwise, its operation is undefined.

- ❑ `process()` can only be called after a successful return from `algInit()` and `algActivate()`.

- ❑ If algorithm uses DMA resources, `process()` can only be called after a successful return from `DMAN3_init()`.
- ❑ `handle` must be a valid handle for the algorithm's instance object.
- ❑ Buffer descriptor for input and output buffers must be valid.
- ❑ Input buffers must have valid input data.

|| Postconditions

The following conditions are true immediately after returning from this function.

- ❑ If the process operation is successful, the return value from this operation is equal to `IALG_EOK`; otherwise it is equal to either `IALG_EFAIL` or an algorithm specific return value.
- ❑ After successful return from `process()` function, `algDeactivate()` can be called.

|| Example

See test application file, `TestAppEncoder.c` available in the `\Client\Test\Src` sub-directory.

|| See Also

`algInit()`, `algDeactivate()`, `control()`

|| Name

`algDeactivate()` – save all persistent data to non-scratch memory

|| Synopsis

```
Void algDeactivate(IALG_Handle handle);
```

|| Arguments

```
IALG_Handle handle; /* algorithm instance handle */
```

|| Return Value

Void

|| Description

`algDeactivate()` saves any persistent information to non-scratch buffers using the persistent memory that is part of the algorithm's instance object.

The first (and only) argument to `algDeactivate()` is an algorithm instance handle. This handle is used by the algorithm to identify various buffers that must be saved prior to next cycle of `algActivate()` and processing.

For more details, see *TMS320 DSP Algorithm Standard API Reference* (literature number SPRU360).

|| See Also

`algActivate()`

4.3.5 Termination API

Termination API is used to terminate the algorithm instance and free up the memory space that it uses.

|| Name

`algFree()` – determine the addresses of all memory buffers used by the algorithm

|| Synopsis

```
XDAS_Int32 algFree(IALG_Handle handle, IALG_MemRec memTab[]);
```

|| Arguments

```
IALG_Handle handle; /* handle to the algorithm instance */  
IALG_MemRec memTab[]; /* output array of memory records */
```

|| Return Value

```
XDAS_Int32; /* Number of buffers used by the algorithm */
```

|| Description

`algFree()` determines the addresses of all memory buffers used by the algorithm. The primary aim of doing so is to free up these memory regions after closing an instance of the algorithm.

The first argument to `algFree()` is a handle to the algorithm instance.

The second argument is a table of memory records that describe the base address, size, alignment, type, and memory space of all buffers previously allocated for the algorithm instance.

For more details, see *TMS320 DSP Algorithm Standard API Reference* (literature number SPRU360).

|| See Also

`algAlloc()`

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Standard Quantization Table

This section provides an example for initialization of a structure of type `IDMJPGE_TIGEM_CustomQuantTables` using the standard Quantization Table.

The sample code provided in this section is an example for initialization of a structure of type `IDMJPGE_TIGEM_CustomQuantTables` using the Standard Quantization Table.

Table A-1. Standard Quantization Table

```
const IDMJPGE_TIGEM_CustomQuantTables
JPEGNC_StandardQuantTable= {

    /* LUMA (Y) */

    16, 11, 10, 16, 24, 40, 51, 61,
    12, 12, 14, 19, 26, 58, 60, 55,
    14, 13, 16, 24, 40, 57, 69, 56,
    14, 17, 22, 29, 51, 87, 80, 62,
    18, 22, 37, 56, 68, 109, 103, 77,
    24, 35, 55, 64, 81, 104, 113, 92,
    49, 64, 78, 87, 103, 121, 120, 101,
    72, 92, 95, 98, 112, 100, 103, 99,

    /* CHROMA (Cb Cr) */

    17, 18, 24, 47, 99, 99, 99, 99,
    18, 21, 26, 66, 99, 99, 99, 99,
    24, 26, 56, 99, 99, 99, 99, 99,
    47, 66, 99, 99, 99, 99, 99, 99,
    99, 99, 99, 99, 99, 99, 99, 99,
    99, 99, 99, 99, 99, 99, 99, 99,
    99, 99, 99, 99, 99, 99, 99, 99,
    99, 99, 99, 99, 99, 99, 99, 99

};
```

