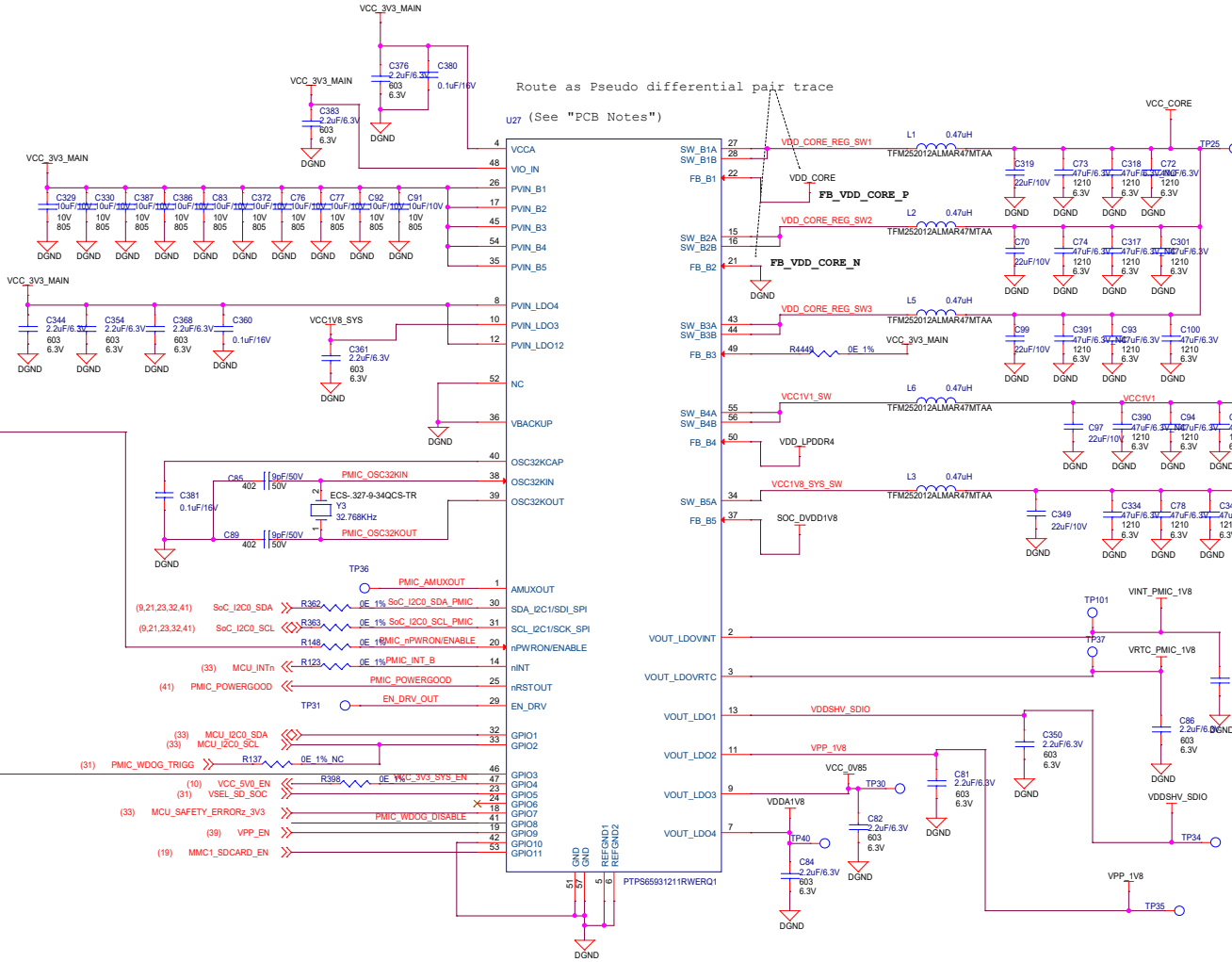
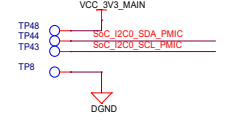


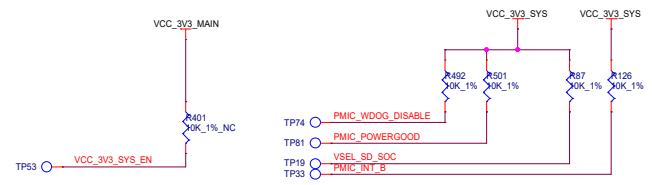
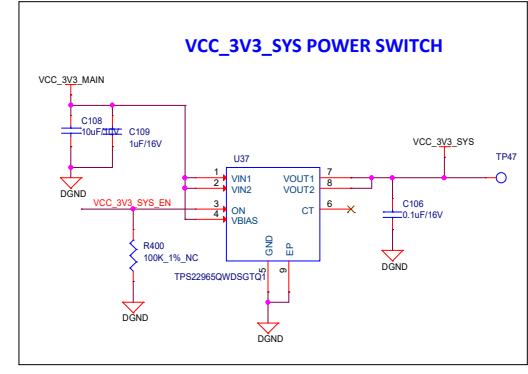
SOC POWER SUPPLY PMIC

PMIC Config option



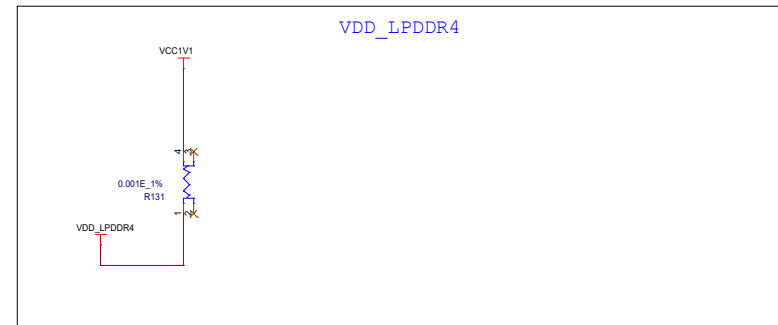
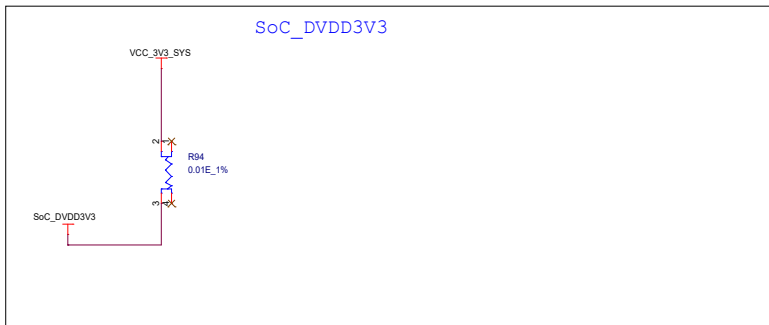
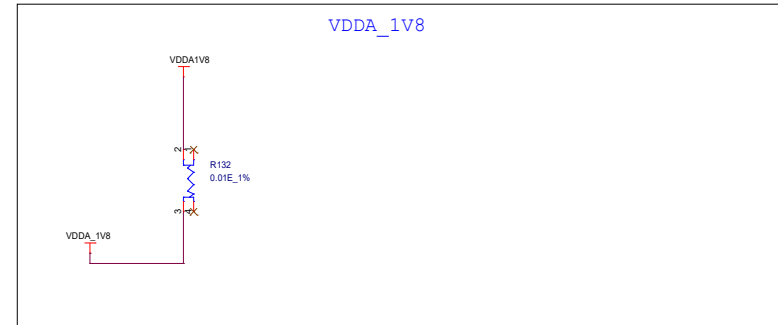
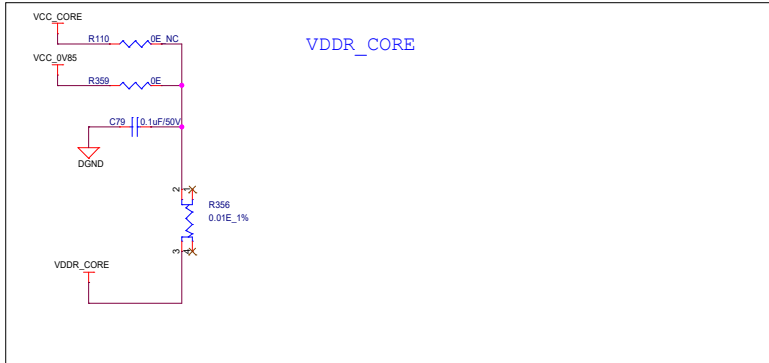
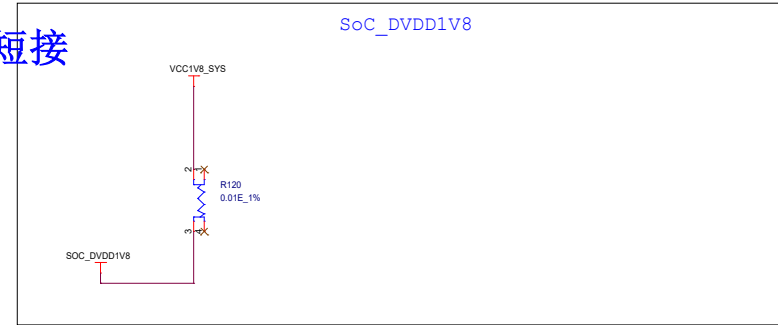
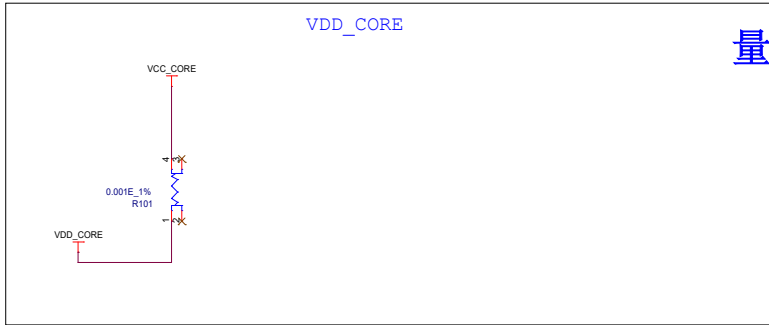
Capacitors C318, C317, C391, C390 & C340 are marked DNP to meet PMIC output cap specifications

PMIC uses default I2C1 ADDR: 0x48,0x49, 0x5A, 0x5B



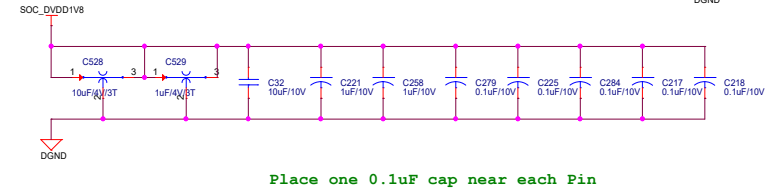
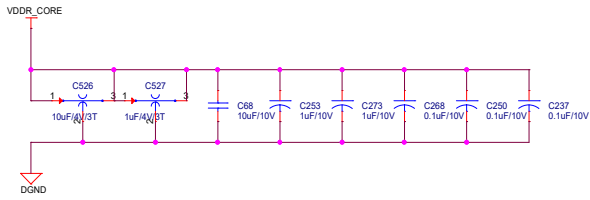
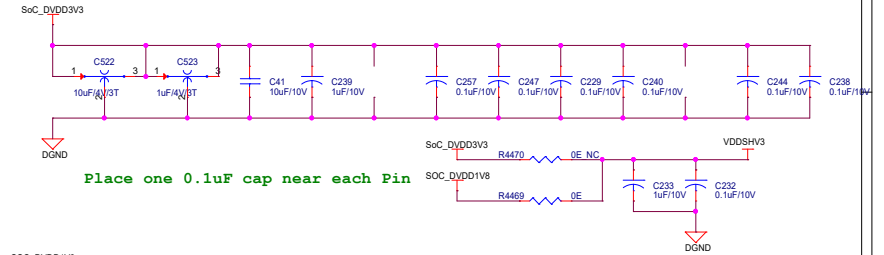
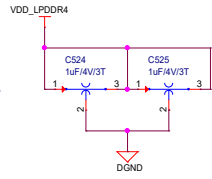
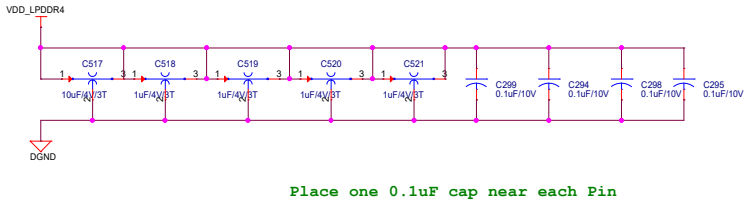
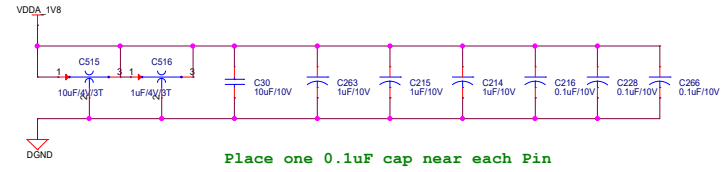
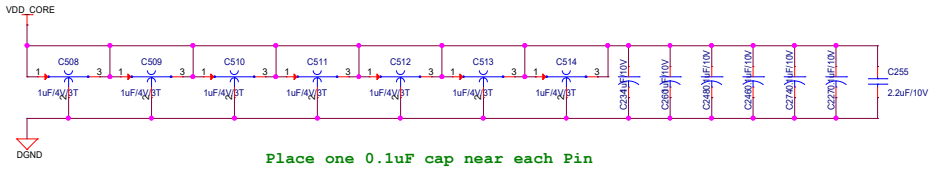
CURRENT MONITORING DEVICES

量产时该部分都直接短接



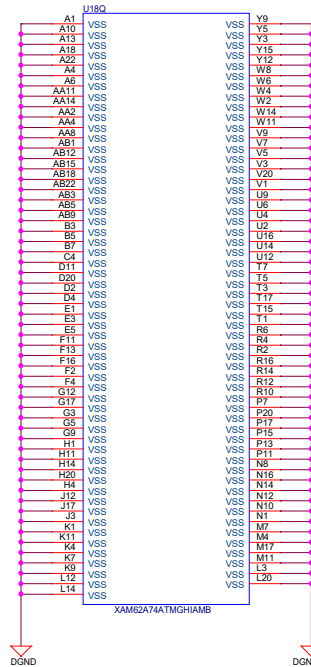
POWER SOURCE	SUPPLY NET	
VCC_CORE	VDD_CORE	
VCC_OV85	VDDR_CORE	
VCC_3V3_SYS	SoC_DVDD3V3	
VCC_1V8	SoC_DVDD1V8	
VDDA1V8	VDDA_1V8	
VCC1V1	VDD_LPDDR4	

SOC POWER DECAPS



SOC VSS

Place one 0.1uF cap near each Pin



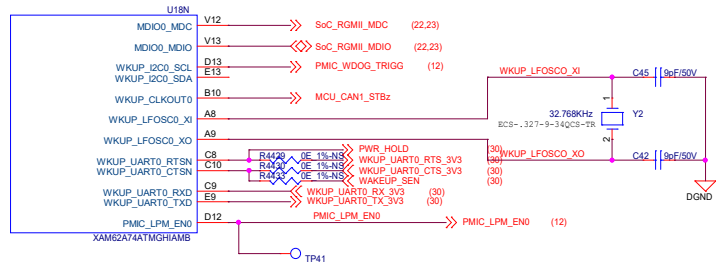
Designed for TI by Mistral Solutions Pvt Ltd



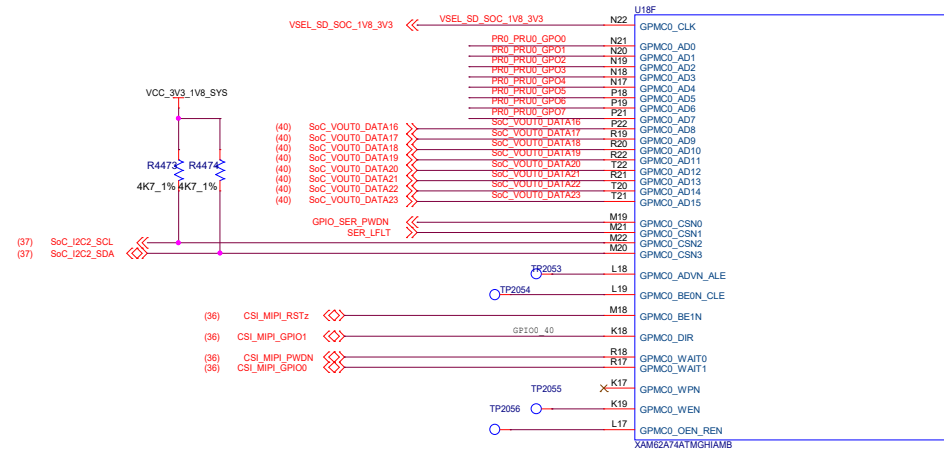
Title: SOC POWER CAPS & SOC VSS

Size		Rev	
C	PROC13E2_SCH		
Date:	Friday, July 07, 2023	Sheet	15 of 44

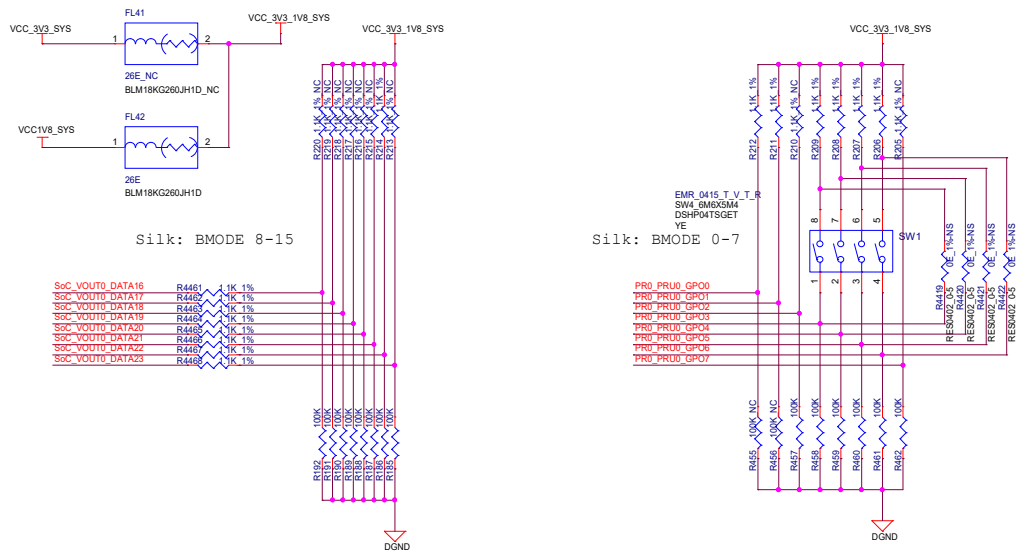
SOC WKUP DOMAIN



SOC GPMC



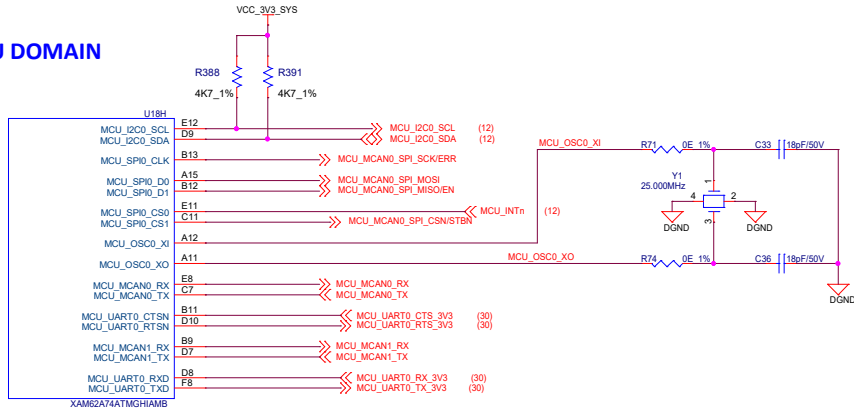
BOOT MODE SWITCHES



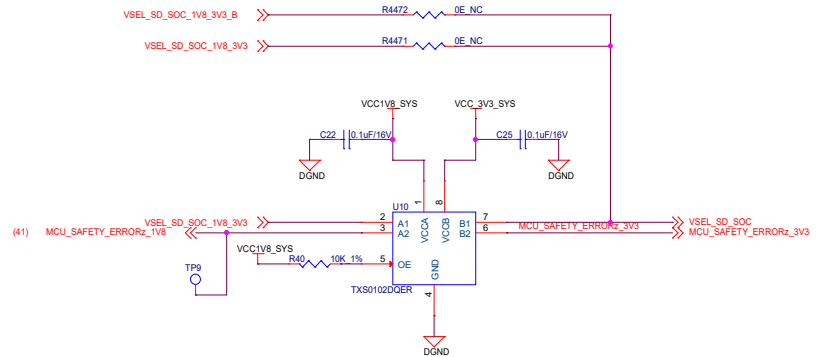
BOOT MODES SUPPORTED

1. OSPI
2. MMC1 - SD CARD
3. UART
4. eMMC
5. ETHERNET
6. USB0 DFU
7. USB0 MS

SOC - MCU DOMAIN



MCU_UART0 MUX



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Title: MCU HEADER

Size	PROC135E2_SCH	Rev	E2
Date:	Friday, July 07, 2023	Sheet	33 of 44

HDMI INTERFACE

U18M	U22	SoC_VOUT0_DATA0
VOUT0_DATA0	U21	SoC_VOUT0_DATA1
VOUT0_DATA1	U20	SoC_VOUT0_DATA2
VOUT0_DATA2	U19	SoC_VOUT0_DATA3
VOUT0_DATA3	U18	SoC_VOUT0_DATA4
VOUT0_DATA4	U17	SoC_VOUT0_DATA5
VOUT0_DATA5	U16	SoC_VOUT0_DATA6
VOUT0_DATA6	U15	SoC_VOUT0_DATA7
VOUT0_DATA7	U14	SoC_VOUT0_DATA8
VOUT0_DATA8	U13	SoC_VOUT0_DATA9
VOUT0_DATA9	U12	SoC_VOUT0_DATA10
VOUT0_DATA10	U11	SoC_VOUT0_DATA11
VOUT0_DATA11	U10	SoC_VOUT0_DATA12
VOUT0_DATA12	U9	SoC_VOUT0_DATA13
VOUT0_DATA13	U8	SoC_VOUT0_DATA14
VOUT0_DATA14	U7	SoC_VOUT0_DATA15
VOUT0_DATA15	U6	SoC_VOUT0_DATA16
VOUT0_PCLK	AA22	SoC_VOUT0_PCLK
VOUT0_DE	U17	SoC_VOUT0_DE
VOUT0_VSYNC	V17	SoC_VOUT0_VSYNC
VOUT0_HSYNC	T18	SoC_VOUT0_HSYNC

XAM82A74T1MGH1AMB

