

MCASP (1 of 3 Added) ADD REMOVE ALL

AUDIO_MCASP1

Name: **AUDIO_MCASP1**

Use Peripheral: **MCASP1**

Preferred Voltage: **Any**

Use Case: **Synchronous (CLKR / FSR internally generated)**

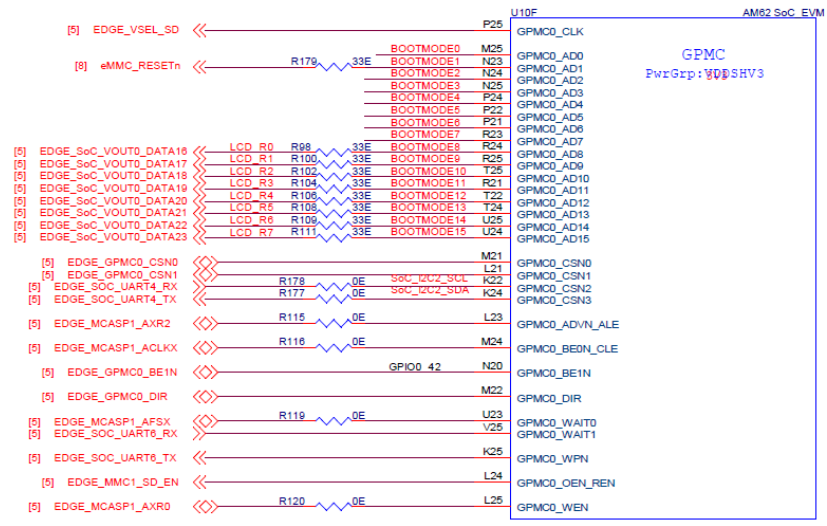
IO Set: **MCASP1_IOSet_4**

Signals	Pins	Pull Up/Down	Rx
<input type="checkbox"/> ACLKR	Any	No Pull	<input checked="" type="checkbox"/>
<input checked="" type="checkbox"/> ACLKX(MCASP1_ACLKX)	Any(GPMC0_BE0n_CLE/M24)	No Pull	<input checked="" type="checkbox"/>
<input checked="" type="checkbox"/> AFSX(MCASP1_AFSX)	Any(GPMC0_WAIT0/U23)	No Pull	<input checked="" type="checkbox"/>
Any 1 of the following pins:			
<input checked="" type="checkbox"/> AXR0(MCASP1_AXR0)	GPMC0_WEn/L25	No Pull	<input checked="" type="checkbox"/>
<input checked="" type="checkbox"/> AXR1	Any	No Pull	<input checked="" type="checkbox"/>
<input checked="" type="checkbox"/> AXR2	GPMC0_ADVn_ALE/L23	No Pull	<input checked="" type="checkbox"/>
<input checked="" type="checkbox"/> AXR3	Any	No Pull	<input checked="" type="checkbox"/>
<input checked="" type="checkbox"/> AXR4	Any	No Pull	<input checked="" type="checkbox"/>
<input checked="" type="checkbox"/> AXR5	Any	No Pull	<input checked="" type="checkbox"/>
<input checked="" type="checkbox"/> AXR6	Any	No Pull	<input checked="" type="checkbox"/>
<input checked="" type="checkbox"/> AXR7	Any	No Pull	<input checked="" type="checkbox"/>
<input checked="" type="checkbox"/> AXR8	Any	No Pull	<input checked="" type="checkbox"/>

```

devicetree.dts
47+ maini2c1_pins_default: maini2c1-default-pins {
48+     pinctrl-single,pins = <
49+         AM62X_IOPAD(0x01e8, PIN_INPUT_PULLUP, 0) /* (B17) I2C1_SCL */
50+         AM62X_IOPAD(0x01ec, PIN_INPUT_PULLUP, 0) /* (A17) I2C1_SDA */
51+     >;
52+ };
53+ maini2c0_pins_default: maini2c0-default-pins {
54+     pinctrl-single,pins = <
55+         AM62X_IOPAD(0x01e0, PIN_INPUT_PULLUP, 0) /* (B16) I2C0_SCL */
56+         AM62X_IOPAD(0x01e4, PIN_INPUT_PULLUP, 0) /* (A16) I2C0_SDA */
57+     >;
58+ };
59+ mainmcan0_pins_default: mainmcan0-default-pins {
60+     pinctrl-single,pins = <
61+         AM62X_IOPAD(0x01dc, PIN_INPUT, 0) /* (E15) MCAN0_RX */
62+         AM62X_IOPAD(0x01d8, PIN_OUTPUT, 0) /* (C15) MCAN0_TX */
63+     >;
64+ };
65+ audiomcaspi_pins_default: audiomcaspi-default-pins {
66+     pinctrl-single,pins = <
67+         AM62X_IOPAD(0x0090, PIN_INPUT, 2) /* (M24) GPMC0_BE0n_CLE.MCASP1_ACLKX */
68+         AM62X_IOPAD(0x0098, PIN_INPUT, 2) /* (U23) GPMC0_WAIT0.MCASP1_AFSX */
69+         AM62X_IOPAD(0x008c, PIN_INPUT, 2) /* (L25) GPMC0_WEn.MCASP1_AXR0 */
70+     >;
71+ };
72+ mainuart4_pins_default: mainuart4-default-pins {
73+     pinctrl-single,pins = <
74+         AM62X_IOPAD(0x00b0, PIN_INPUT, 3) /* (K22) GPMC0_CSn2.UART4_RXD */
75+         AM62X_IOPAD(0x00b4, PIN_OUTPUT, 3) /* (K24) GPMC0_CSn3.UART4_TXD */
76+     >;
77+ };
  
```

SOC GPMC



Audio AMP

(Class-D 2.6W Mono)

