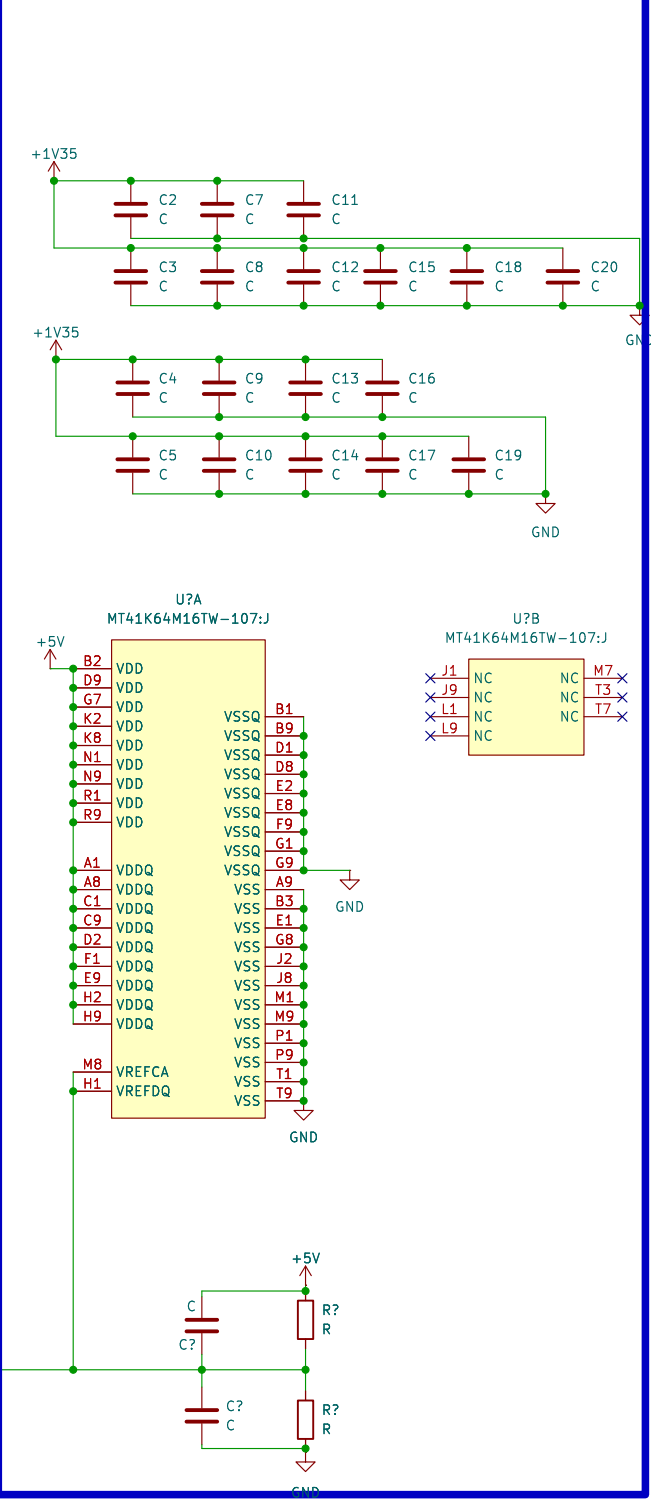
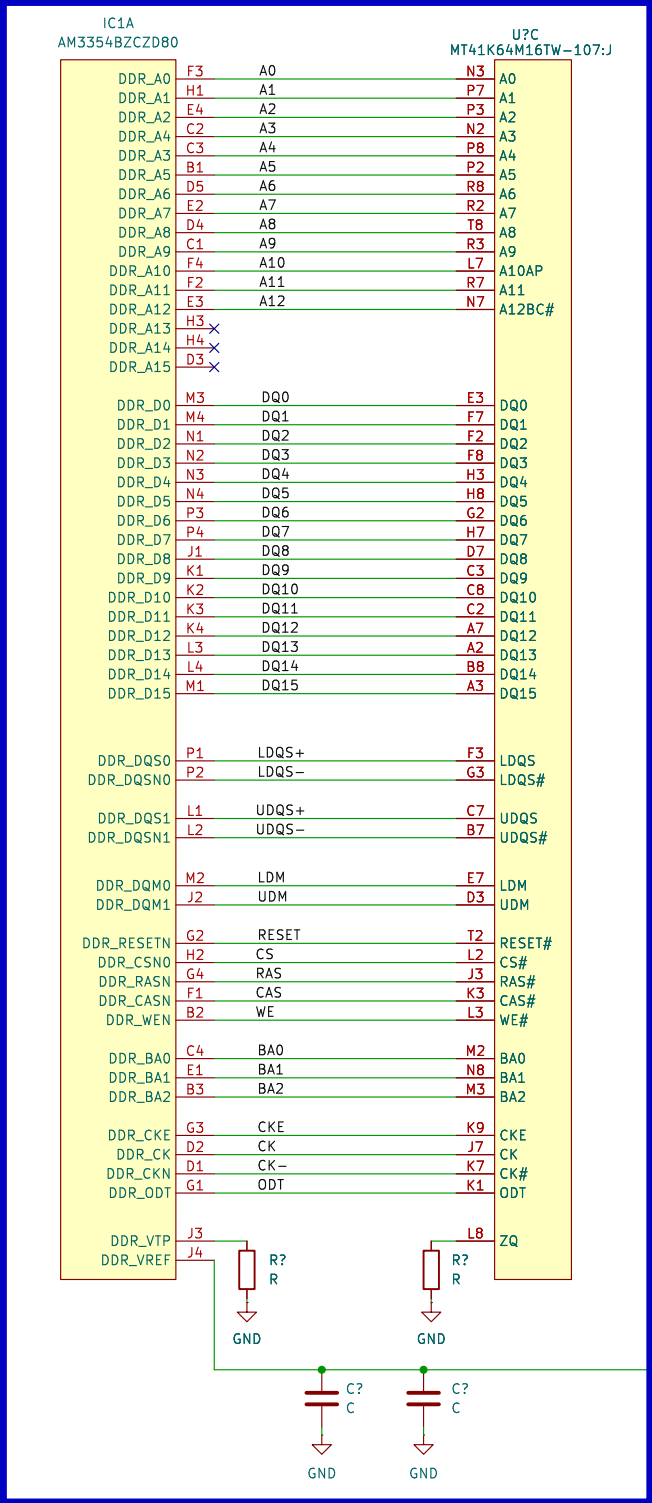


	1	2	3	4	5	6	7	8
A	DDR3 File: DDR3.kicad_sch	Ports File: Ports.kicad_sch	Power File: Power.kicad_sch					
B								
C								
D								
E								
F								

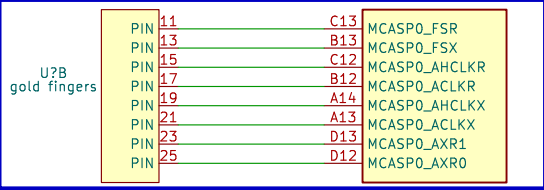
Sheet: / File: MRTB.kicad_sch		
Title:		
Size: A3	Date:	Rev:
KiCad E.D.A. 9.0.7	Id: 1/4	

Am3354 to DDR3L

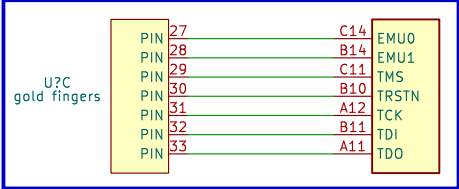
DDR3L Power



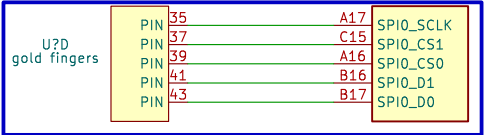
Audio



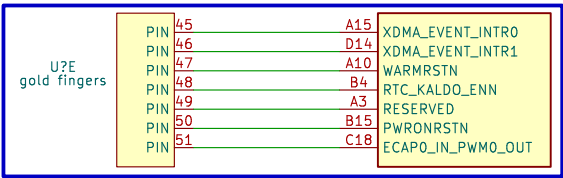
JTAG



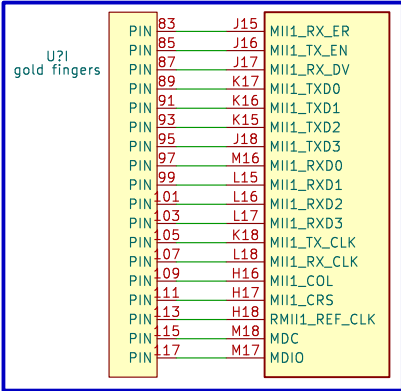
SPI



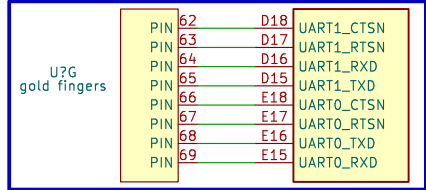
REST



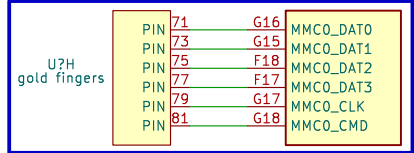
ETHERNET/10/100/1000



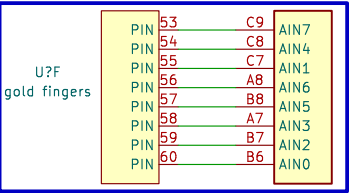
UART



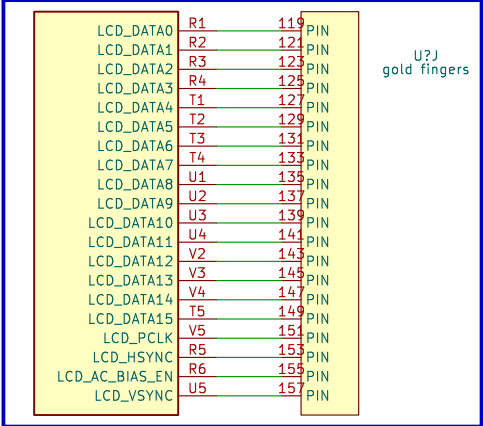
RMI



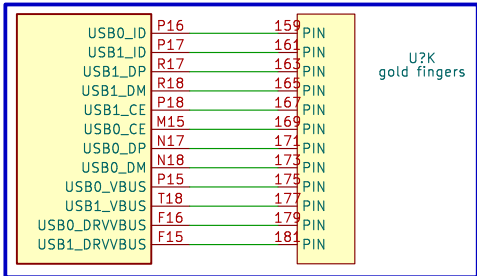
ADC



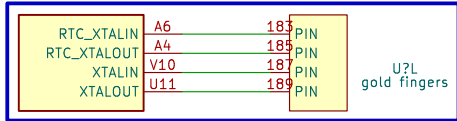
LCD



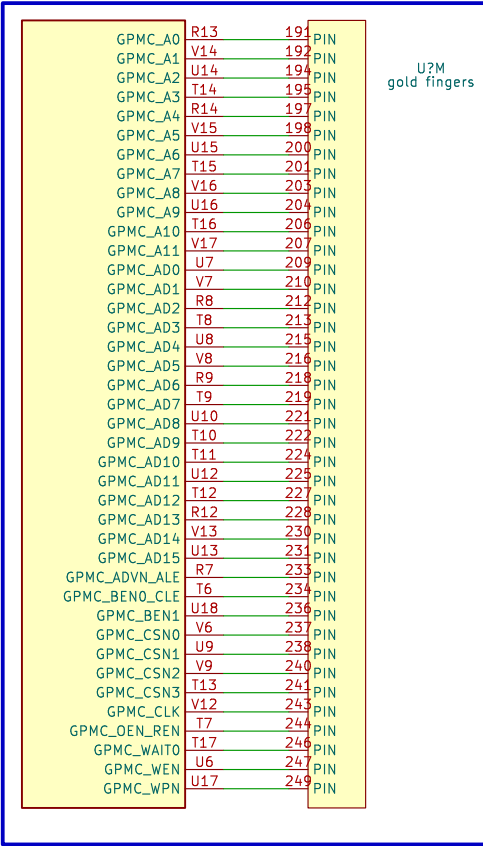
USB



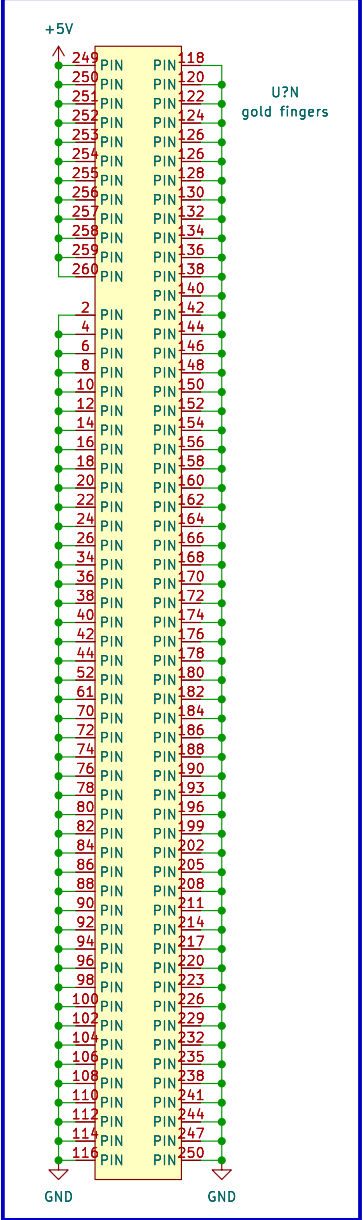
CLOCK



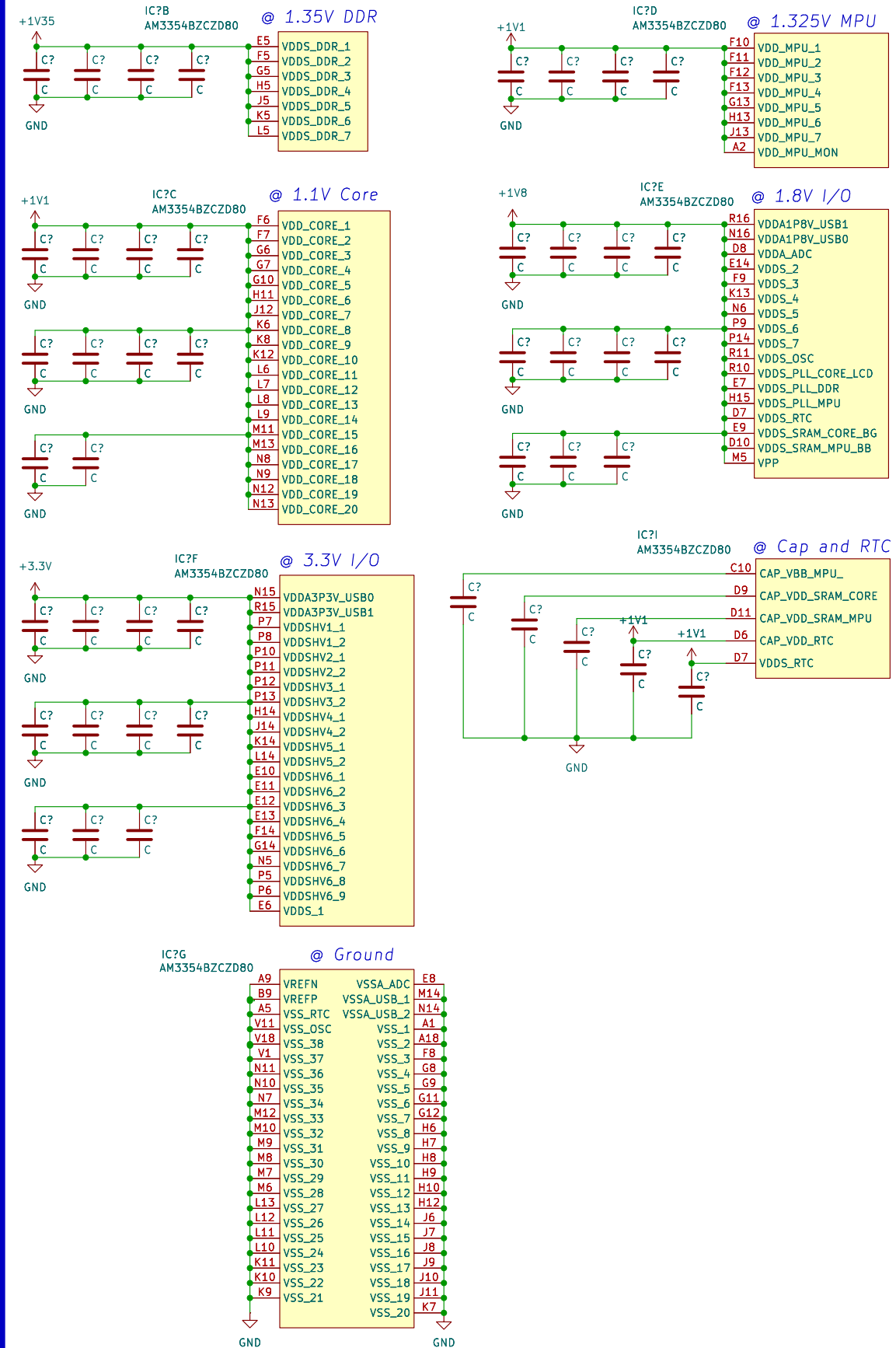
General-Purpose Memory Controller



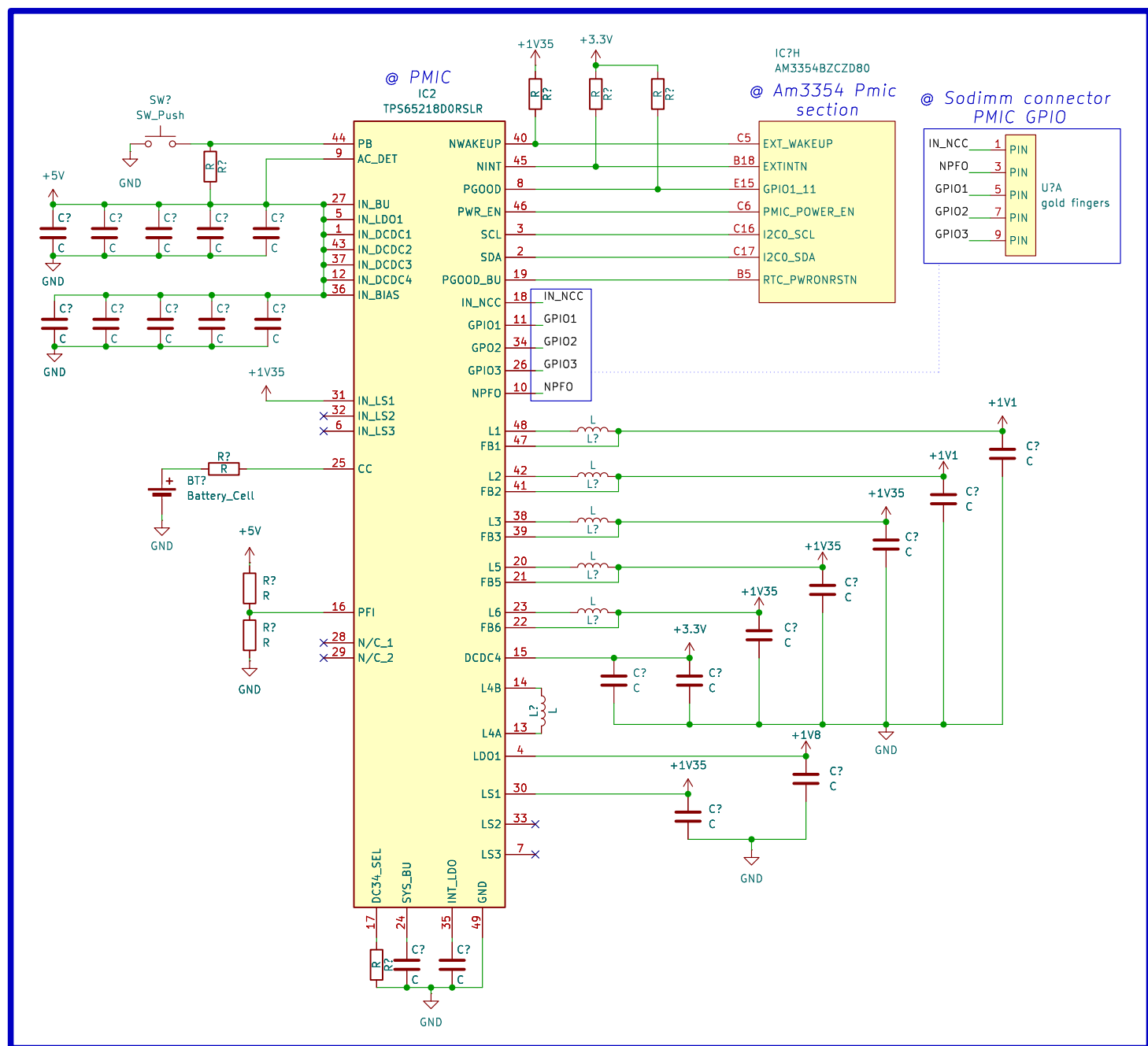
SODIMM Power Pins



Power Am3354



PMIC TPS65218D0RSLR



Rev:
Id: 4/4