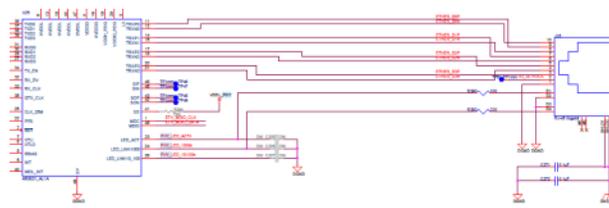## AR8031 Integrated 10/100/1(Transceiver

General Description

The AR8031 is part of the Arctic family of devices — which includes the AR8031, AR8033, and AR8035. The AR8031 is Atheros' 4th generation, single port, 10/100/1000 Mbps, Tri-speed Ethernet PHY. It supports both RGMII and SGMII interfaces to the MAC.
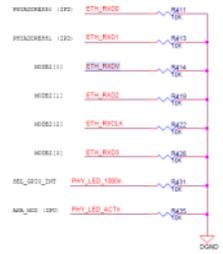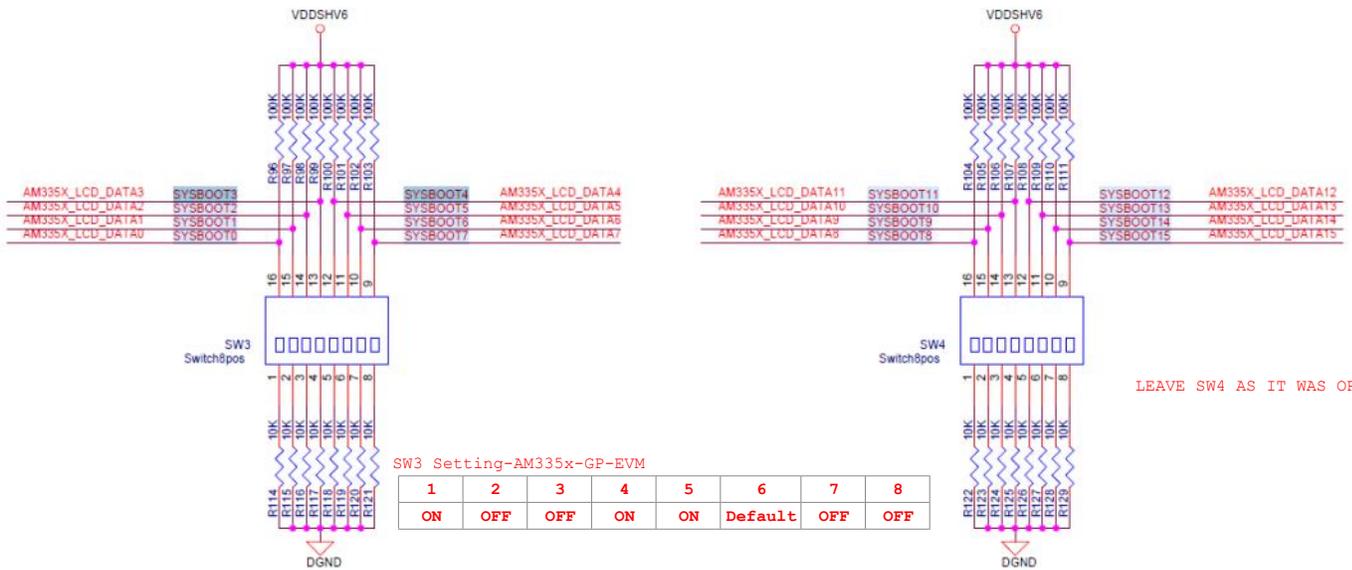
Configured FOR RGMII

Table 1-3. **Mode Definition**

Mode [3:0]	Description
0000	1000 BASE-T, RGMII

SYSBOOT[15:14]	SYSBOOT[13:12]	SYSBOOT[11:10]	SYSBOOT[9]	SYSBOOT[8]	SYSBOOT[7:6]	SYSBOOT[5]	SYSBOOT[4:0]	Boot Sequence			
00b = 19.2MHz 01b = 24MHz 10b = 25MHz 11b = 26MHz	00b (all other values reserved)	For NAND boot: must be 00b	0 = ECC done by ROM 1 = ECC handled by NAND	Don't care for ROM code	00b = MII 01b = RMII 10b = Reserved 11b = RGMII w/o internal delay	0 = CLKOUT1 disabled 1 = CLKOUT1 enabled	00110b	EMAC1	SPI0	NAND	NANDI2C
		must be 00b									

SW3 Setting-AM335x-GP-EVM

1	2	3	4	5	6	7	8
ON	OFF	OFF	ON	ON	Default	OFF	OFF

LEAVE SW4 AS IT WAS ORIGNALLY