

3 Functional Description (cont'd) 3.1 Initialization (cont'd)

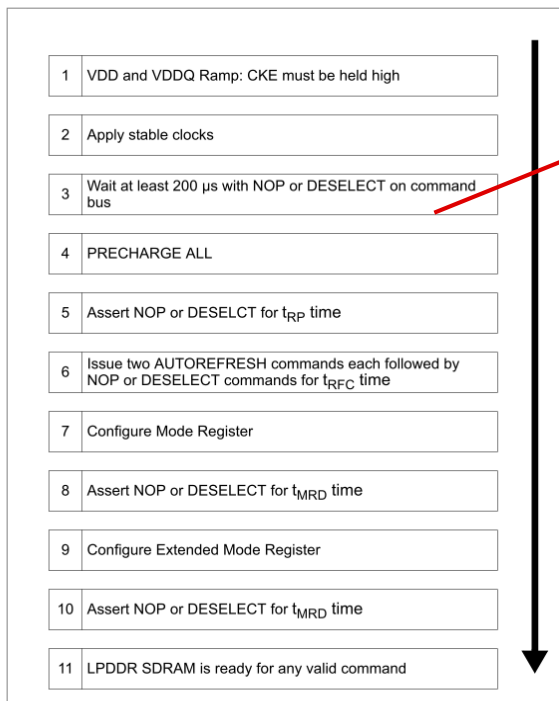


Figure 4 — Initialization Flow Diagram

3 Functional Description (cont'd) 3.1 Initialization (cont'd)

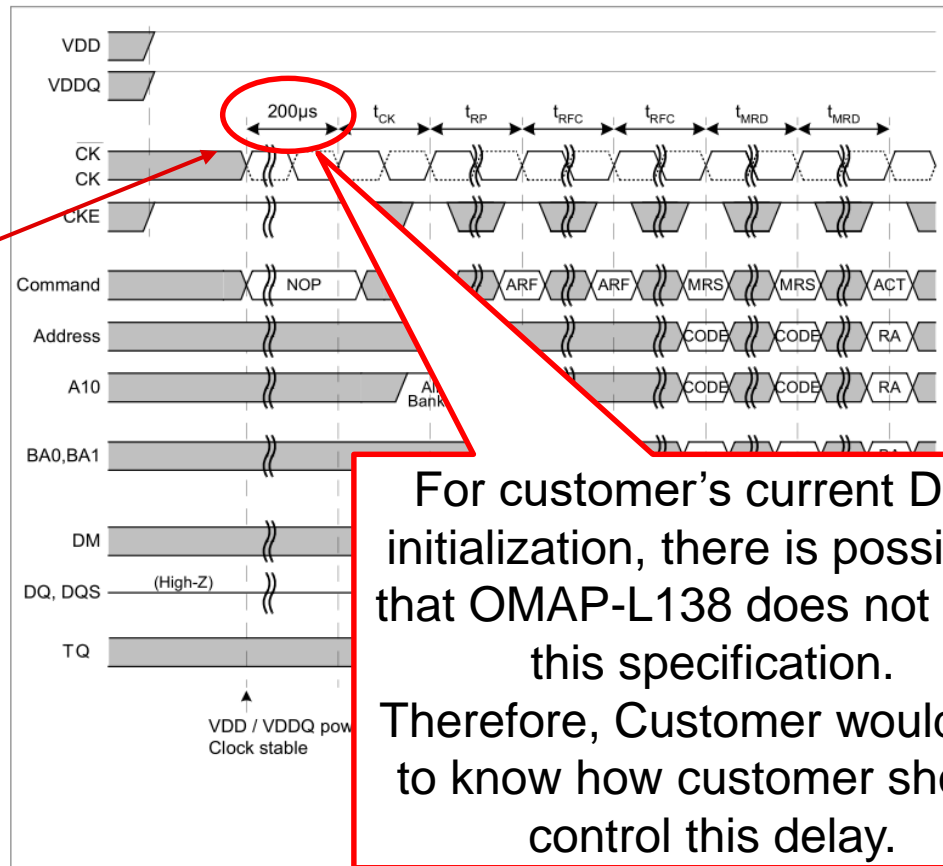


Figure 5 — Initialization Waveform Sequence

For customer's current DDR initialization, there is possibility that OMAP-L138 does not meet this specification. Therefore, Customer would like to know how customer should control this delay.