The following steps can be used in a command shell to generate the CCS projects for PDK low level drivers. Users are expected to complete component discovery in CCS before using these steps.

***Setup environment from root directory:***

***<SDK\_INSTALL\_PATH>\processor\_sdk\_rtos\_am65xx\_5\_01\_00\_11***

***setupenv.bat***

**Optional step***: SDK contains prebuilt binaries for driver and CSL libraries so there is no need to rebuild the libraries but if you need to rebuild the libraries, you can do it using gmake utility using the following options.*

***gmake <target>\_clean***

***gmake <target>***

***<target>:*** *Refer to makefile in the pdk\_am65xx\_x\_x\_x\packages*

*all csl board emac fvid2 gpio i2c mcasp mmcsd nimu pm profiling pruss sciclient spi uart udma usb sbl sa osal can be used as <target>*

This can take a while to build. To limit the build to A53 core, use the additional arguments as

***gmake <target> LIMIT\_BOARDS=am65xx\_evm LIMIT\_CORES=mpu1\_0 <ARGUMENTS>***

|  |  |  |
| --- | --- | --- |
| ***ARGUMENTS*** | ***Options*** | ***Description*** |
| ***LIMIT\_BOARDS*** | ***am65xx\_evm, am65xx\_idk*** | ***Supported evaluation platform*** |
| ***LIMIT\_SOC*** | ***am65xx*** | ***Supported SOC options*** |
| ***LIMIT\_CORES*** | ***mcu1\_0, mpu1\_0*** | ***Support for different cores*** |
| ***DISABLE\_RECURSE\_DEPS*** | ***yes, no*** | ***Disable for building dependencies*** |
| ***BUILD\_PROFILE*** | ***release, debug*** | ***Build profile*** |

To apply the options globally, set the environment variables:

***set LIMIT\_BOARDS=am65xx\_evm***

***set LIMIT\_SOC=am65xx***

***set DISABLE\_RECURSE\_DEPS=yes***

***set BUILD\_PROFILE=release***