

Features	C5515	C5545	C5517	C5502
Speed and Performance	150 MHz	120 Mhz	200 MHz	300 MHz
	Dual 16-bit support per cycle	Dual 16-bit support per cycle	Dual 16-bit support per cycle	One/Two Instructions Executed per Cycle
Package	196-Terminal BGA 10x10 mm	118-Terminal BGA 7x7mm	196-Terminal BGA 10x10 mm (0.65 mm)	176-Terminal LQFP 26x26 mm201-Terminal BGA 15x15 mm (0.8 mm)
Power Consumption	028 mW/MHz @ 100MHz & 1.3V	0.28 mW/MHz @ 100MHz & 1.3V	0.47mW/MHz @ 100MHz & 1.3V	1mW/MHz @300 MHz & 1.26V
On Chip RAM/ \$	NA	NA	NA	16K-Byte Instruction Cache (I-Cache)
	320K Bytes On-Chip RAM	320K Bytes On-Chip RAM	320K Bytes On-Chip RAM	64K Bytes On-Chip RAM (DARAM)
	128KB ROM	128KB ROM	128KB ROM	32K Byte ROM
I2C	1	1	1	1
Timer/ Counter	3 - 32-Bit GP Timers (1 WDT)	3 - 32-Bit GP Timers (1 WDT)	3 - 32-Bit GP Timers (1 WDT)	2 64-bit GP Timers
	1 Watchdog Timer	1 Watchdog Timer	1 Watchdog Timer	1 programmable watchdog timer
	NA	NA	NA	DSP/BIOS Counter
SPI	1 w/ 4 CS	1 w/ 3 CS	1 w/ 4 CS	No SPI , McBSP SPI mode
McSPI	NA	NA	Interface (McSPI) with Three Chip Selects	NA
USB	1 USB 2.0	1 USB 2.0	1 USB 2.0 Device	NA
Parallel Ports	NA	NA	1 UHPI	1 UHPI
UART	1 UART	1 UART	1 UART	1 UART
RTC	1	1	1	NA
MMC/SD	2	2	2	NA
ADC	10-Bit 4-Input SAR	10-Bit 3-Input SAR	10-Bit 4-Input SAR	NA
EMIF	8/16b NAND, NOR	NA	8/16b NAND, NOR	NA
	16-bit SDRAM/mSDRAM		16-bit SDRAM/mSDRAM	Synchronous DRAM (SDRAM)
FFT	1	1	1	NA
LCD	1	1	NA	NA