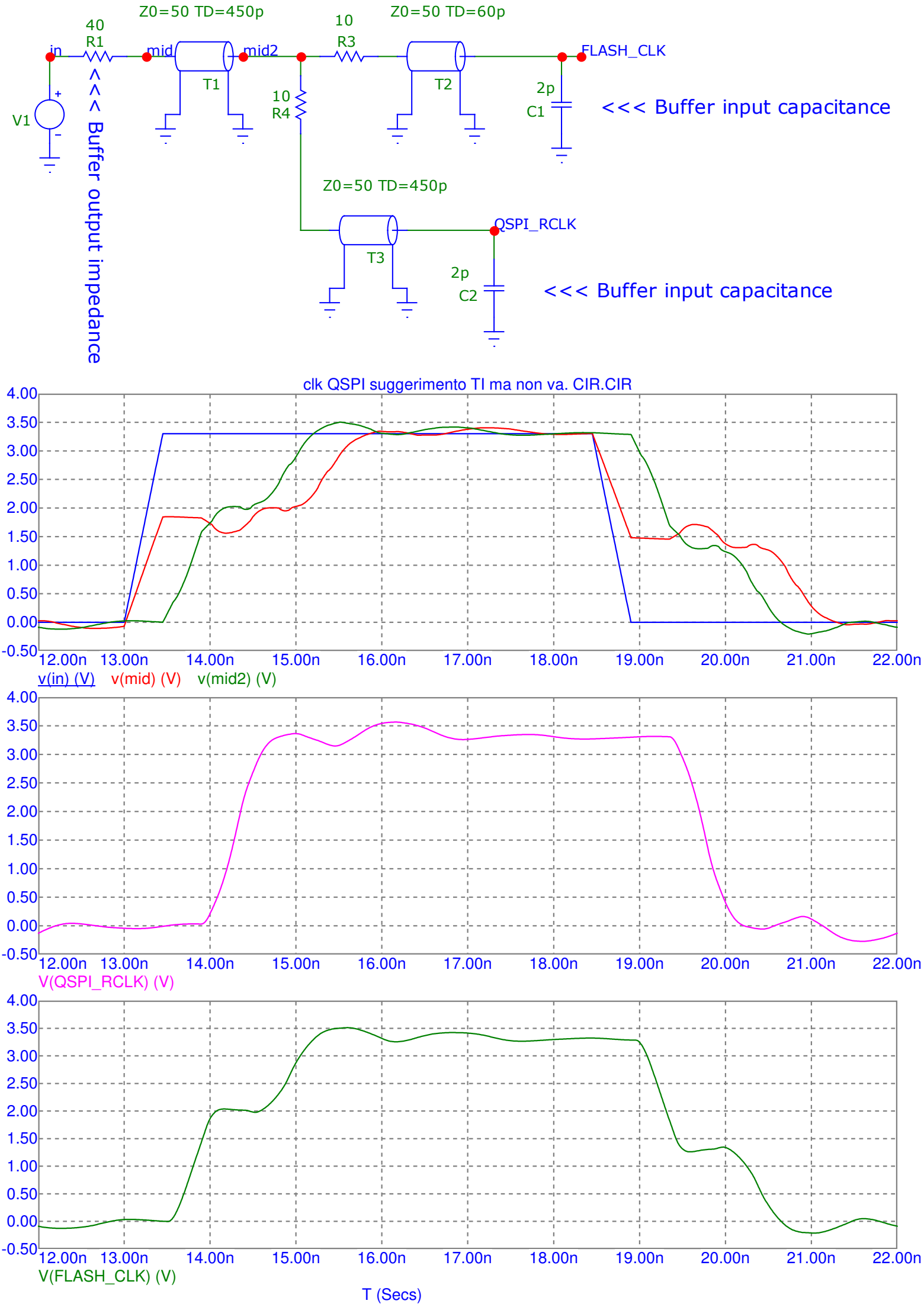
Z0=50 TD=450p
40
R1
in
mid
mid2
T1
10
R3
Z0=50 TD=60p
T2
FLASH_CLK
2p
C1
<<< Buffer input capacitance
V1
<<< Buffer output impedance
10
R4
Z0=50 TD=450p
T3
QSPI_RCLK
2p
C2
<<< Buffer input capacitance
clk QSPI suggerimento TI ma non va. CIR.CIR
4.00
3.50
3.00
2.50
2.00
1.50
1.00
0.50
0.00
-0.50
12.00n
13.00n
14.00n
15.00n
16.00n
17.00n
18.00n
19.00n
20.00n
21.00n
22.00n
v(in) (V)
v(mid) (V)
v(mid2) (V)
V(QSPI_RCLK) (V)
V(FLASH_CLK) (V)
T (Secs)