3.16 Read ID

The device contains a product identification mode, initiated by writing 90h to the command register, followed by an address input of 00h.

Note: If you want to execute Read Status command (0x70) after Read ID sequence, you should input dummy command (0x00) before Read Status command (0x70).

For the S34ML02G1 and S34ML04G1 devices, five read cycles sequentially output the manufacturer code (01h), and the device code and 3rd, 4th, and 5th cycle ID, respectively. For the S34ML01G1 device, four read cycles sequentially output the manufacturer code (01h), device id (F1h), 3rd cycle (00h), and 4th cycle ID of 1Dh respectively. The command register remains in Read ID mode until further commands are issued to it. Figure 41 on page 53 shows the operation sequence, while Table 14 to Table 3.3 explain the byte meaning.

Table 14. Read ID for Supported Configurations

Density	Org	V_{CC}	1st	2nd	3rd	4th	5th
1 Gb	×8	3.3V	01h	F1h	00h	1Dh	—
2 Gb			01h	DAh	90h	95h	44h
4 Gb			01h	DCh	90h	95h	54h
1 Gb	×16		01h	C1h	00h	5Dh	—
2 Gb			01h	CAh	90h	D5h	44h
4 Gb			01h	CCh	90h	D5h	54h

Table 15. Read ID Bytes

Device Identifier Byte	Description
1st	Manufacturer Code
2nd	Device Identifier
3rd	Internal chip number, cell type, etc.
4th	Page Size, Block Size, Spare Size, Serial Access Time, Organization
5th **(S34ML02G1, S34ML04G1)**	ECC, Multiplane information