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REV	DESCRIPTION	DATE	APPROVED
A	Initial schematic for layout	10/21/2010	RRP
B	ADD CODEC	12/21/2010	RRP
C	MULTIPLE CHANGES	8/21/2011	RRP
D	COMPLIANCE CHANGES	3/02/2012	RRP

I2C ADDRESS MAP

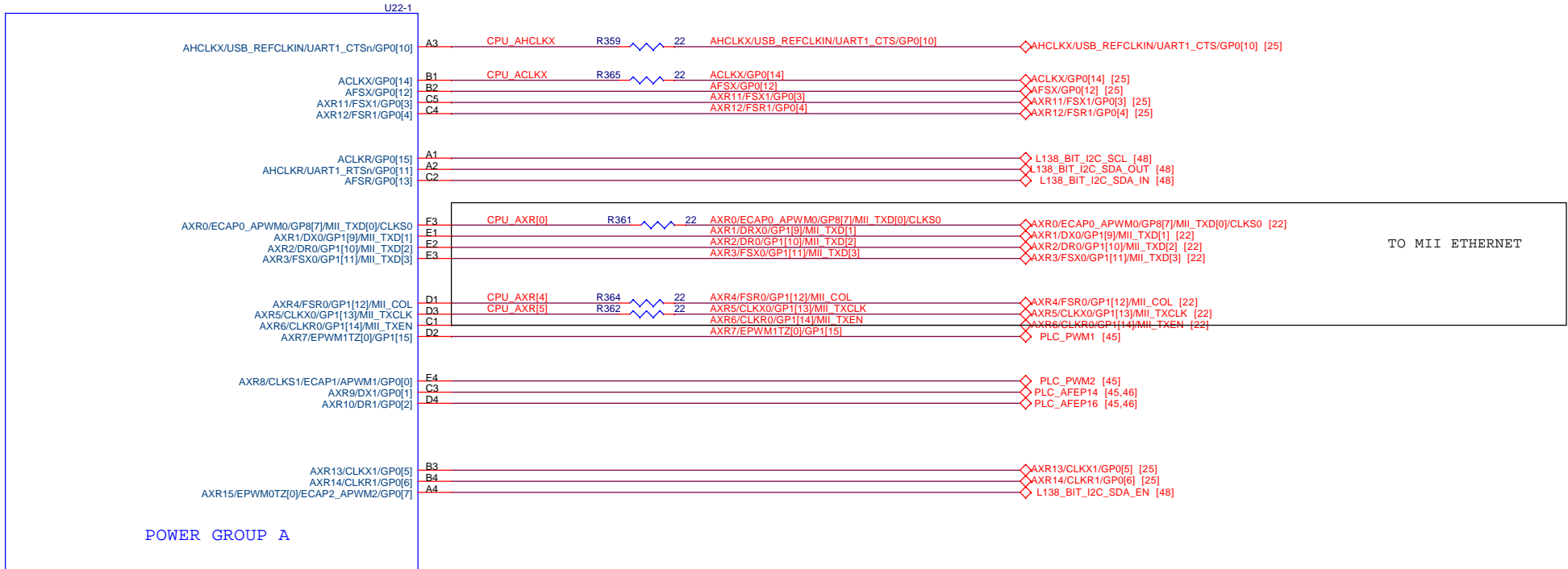
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0x50	101 0000W	I2C EEPROM	
0x48	100 1000W	TPS65070 PMIC	
0x18	001 1000W	TLV320AIC3106	
0x21	010 0001W	TCA6416 - U34 - User Switches	
POWER MEASUREMENT - NOT ON OMAP-L138 CPU BUS			
	100 0000W	INA219 - 5V INPUT	
	100 0001W	INA219 - PMIC 3V3	
	100 0010W	INA219 - PMIC I/O VOLTAGE (1V8/3V3)	
	100 0011W	INA219 - PMIC 1V2	
	100 0100W	INA219 - PMIC 1V2 LDO	
	100 0101W	INA219 - PMIC 1V8 LDO	
	100 0110W	INA219 - RTC 1V2 LDO EXTERNAL	

REVISION STATUS OF SHEETS

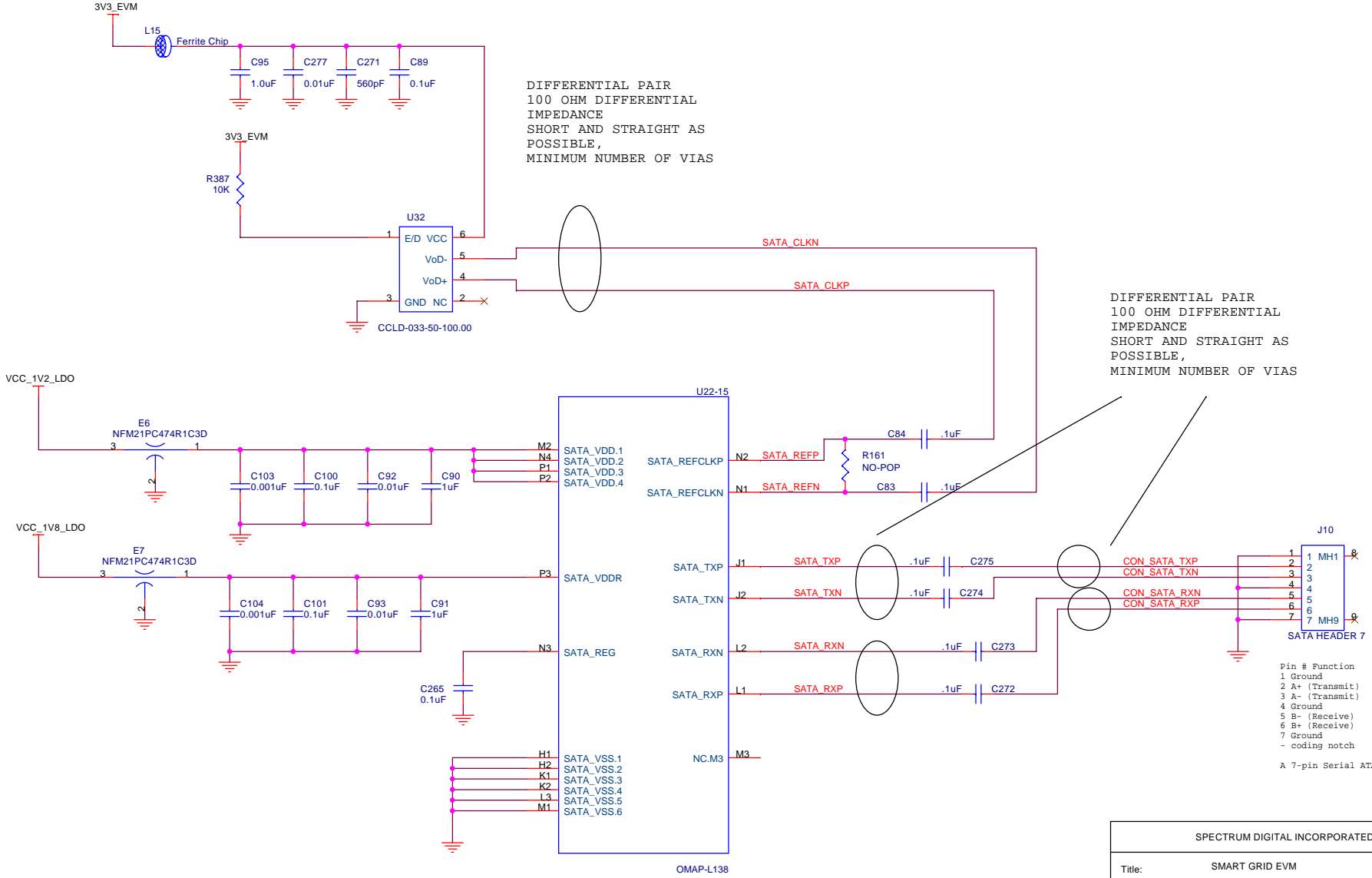
REV	A									
SH	51									
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SH	31	32	33	34	35	36	37	38	39	40
REV	A	A	A	A	A	A	A	A	A	A
SH	21	22	23	24	25	26	27	28	29	30
REV	A	A	A	A	A	A	A	A	A	A
SH	11	12	13	14	15	16	17	18	19	20
REV	B	B	B	B	B	A	A	A	A	A
SH	1	2	3	4	5	6	7	8	9	10

DATE	DATE	DATE	DATE	DATE	DATE	DATE	DATE
DWN	R.R.P.	10/21/2010					
CHK	T.W.K.	10/21/2010					
ENGR	R.R.P.	10/21/2010					
ENGR-MGR	R.R.P.	10/21/2010					
QA	C.M.D.	10/21/2010					
MFG	R.R.P.	10/21/2010					
RLSE	R.R.P.	10/21/2010					

SPECTRUM DIGITAL INCORPORATED			
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Size: B	DWG NO	514202-0001	Revision: D
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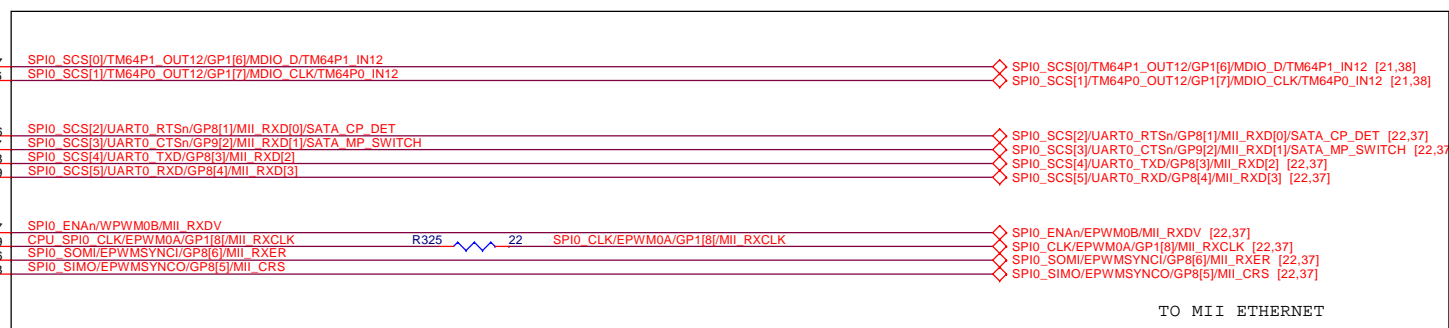
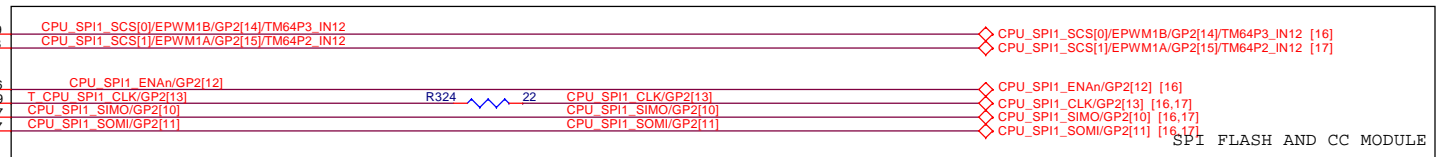
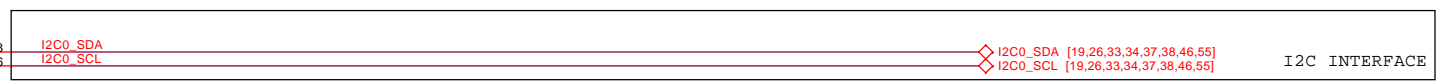
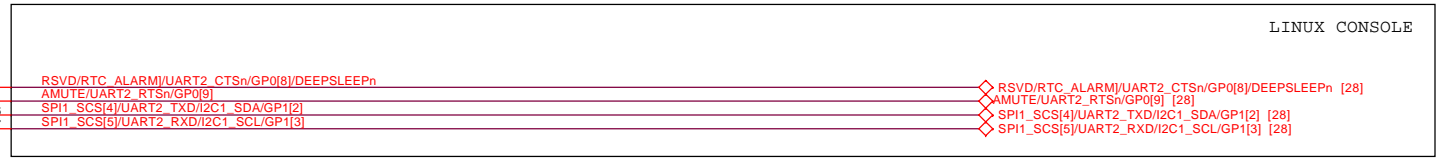
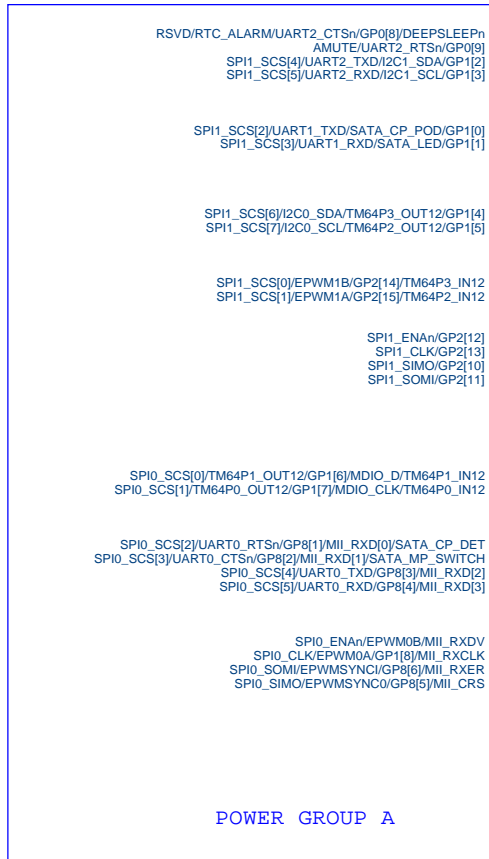


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Size: B	DWG NO	514202-0001	Revision: A
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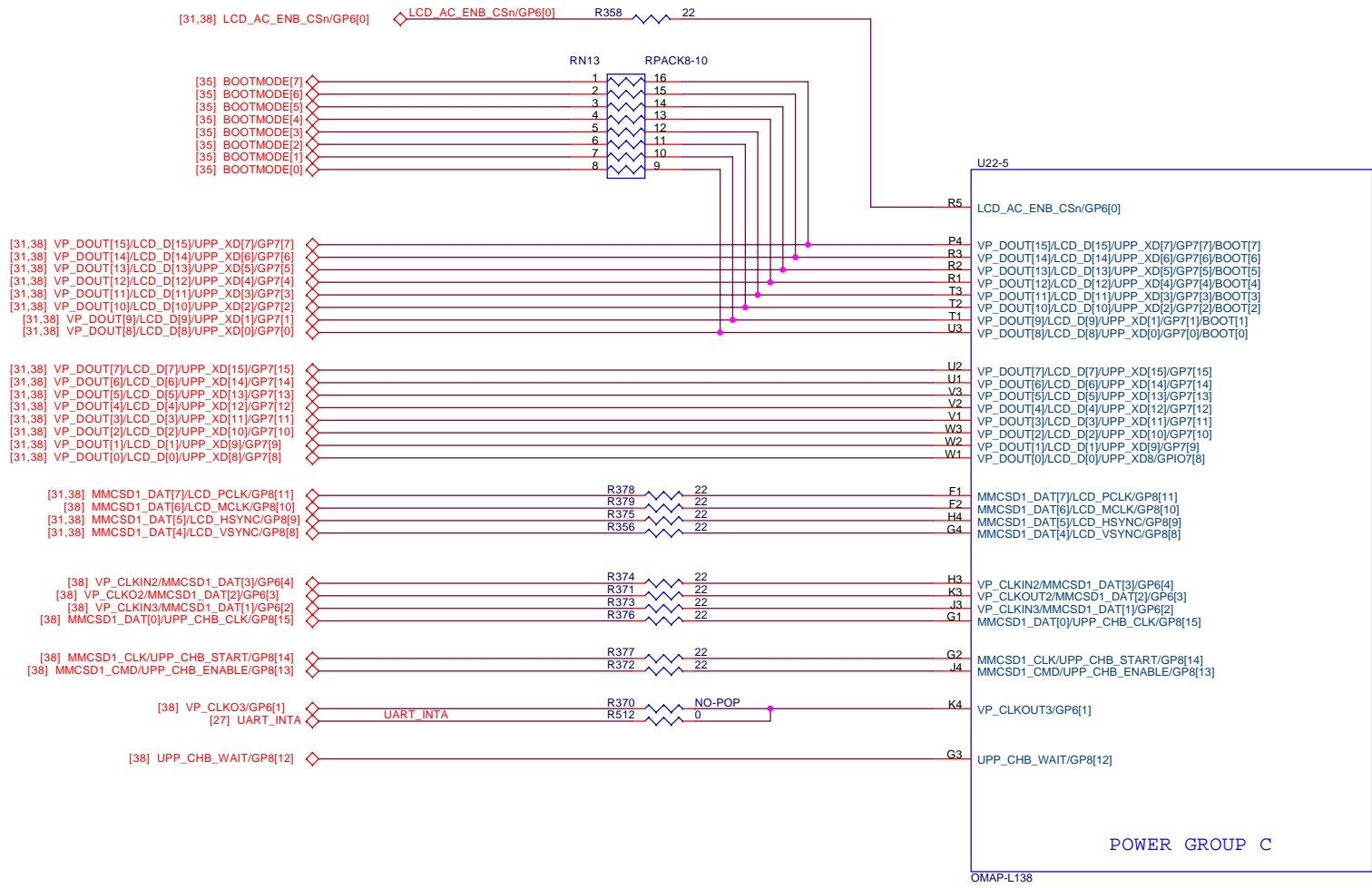


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Size: B	DWG NO	514202-0001	Revision: A
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U22-6



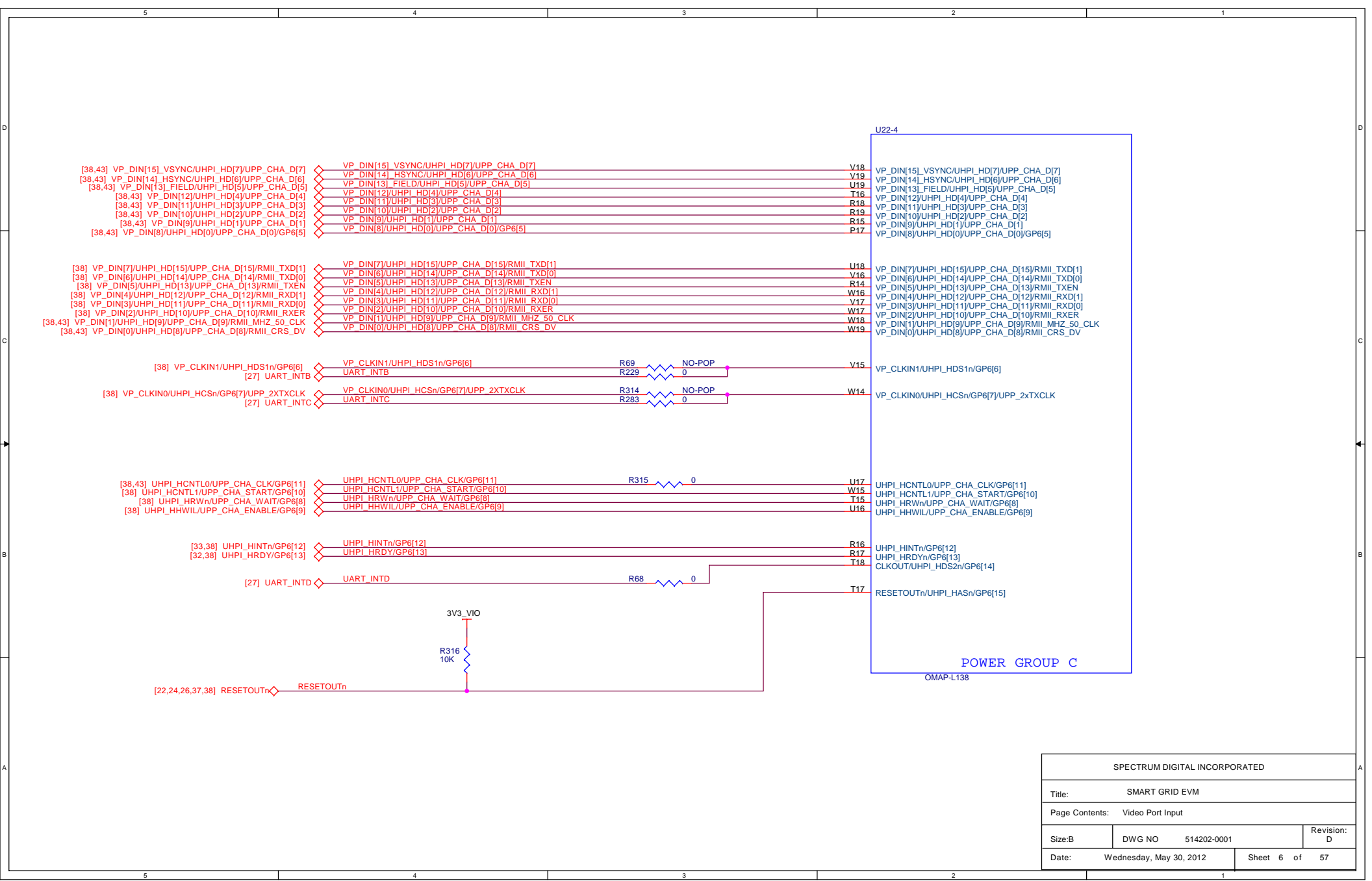
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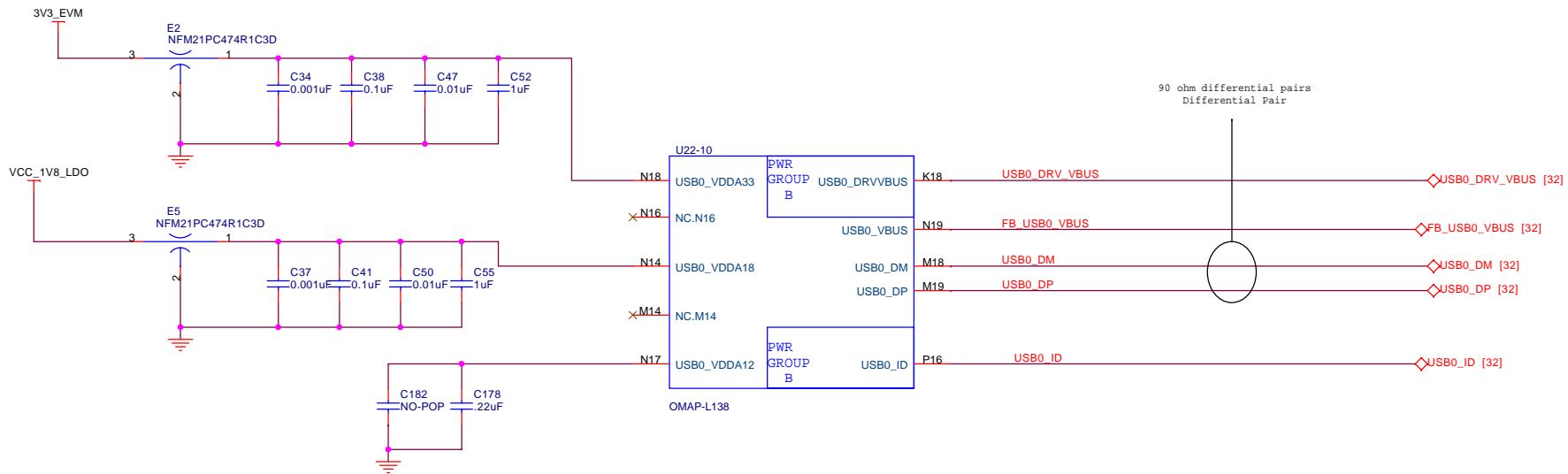
OMAP-L138

POWER GROUP C

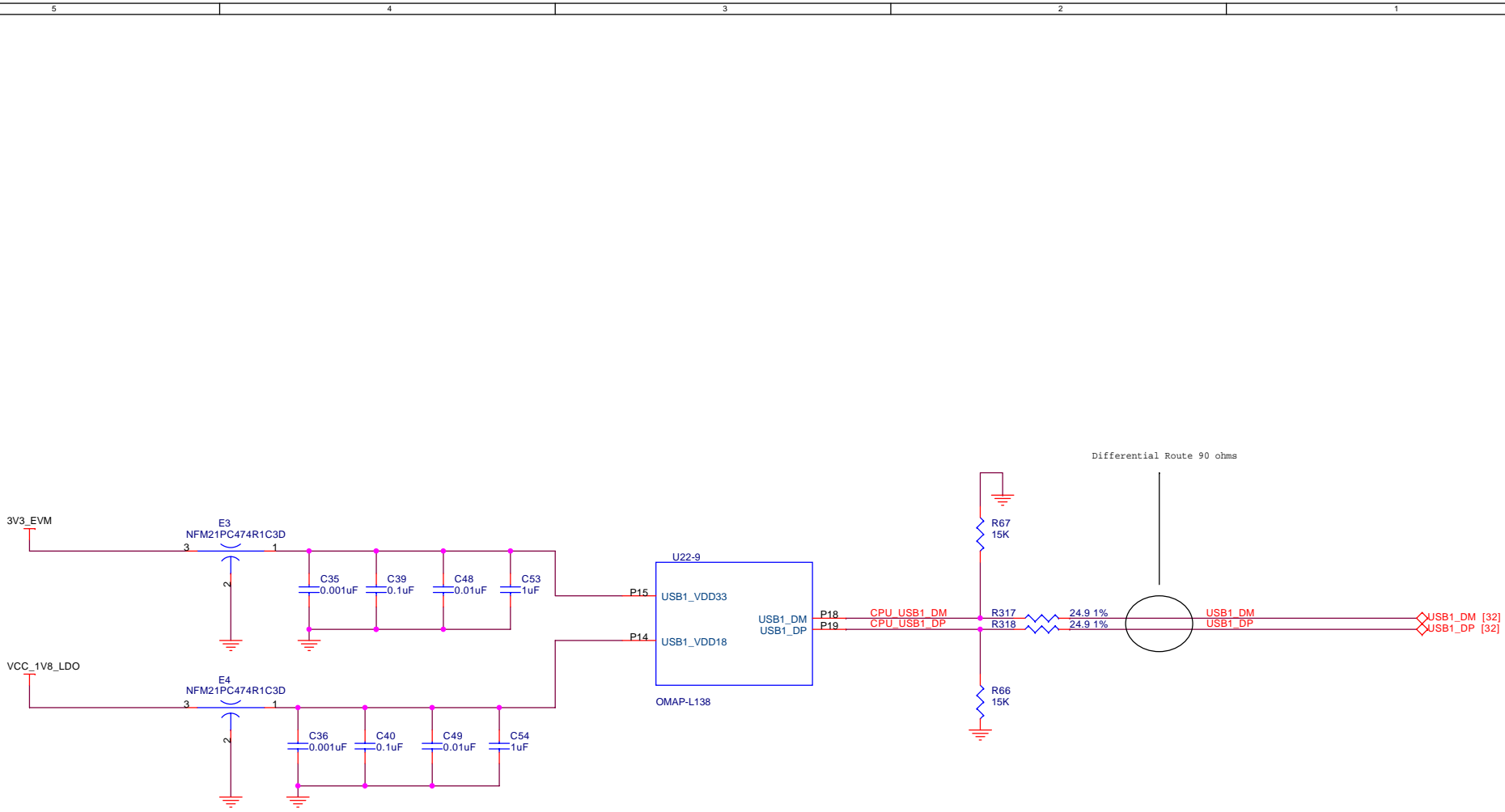
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Size: B	DWG NO	514202-0001	Revision: D
Date:	Wednesday, May 30, 2012	Sheet 5 of	57



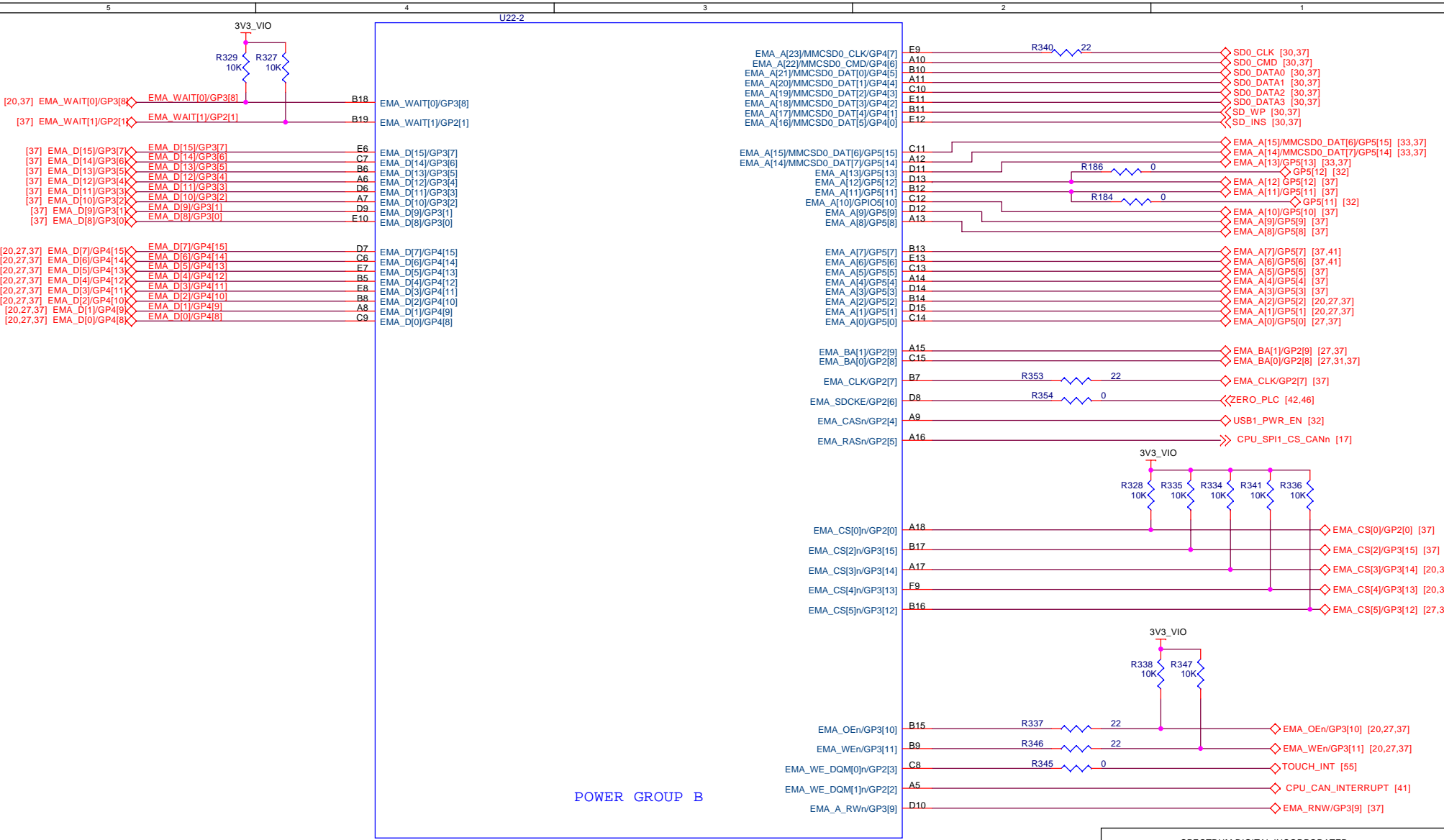
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Size: B	DWG NO	514202-0001	Revision: D
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SPECTRUM DIGITAL INCORPORATED			
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Size: B	DWG NO	514202-0001	Revision: A
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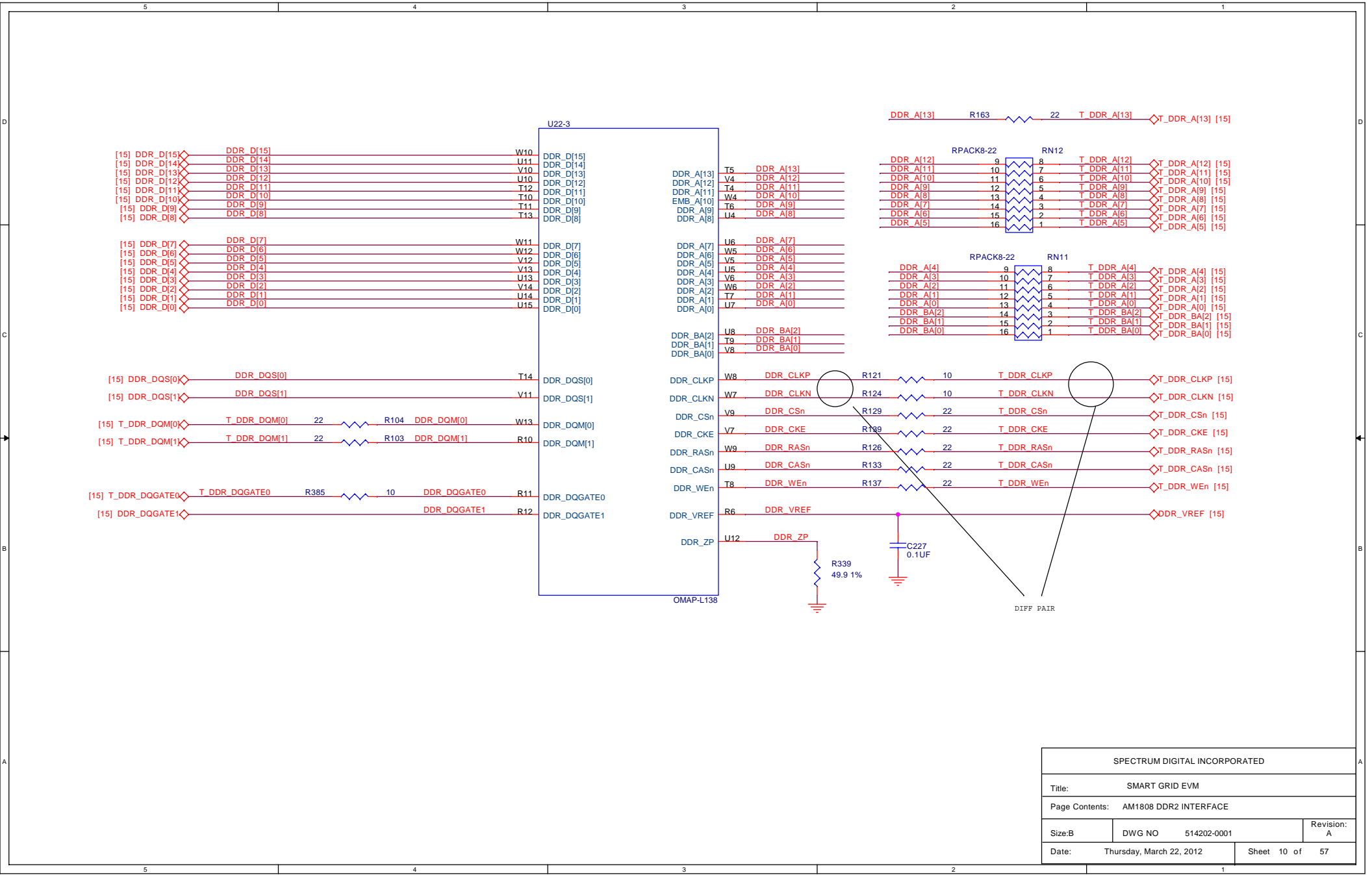
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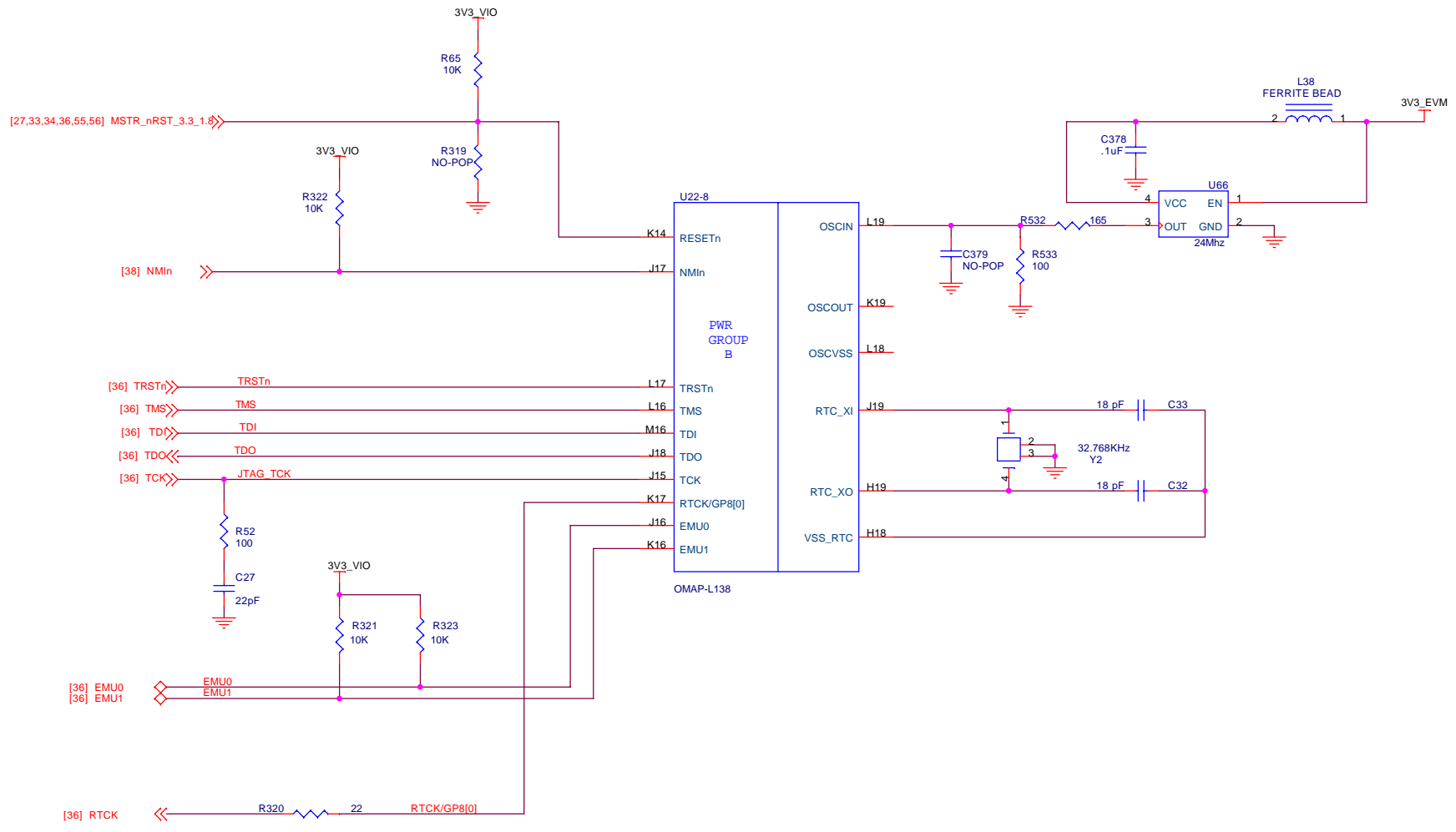
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OMAP-L138

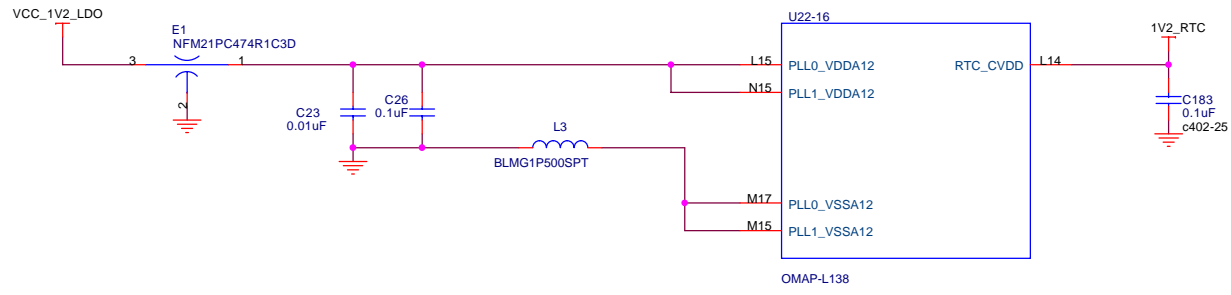
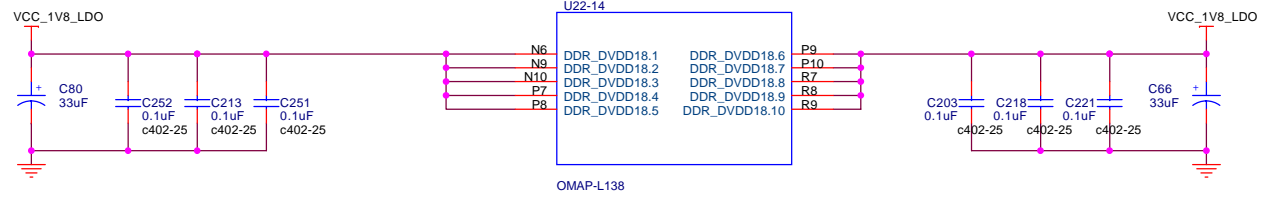
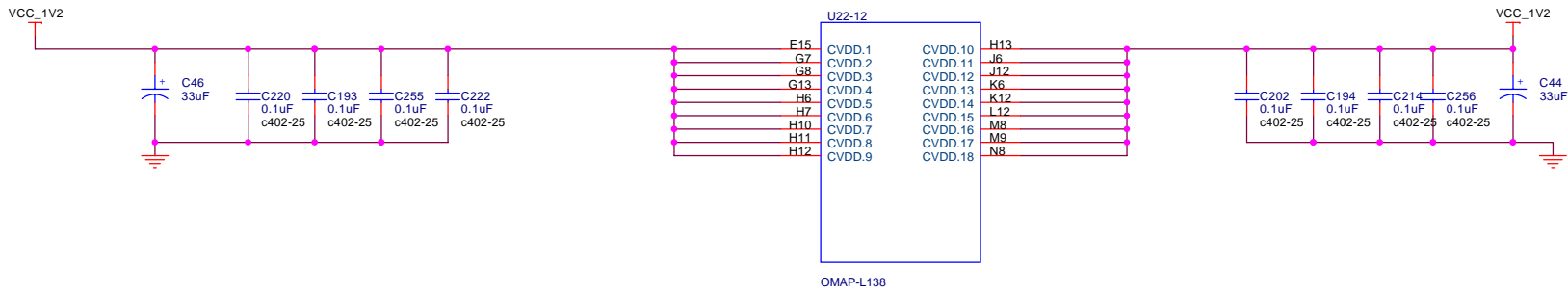
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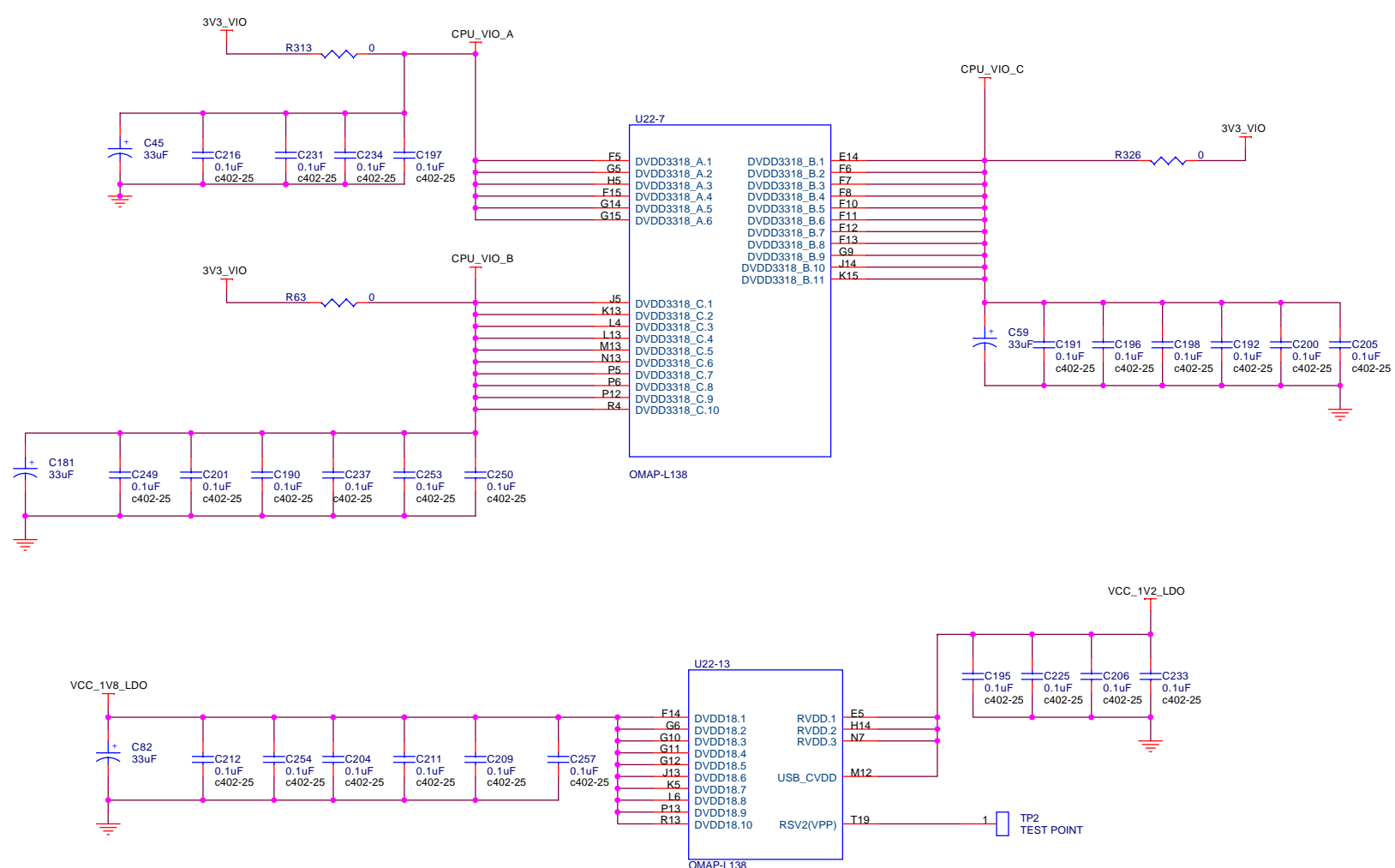
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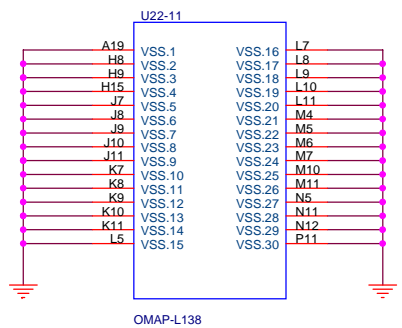
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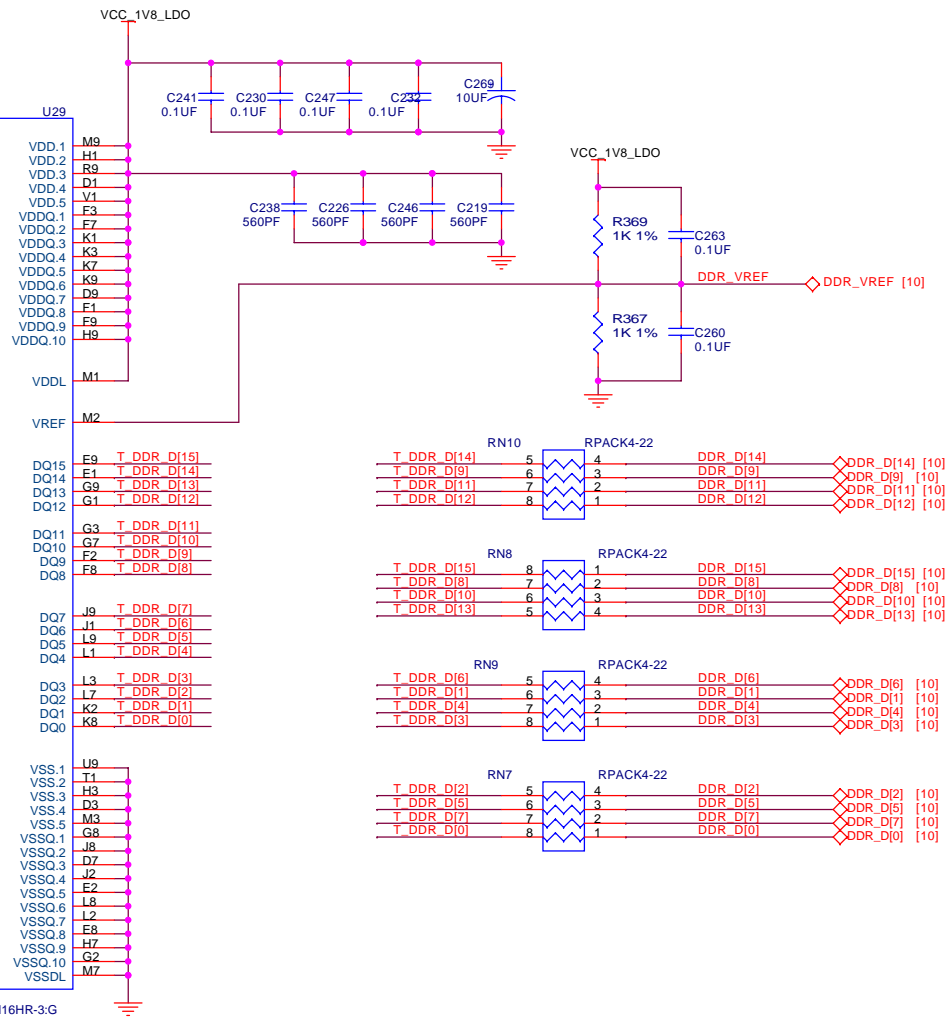
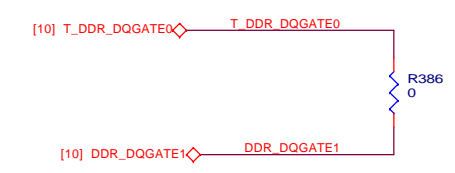
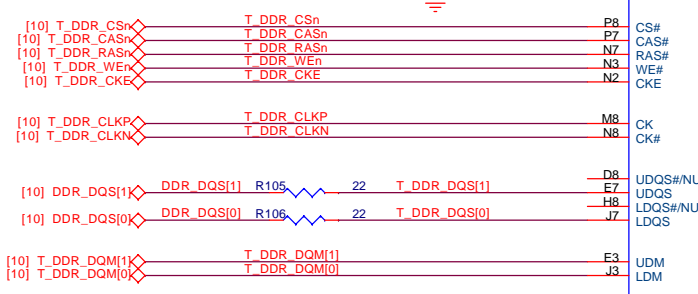
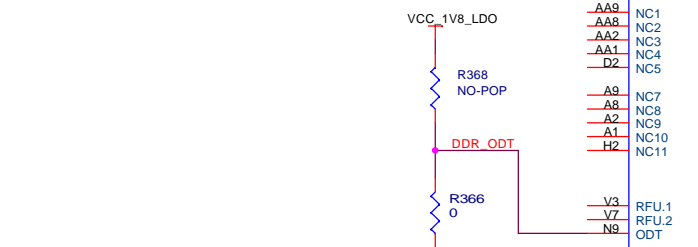
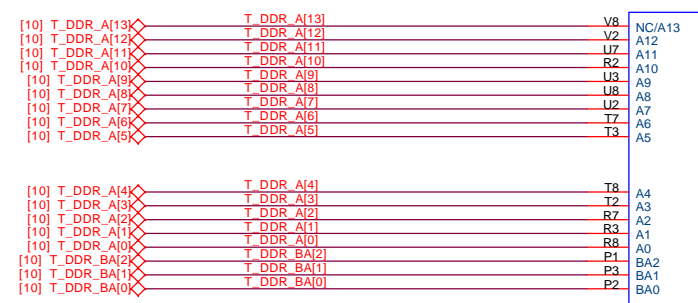
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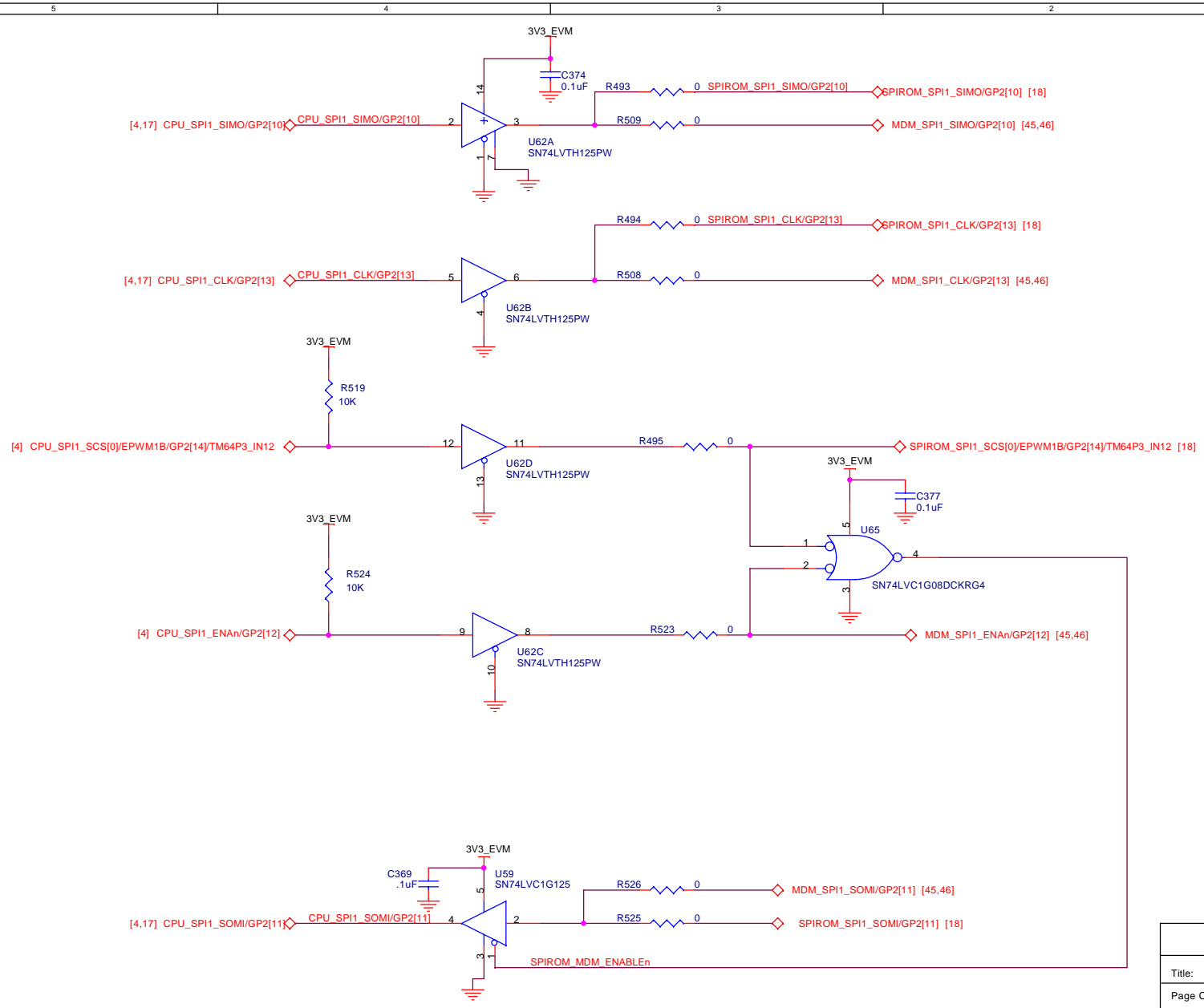
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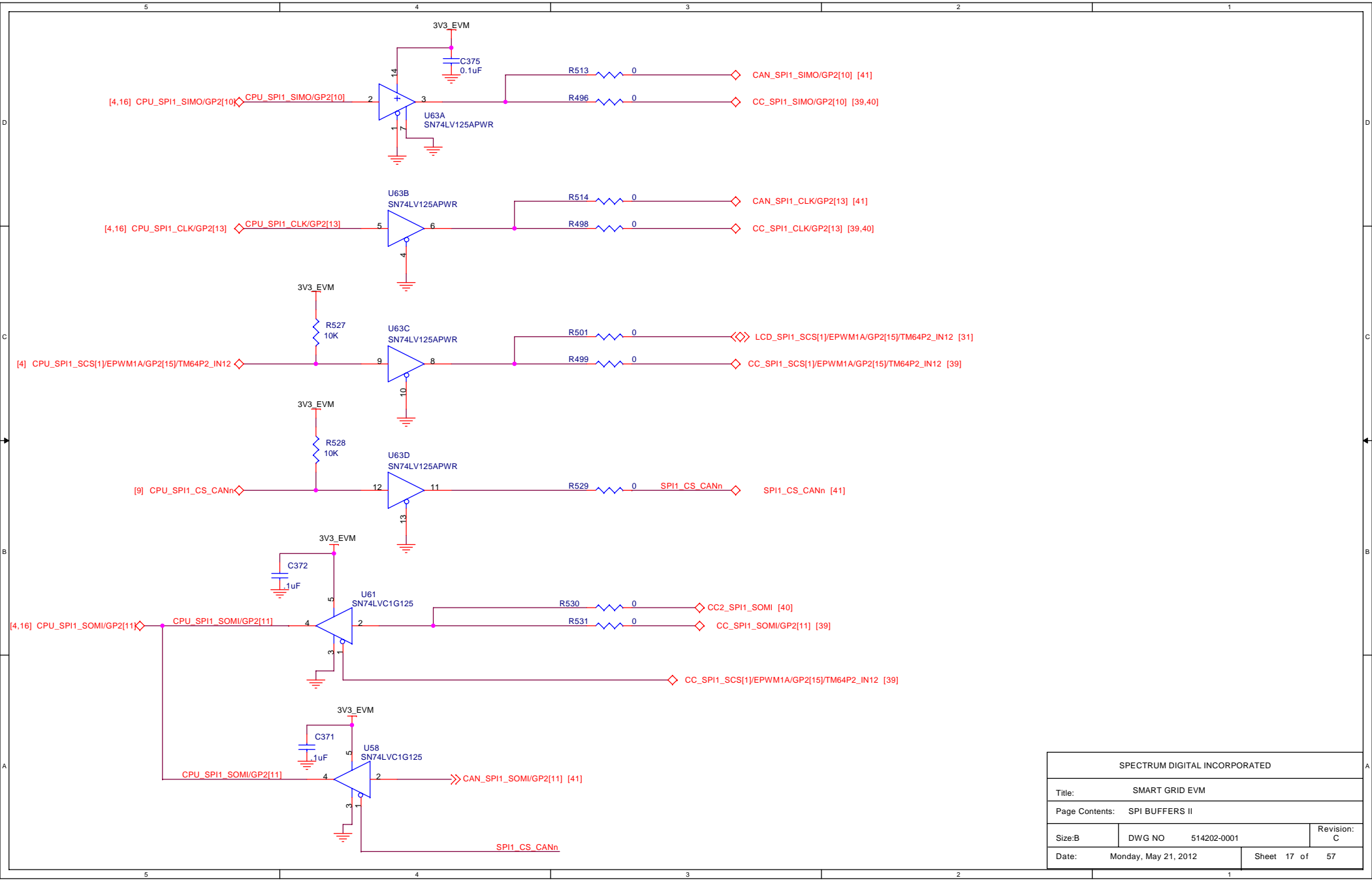
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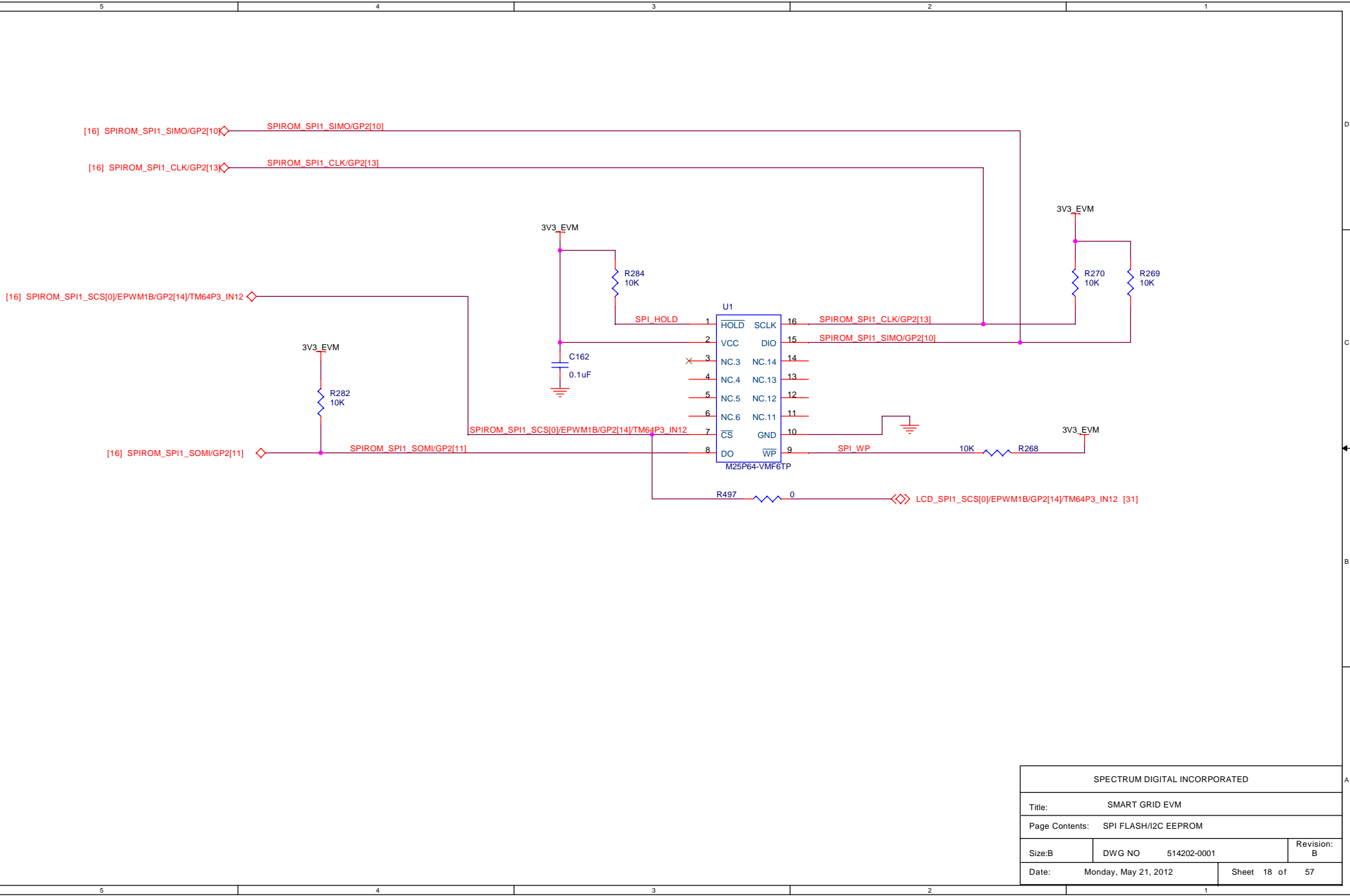
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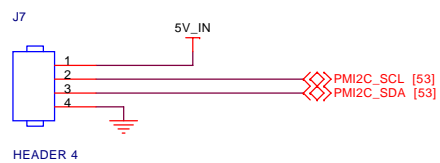
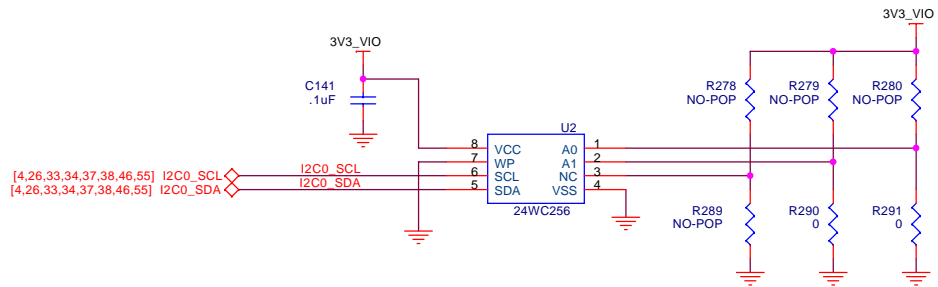
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Size: B	DWG NO	514202-0001	Revision: C
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SPECTRUM DIGITAL INCORPORATED			
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Size: B	DWG NO	514202-0001	Revision: C
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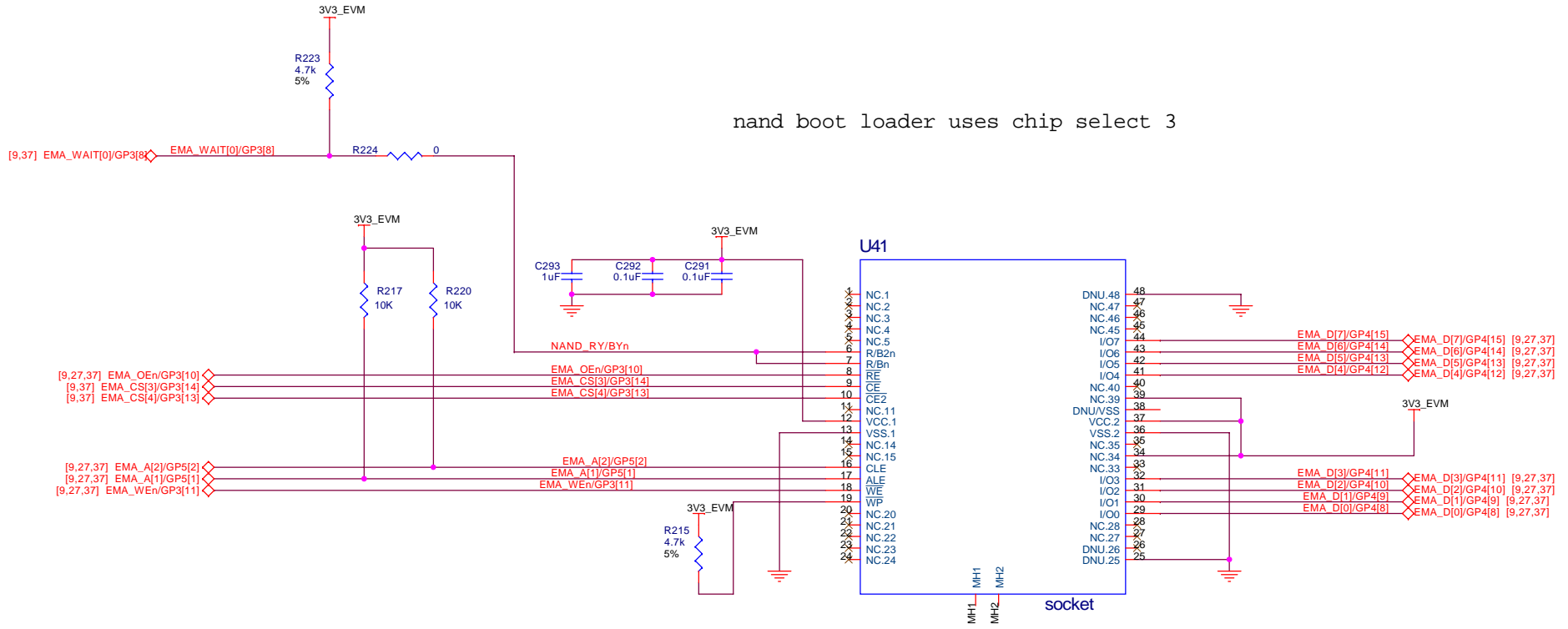


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Size: B	DWG NO	514202-0001	Revision: B
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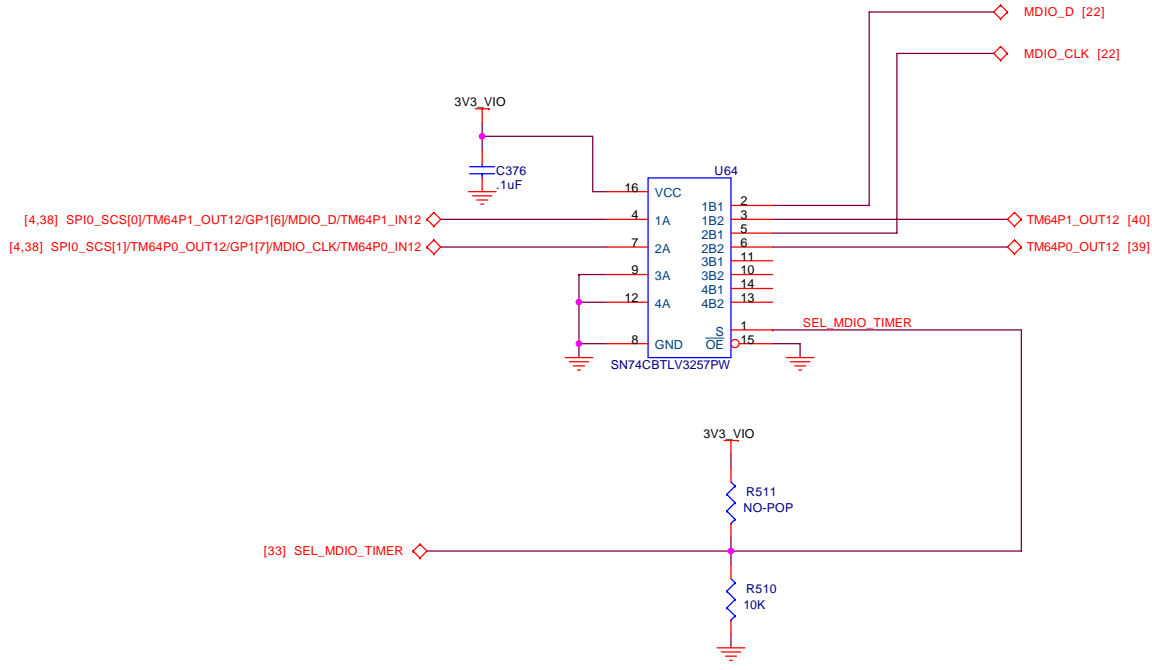


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Title: SMART GRID EVM			
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Size: B	DWG NO	514202-0001	Revision: B
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nand boot loader uses chip select 3

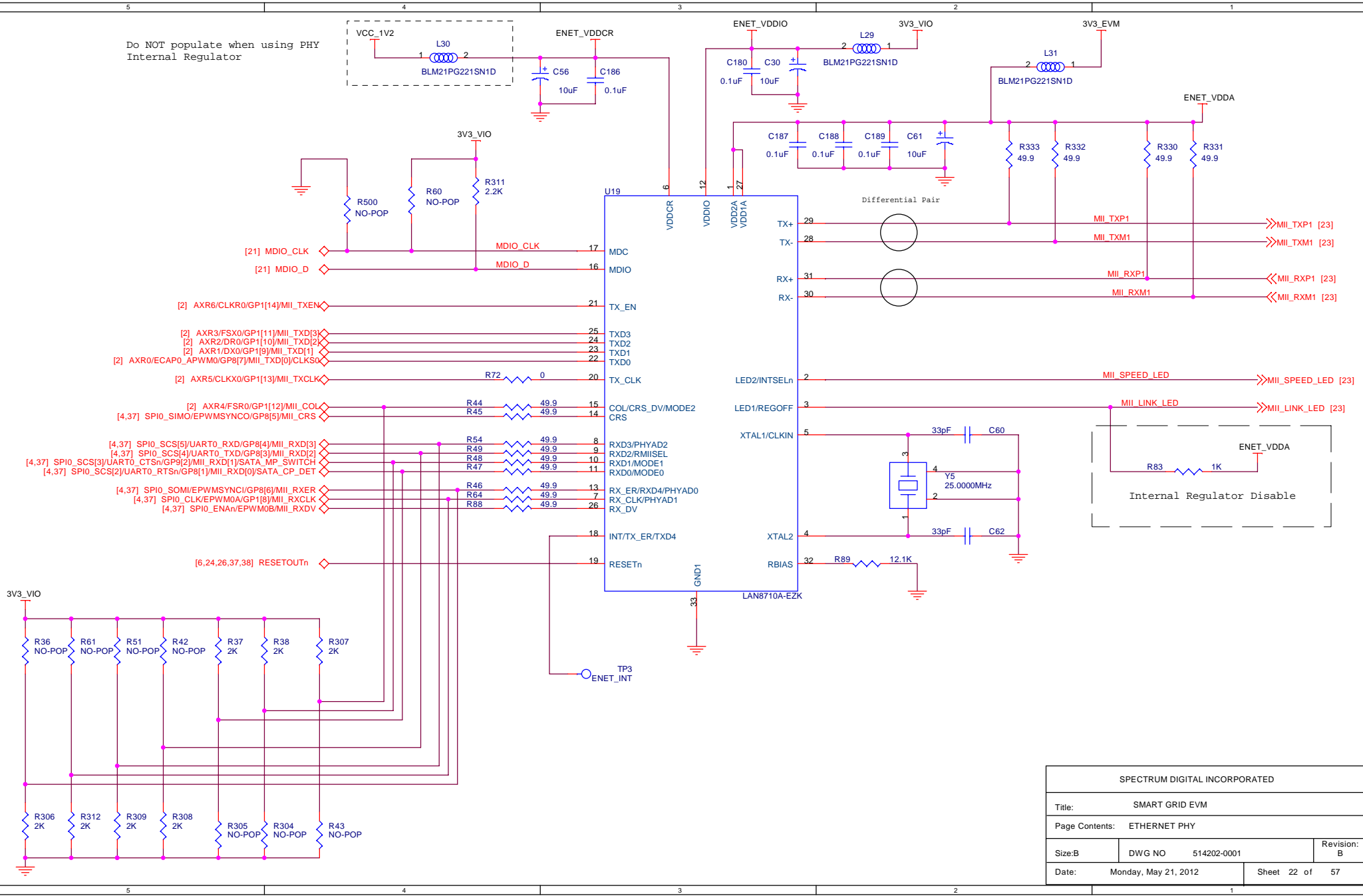


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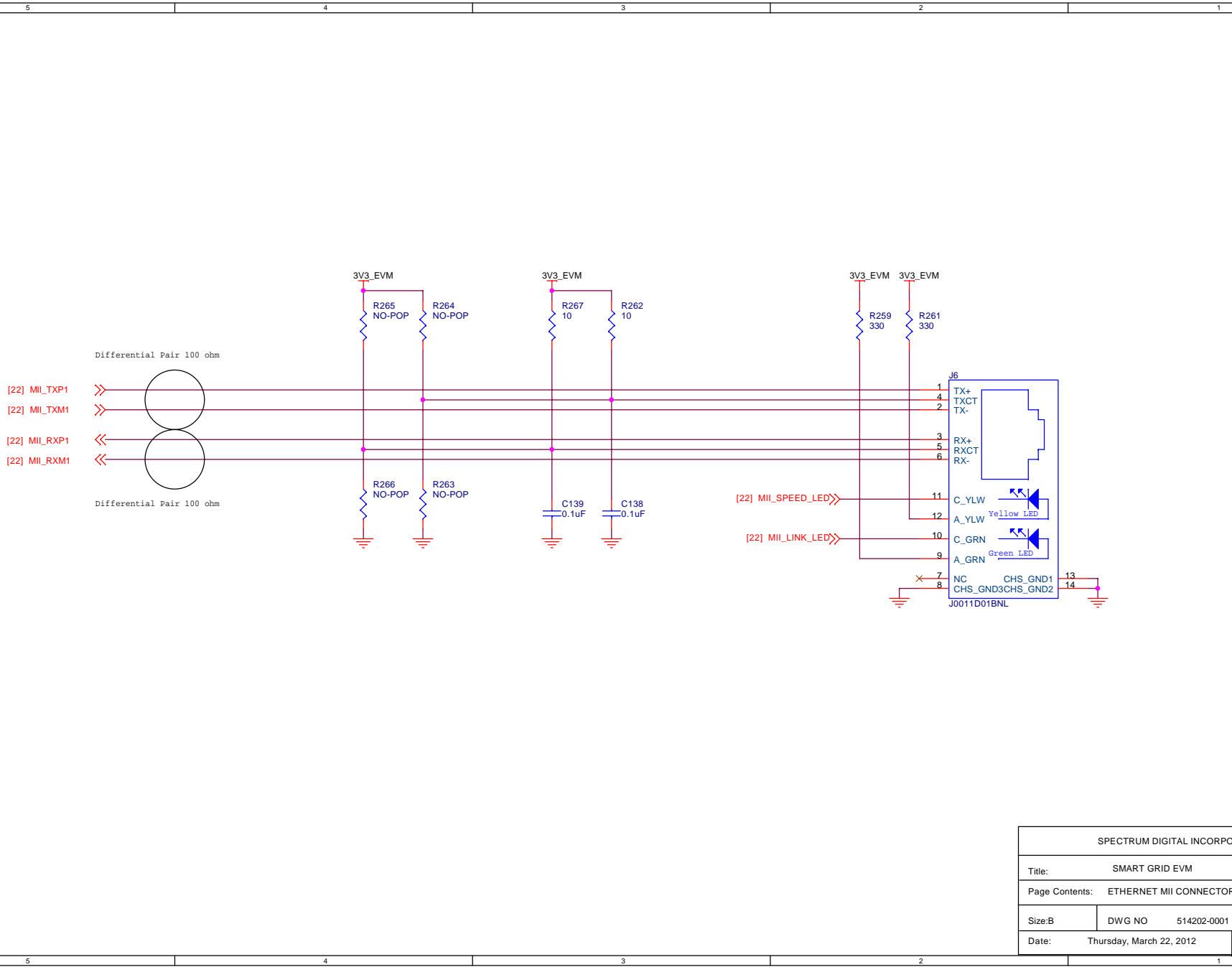


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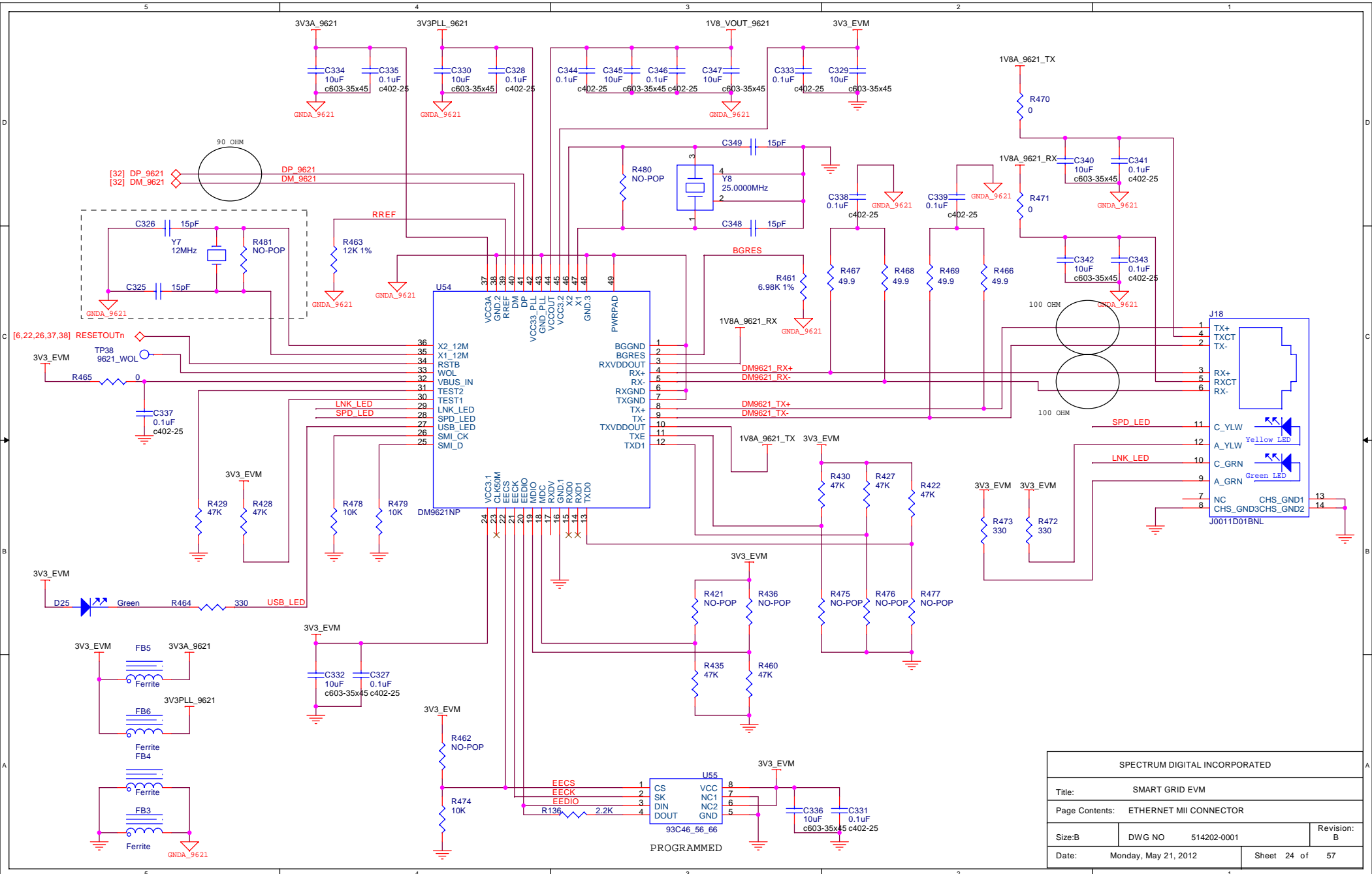
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Internal Regulator



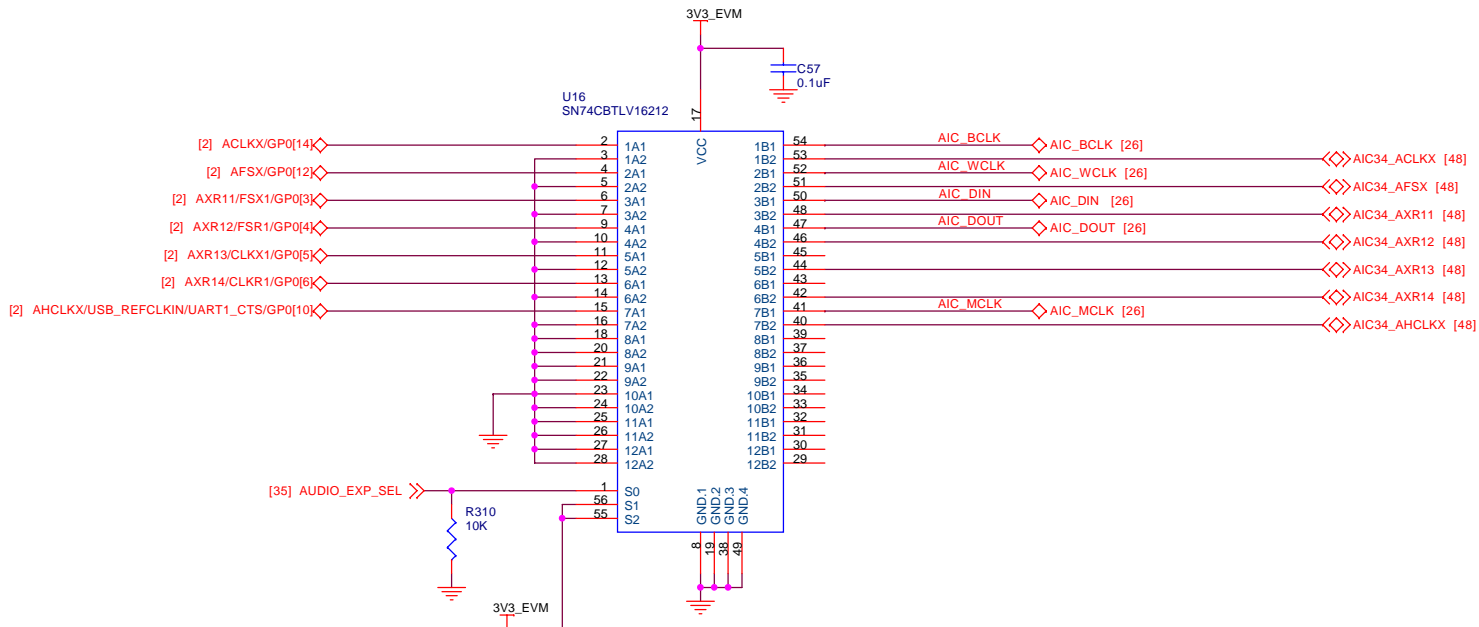
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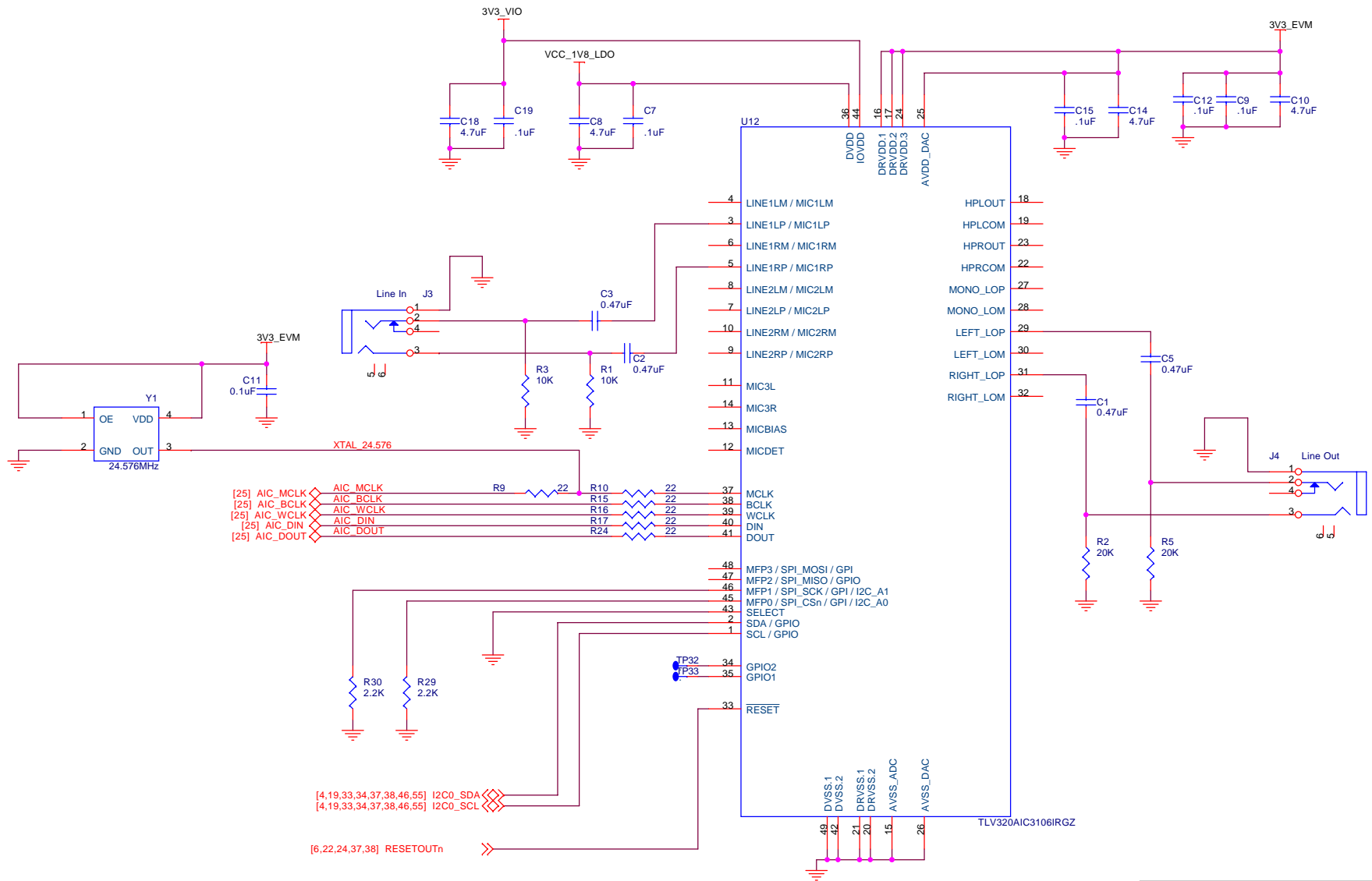
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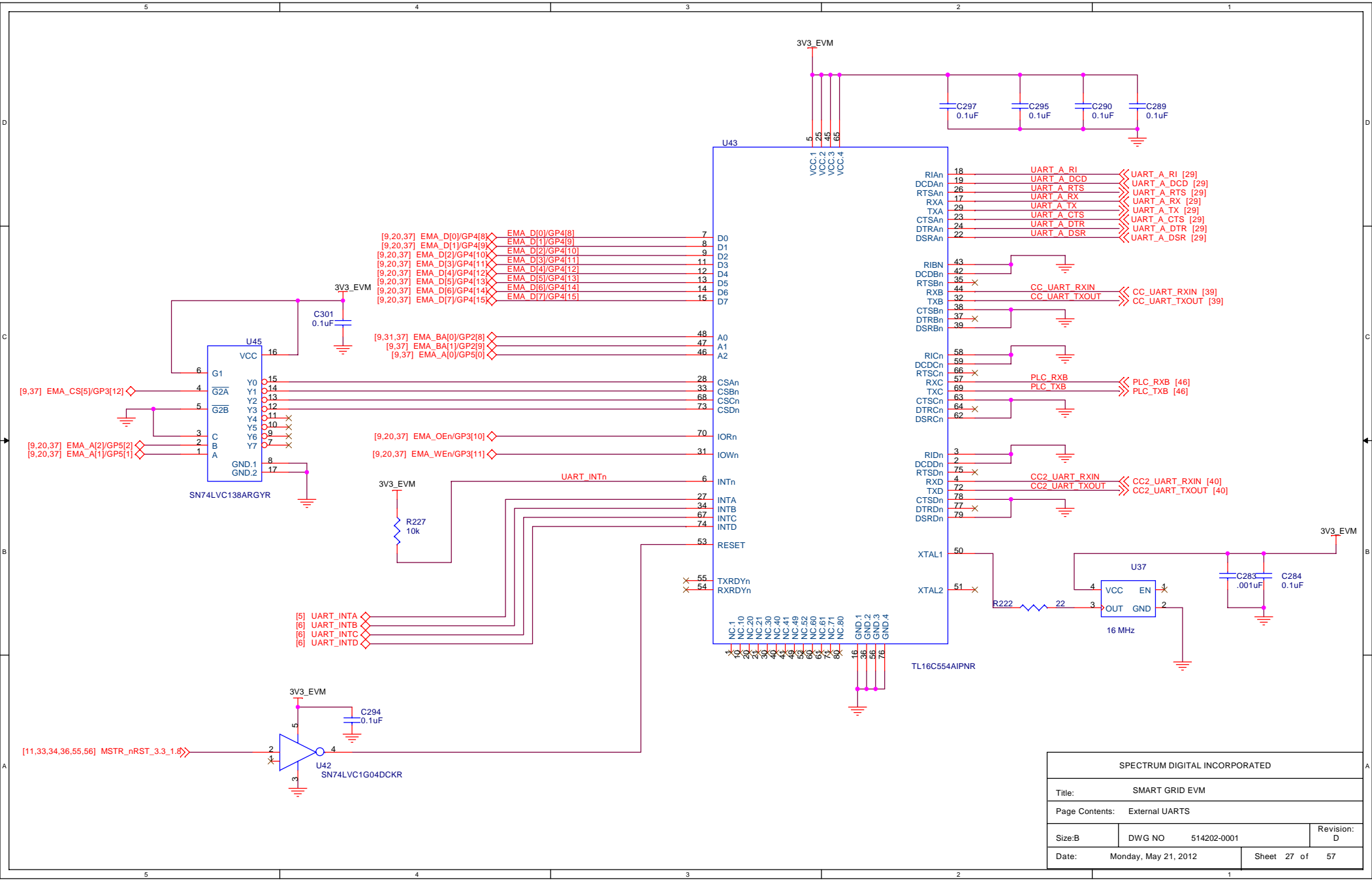
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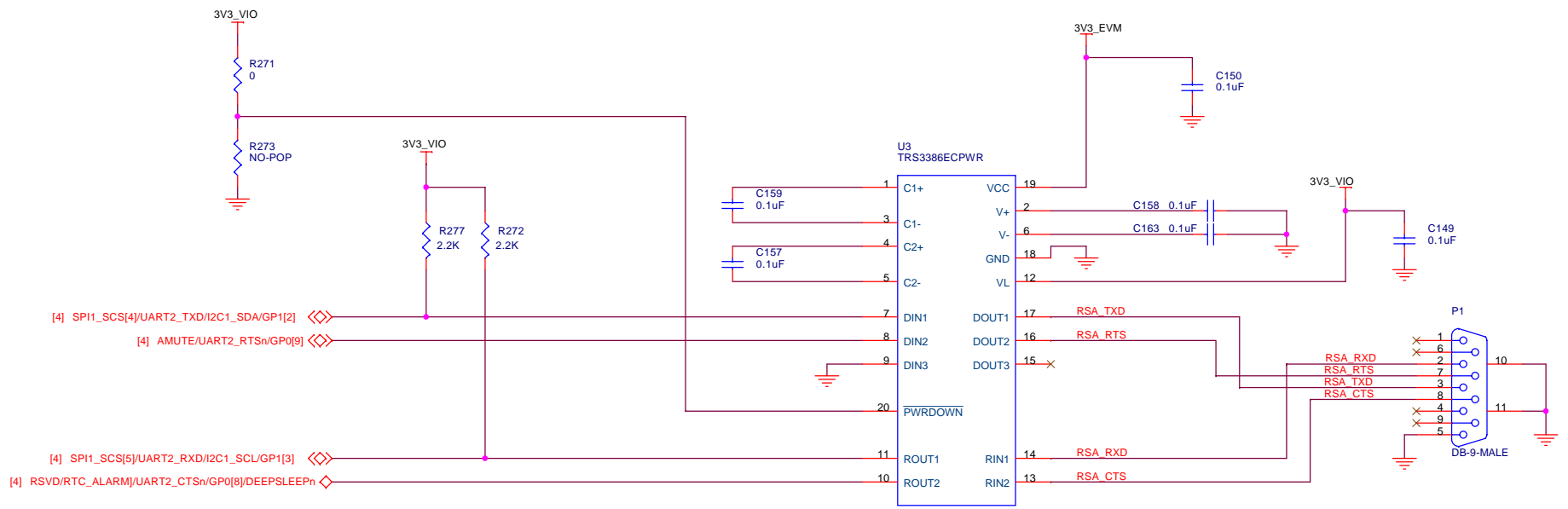
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Date:	Thursday, March 22, 2012		Sheet 25 of 57



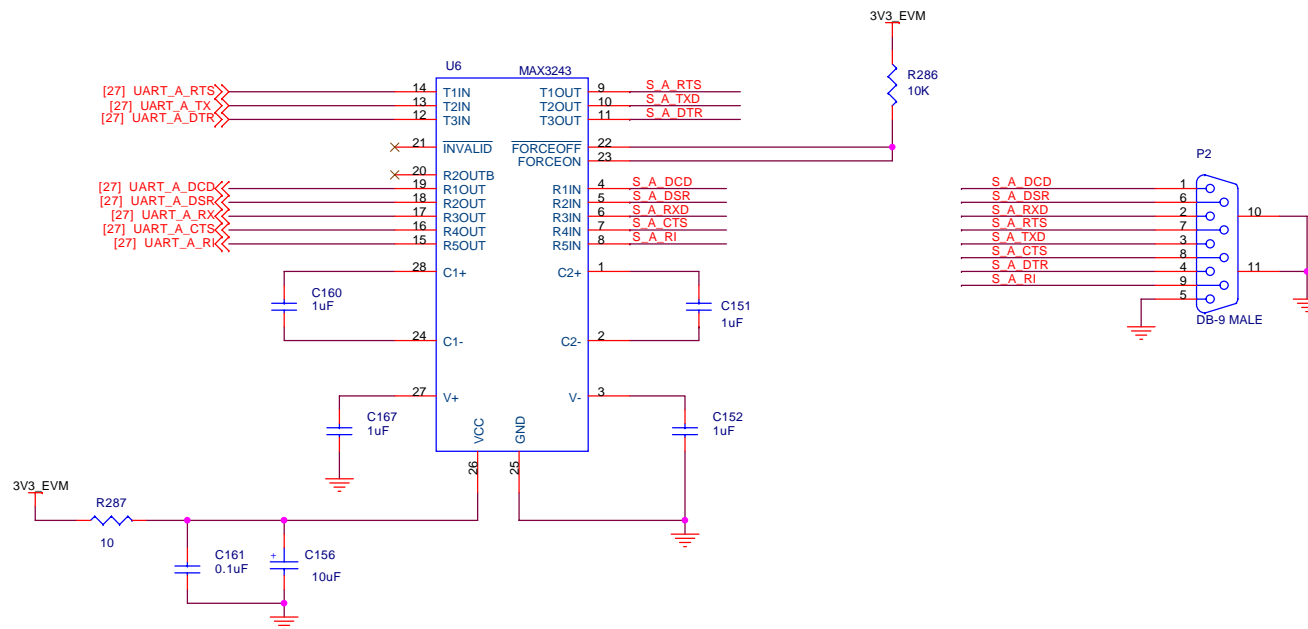
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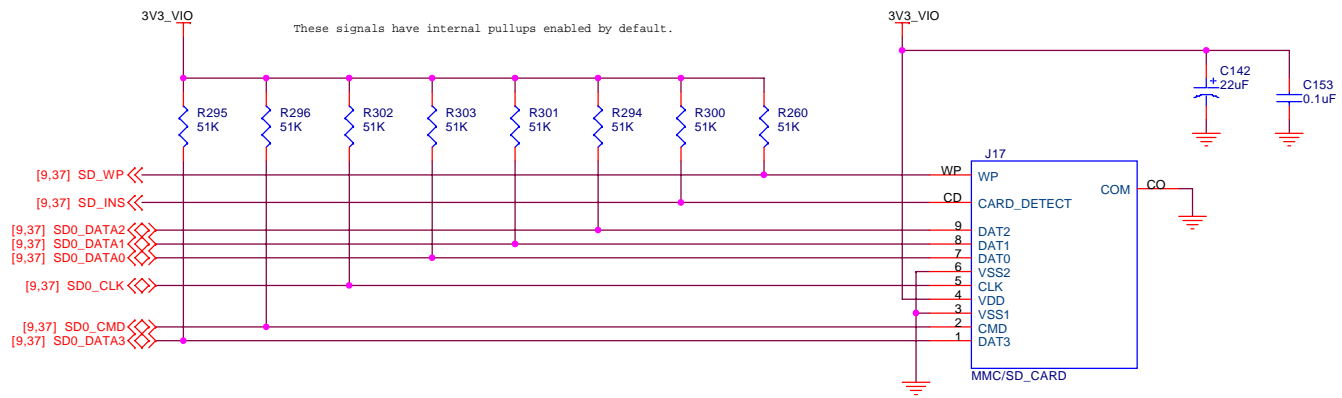
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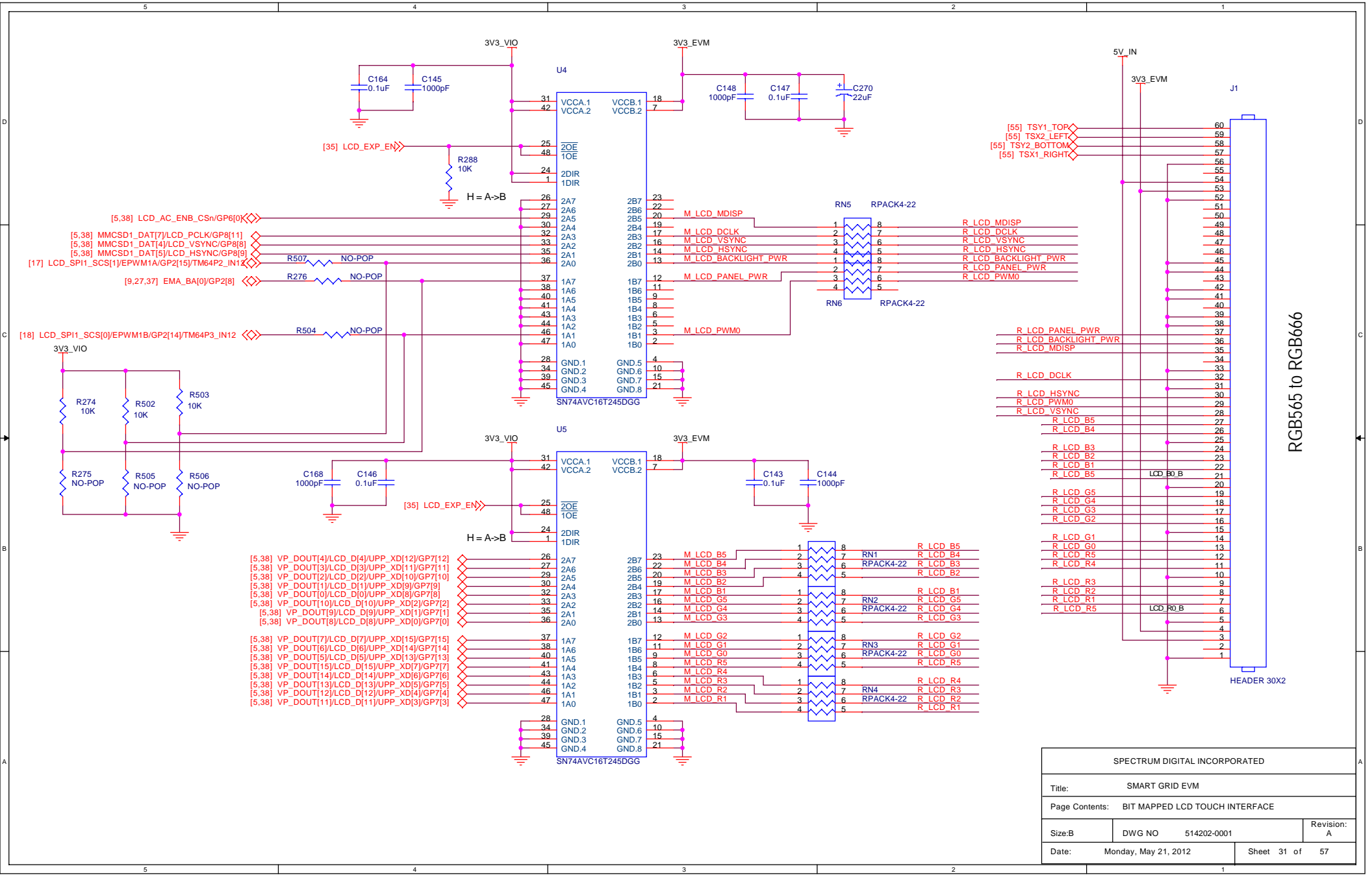
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SPECTRUM DIGITAL INCORPORATED			
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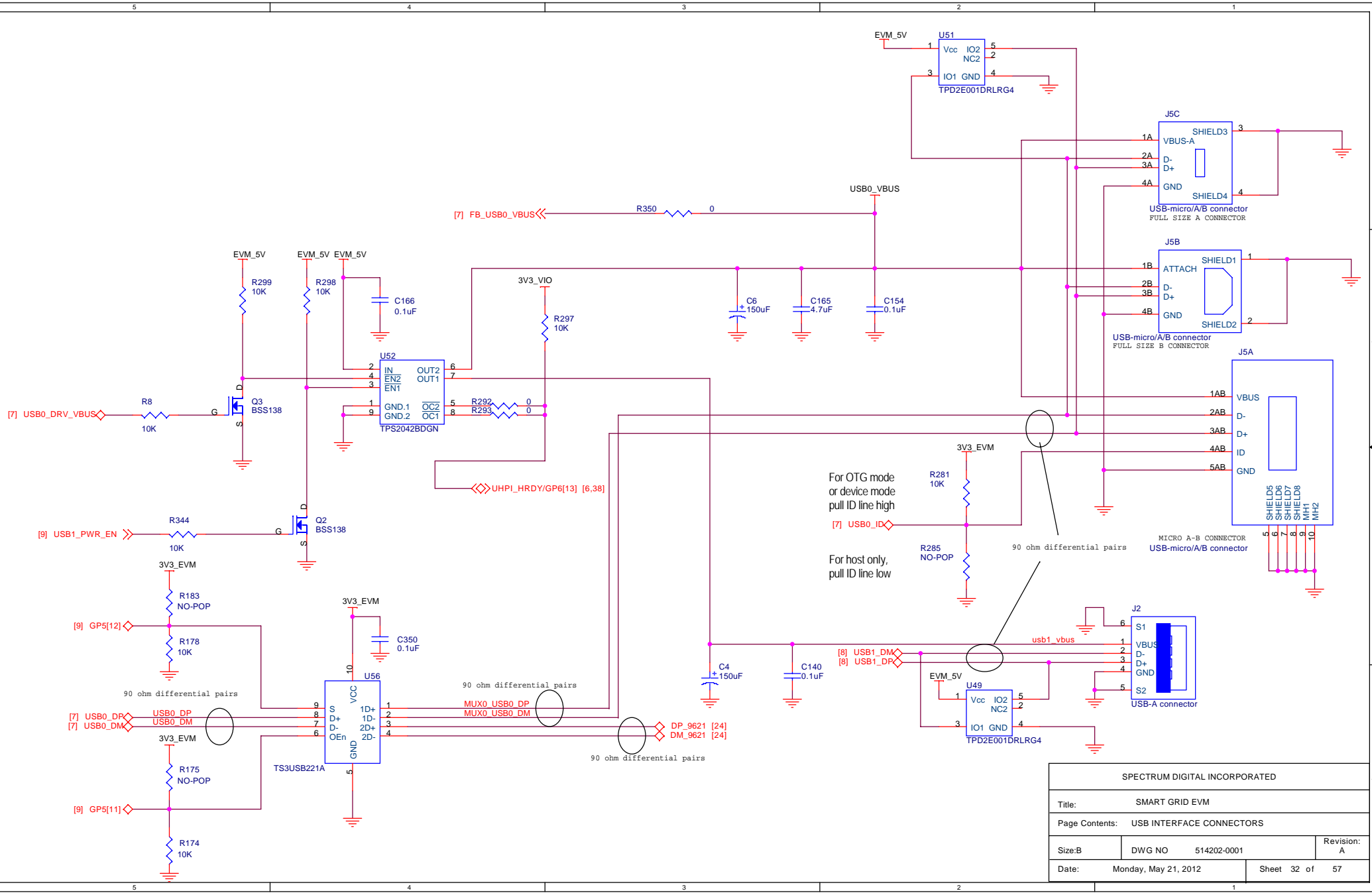


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RGB565 to RGB666

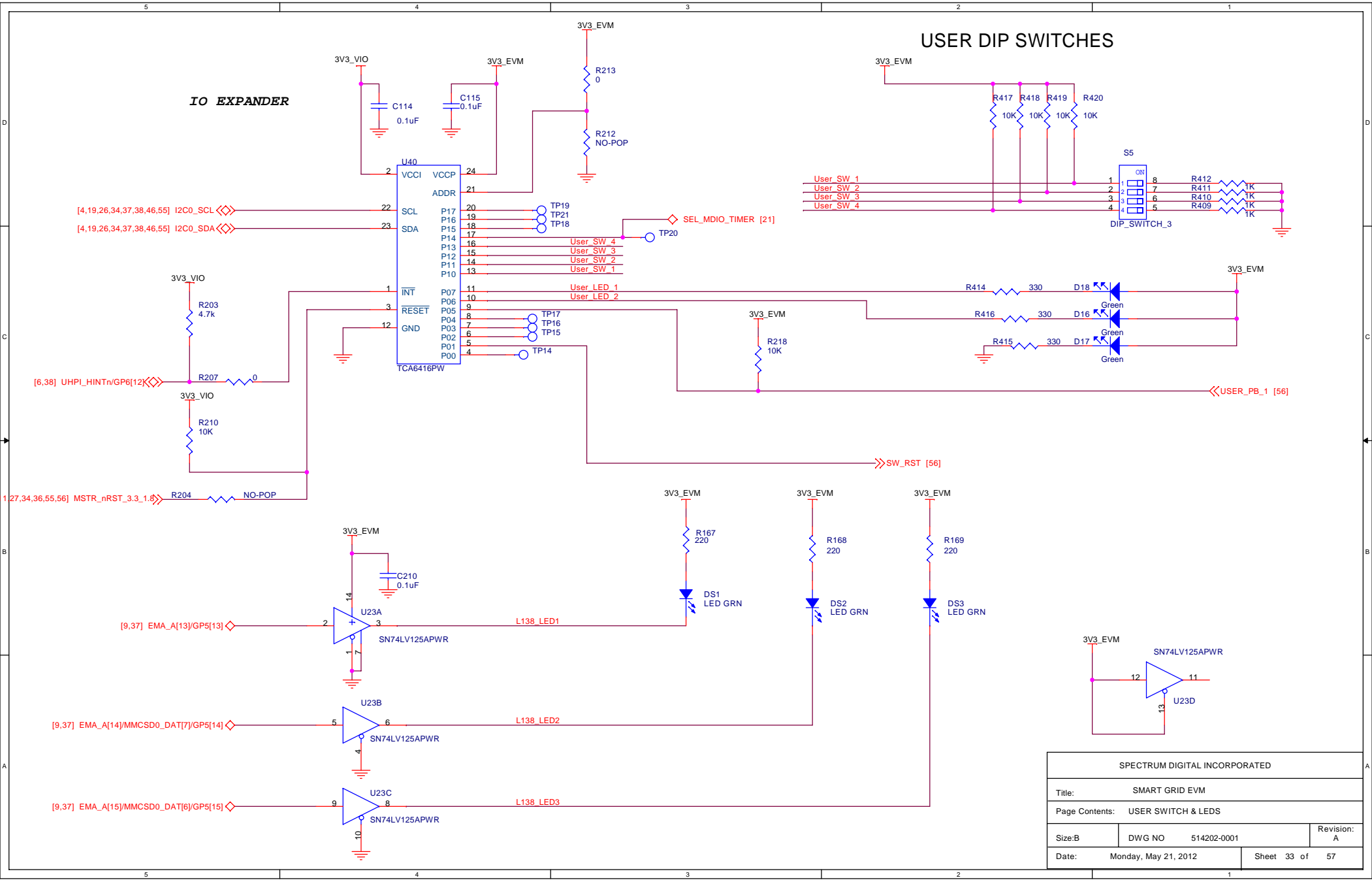
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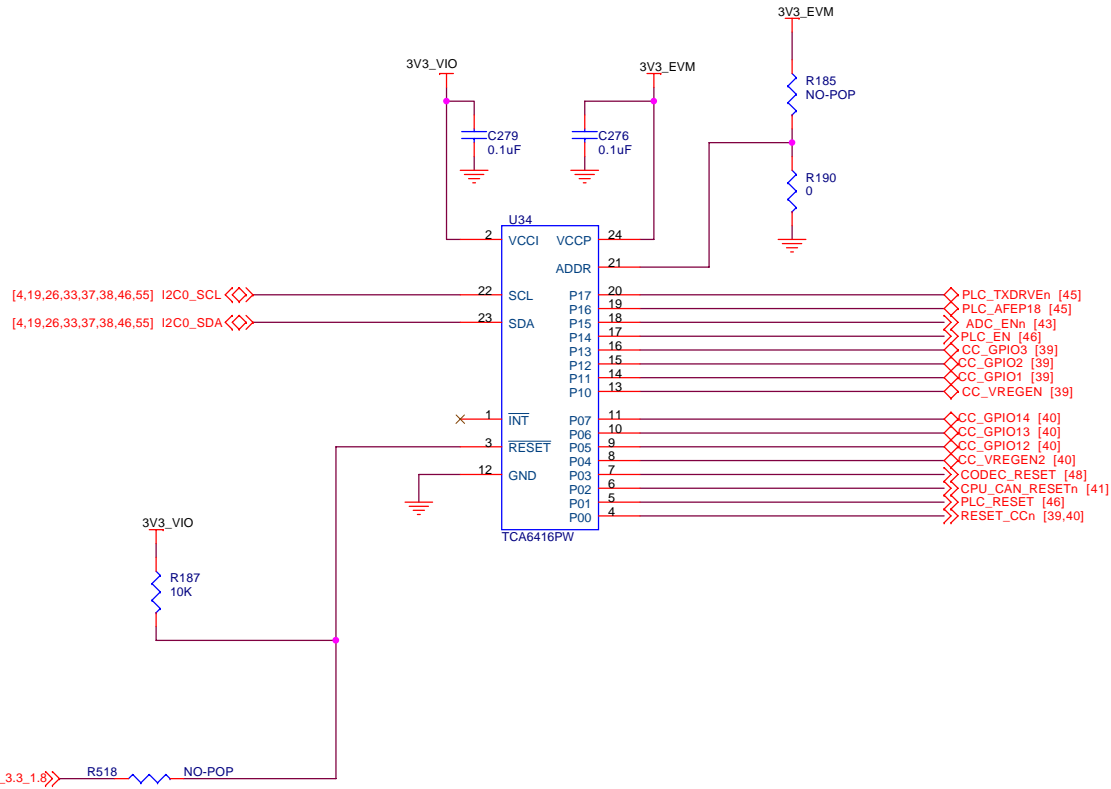
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IO EXPANDER

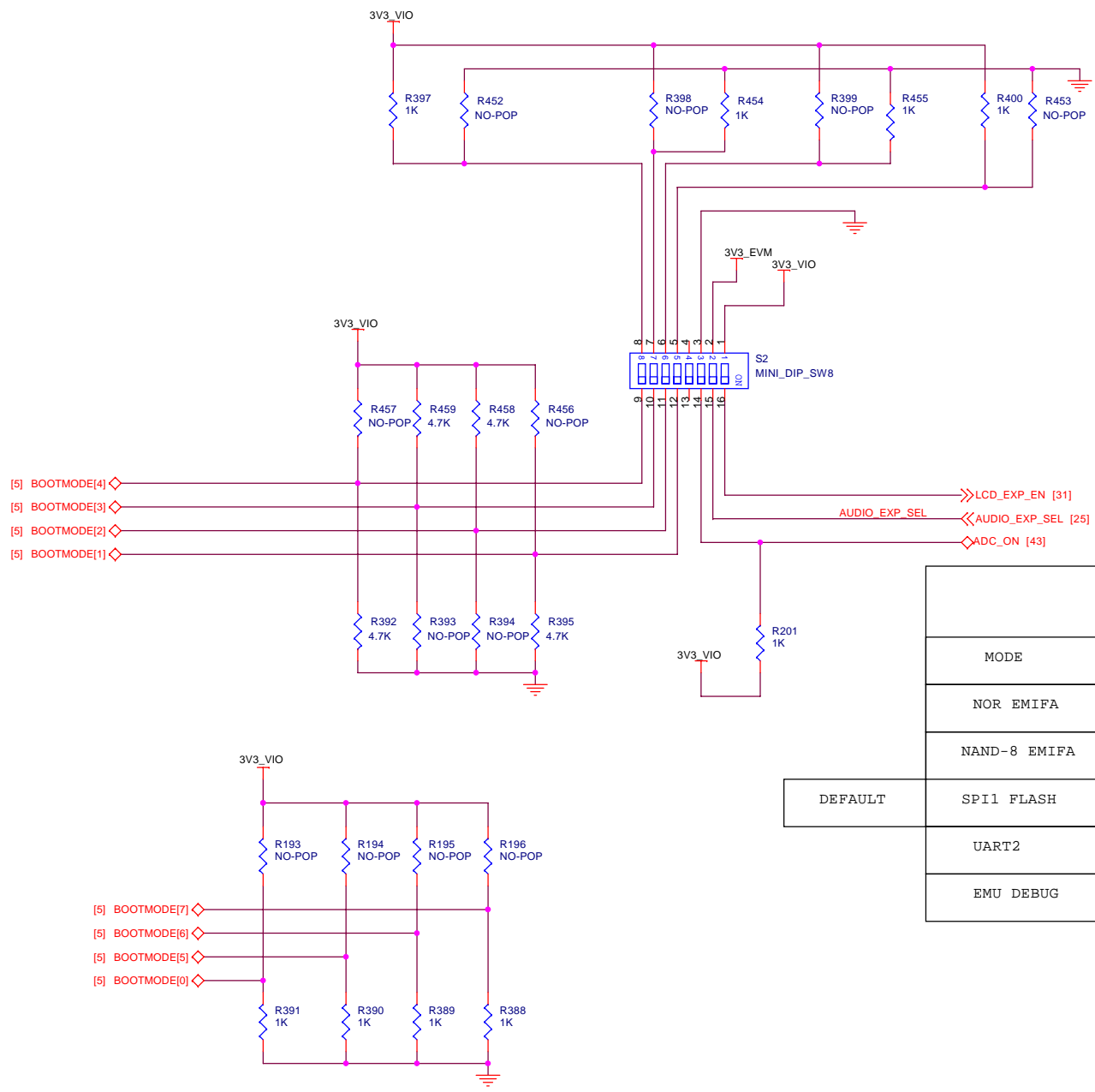
USER DIP SWITCHES



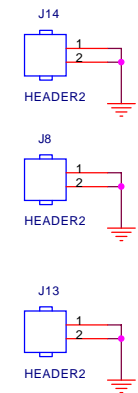
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SPECTRUM DIGITAL INCORPORATED			
Title: SMART GRID EVM			
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PROBE GND CONNECTOR

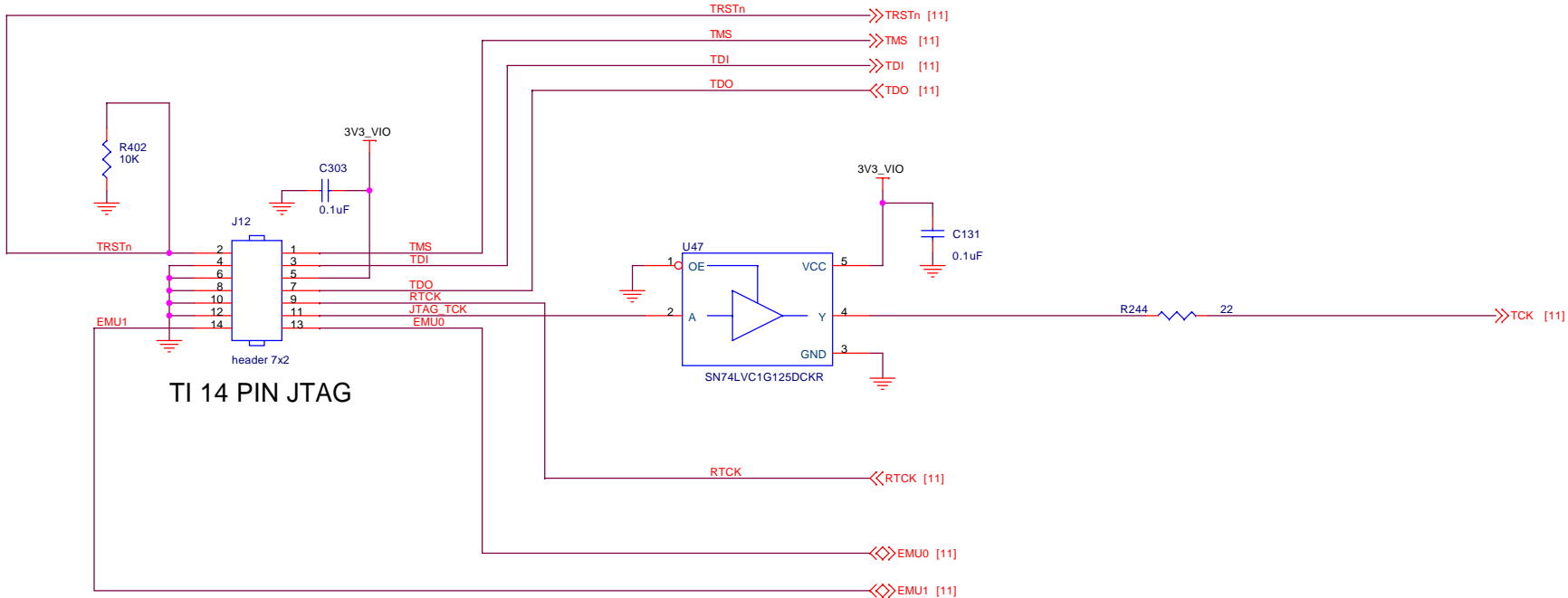


Distribute 2 pin headers around outside edges of the board

BOOT MODE				
	S2:8	S2:7	S2:6	S2:5
MODE	BOOT[4]	BOOT[3]	BOOT[1]	BOOT[0]
NOR EMIFA	OFF	ON	ON	ON
NAND-8 EMIFA	OFF	OFF	OFF	ON
DEFAULT	SPI1 FLASH	OFF	OFF	OFF
	UART2	ON	ON	OFF
	EMU DEBUG	ON	OFF	OFF
			OFF	ON

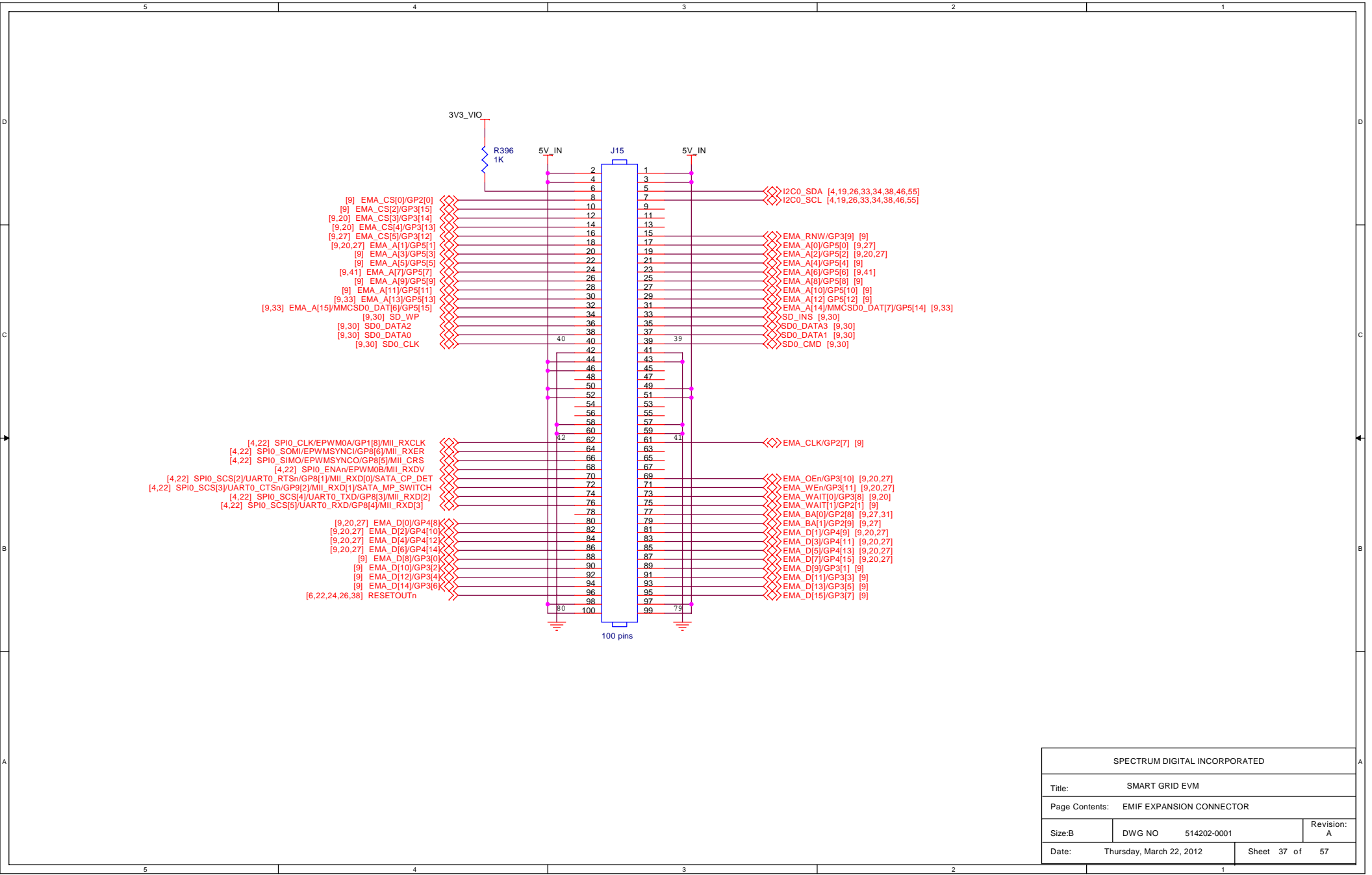
SPECTRUM DIGITAL INCORPORATED			
Title: SMART GRID EVM			
Page Contents: BOOT SWITCH			
Size: B	DWG NO	514202-0001	Revision: A
Date:	Thursday, March 22, 2012		Sheet 35 of 57

5 4 3 2 1

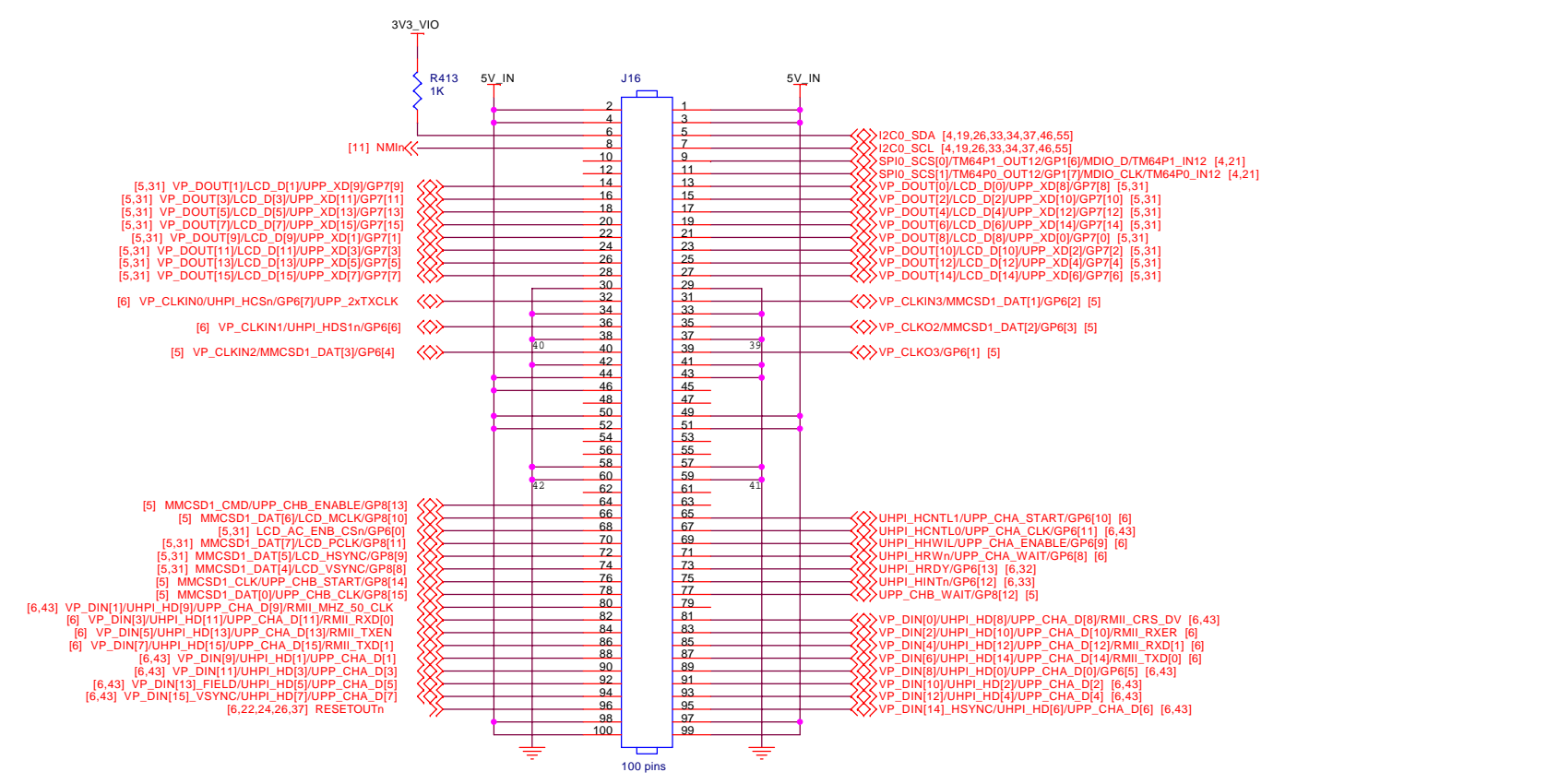


SPECTRUM DIGITAL INCORPORATED			
Title: SMART GRID EVM			
Page Contents: JTAG CONNECTORS			
Size: B	DWG NO	514202-0001	Revision: A
Date:	Thursday, March 22, 2012		Sheet 36 of 57

5 4 3 2 1

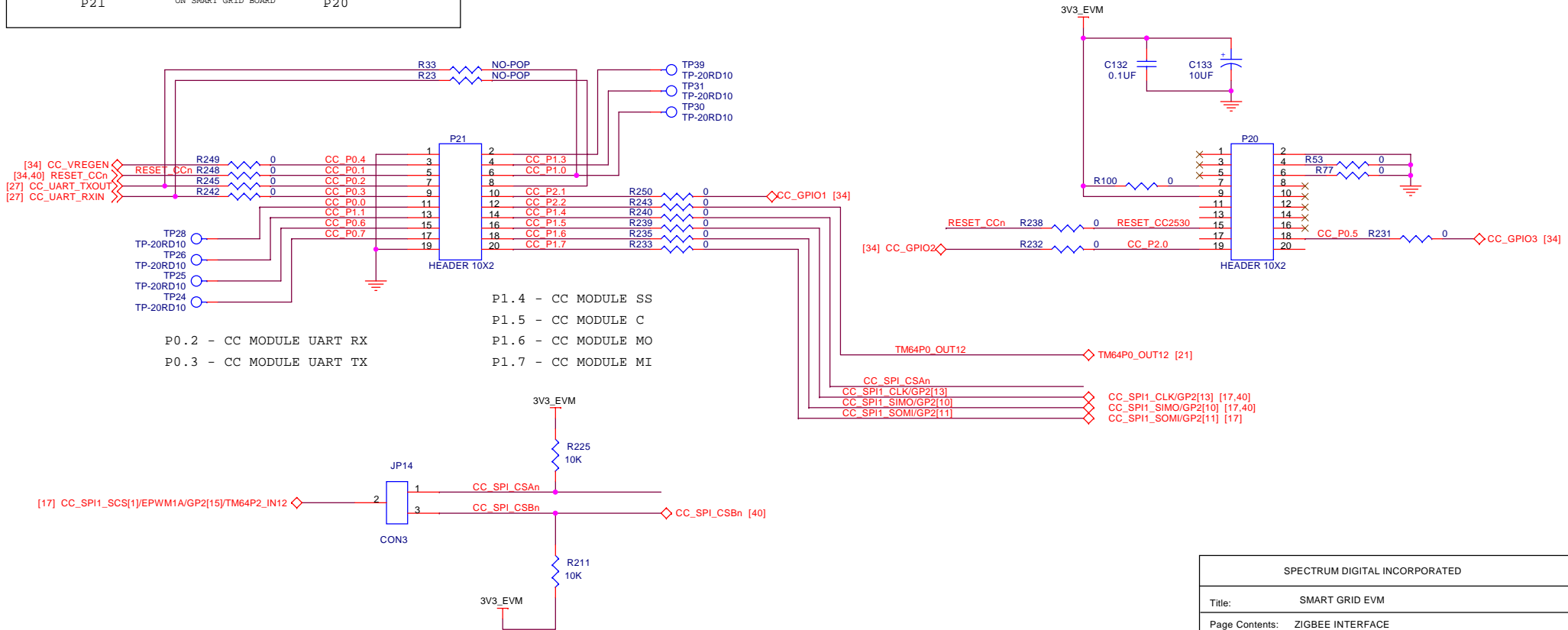
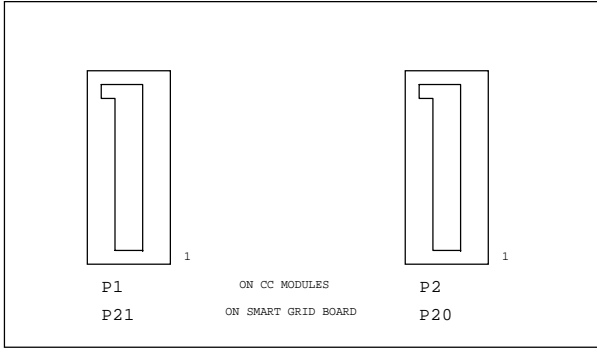


SPECTRUM DIGITAL INCORPORATED			
Title: SMART GRID EVM			
Page Contents: EMIF EXPANSION CONNECTOR			
Size: B	DWG NO	514202-0001	Revision: A
Date:	Thursday, March 22, 2012		Sheet 37 of 57



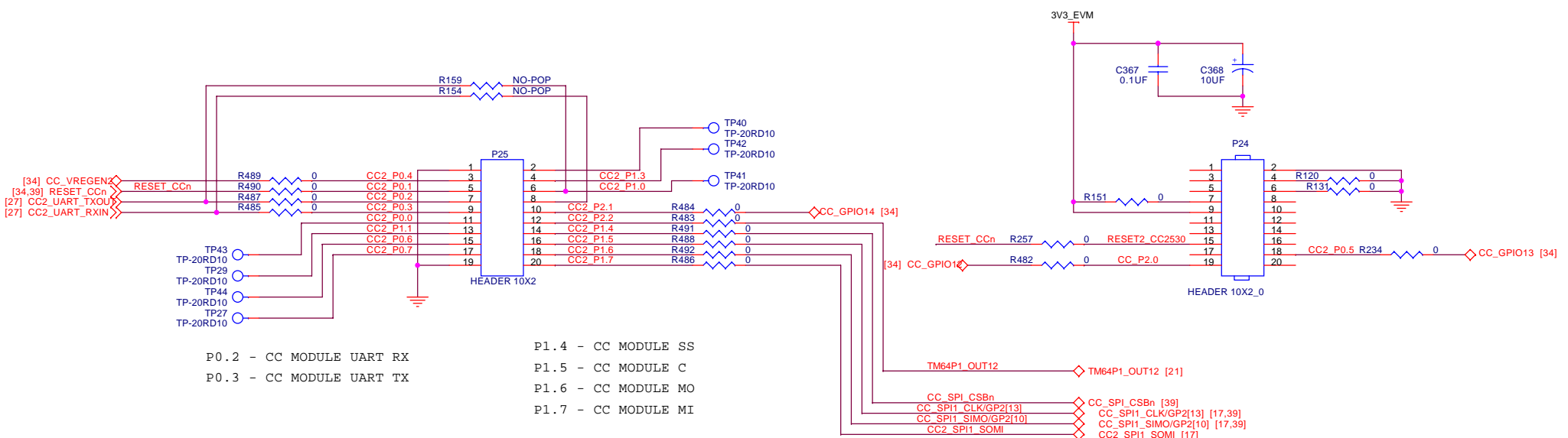
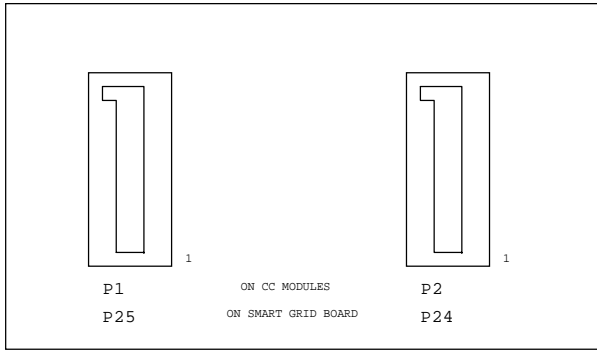
SPECTRUM DIGITAL INCORPORATED			
Title: SMART GRID EVM			
Page Contents: LCD/VPIF EXPANSION CONNECTOR			
Size: B	DWG NO	514202-0001	Revision: A
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NOTE: DIMENSIONS AND LOCATIONS OF THESE CONNECTORS MUST MEET SPECIFICATION FOR INTERFACE MODULES
 REFERENCE CC2530-EVM-UG



SPECTRUM DIGITAL INCORPORATED			
Title: SMART GRID EVM			
Page Contents: ZIGBEE INTERFACE			
Size: B	DWG NO	514202-0001	Revision: D
Date:	Monday, May 21, 2012	Sheet 39 of	57

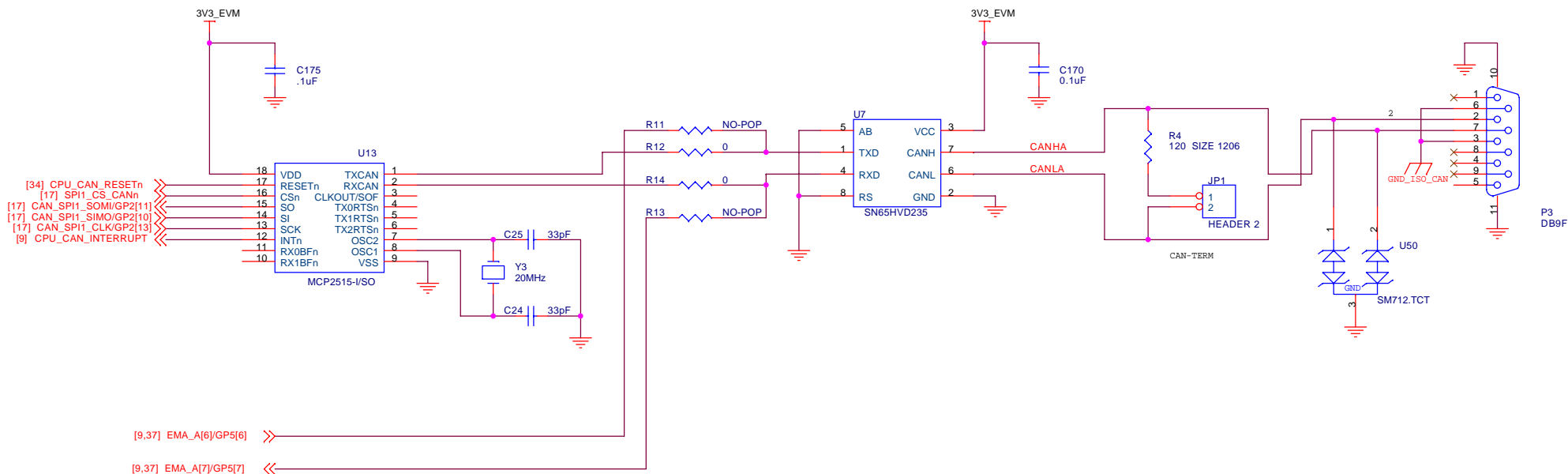
NOTE: DIMENSIONS AND LOCATIONS OF THESE CONNECTORS MUST MEET SPECIFICATION FOR INTERFACE MODULES REFERENCE



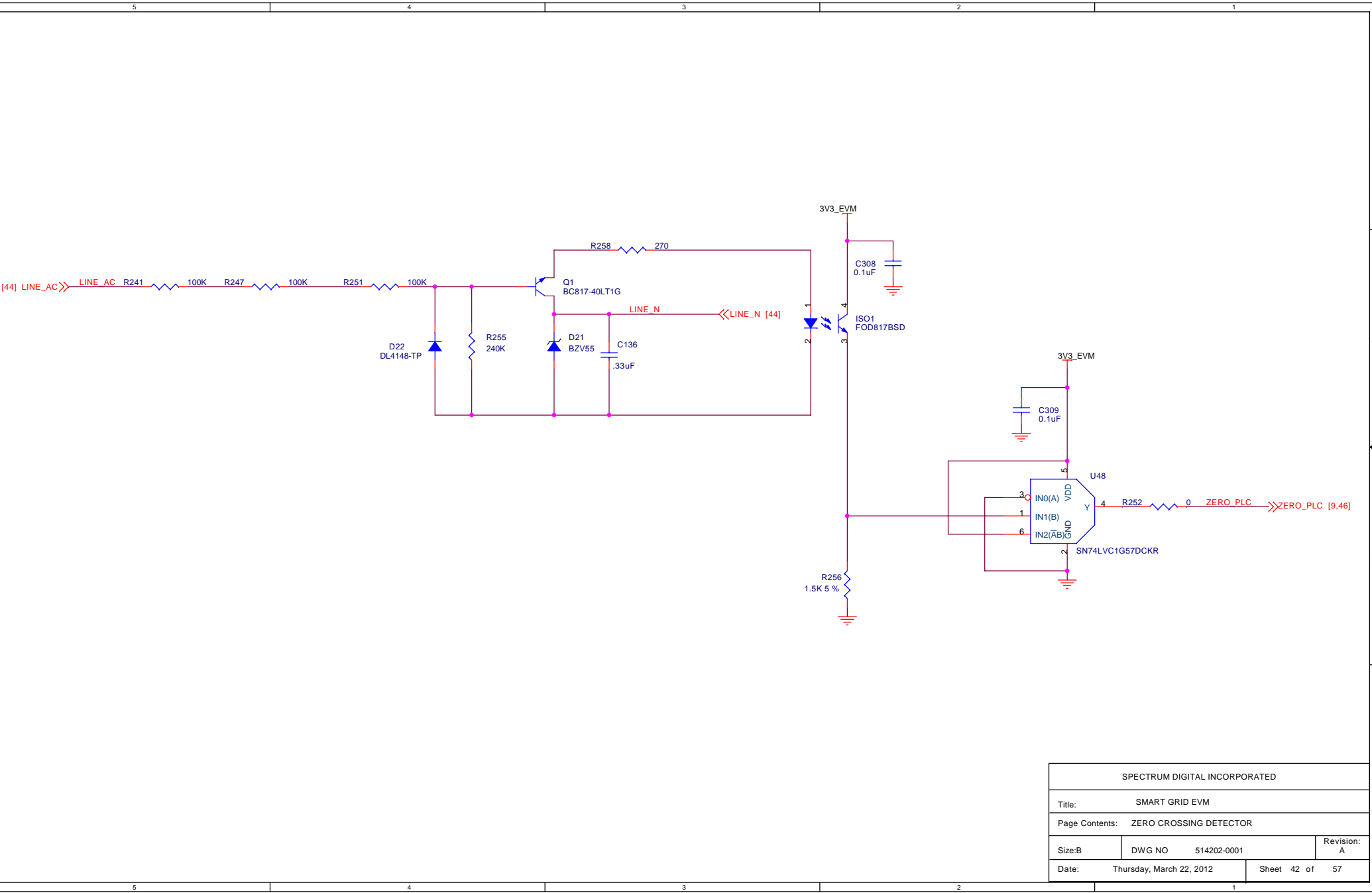
P0.2 - CC MODULE UART RX
P0.3 - CC MODULE UART TX

P1.4 - CC MODULE SS
P1.5 - CC MODULE C
P1.6 - CC MODULE MO
P1.7 - CC MODULE MI

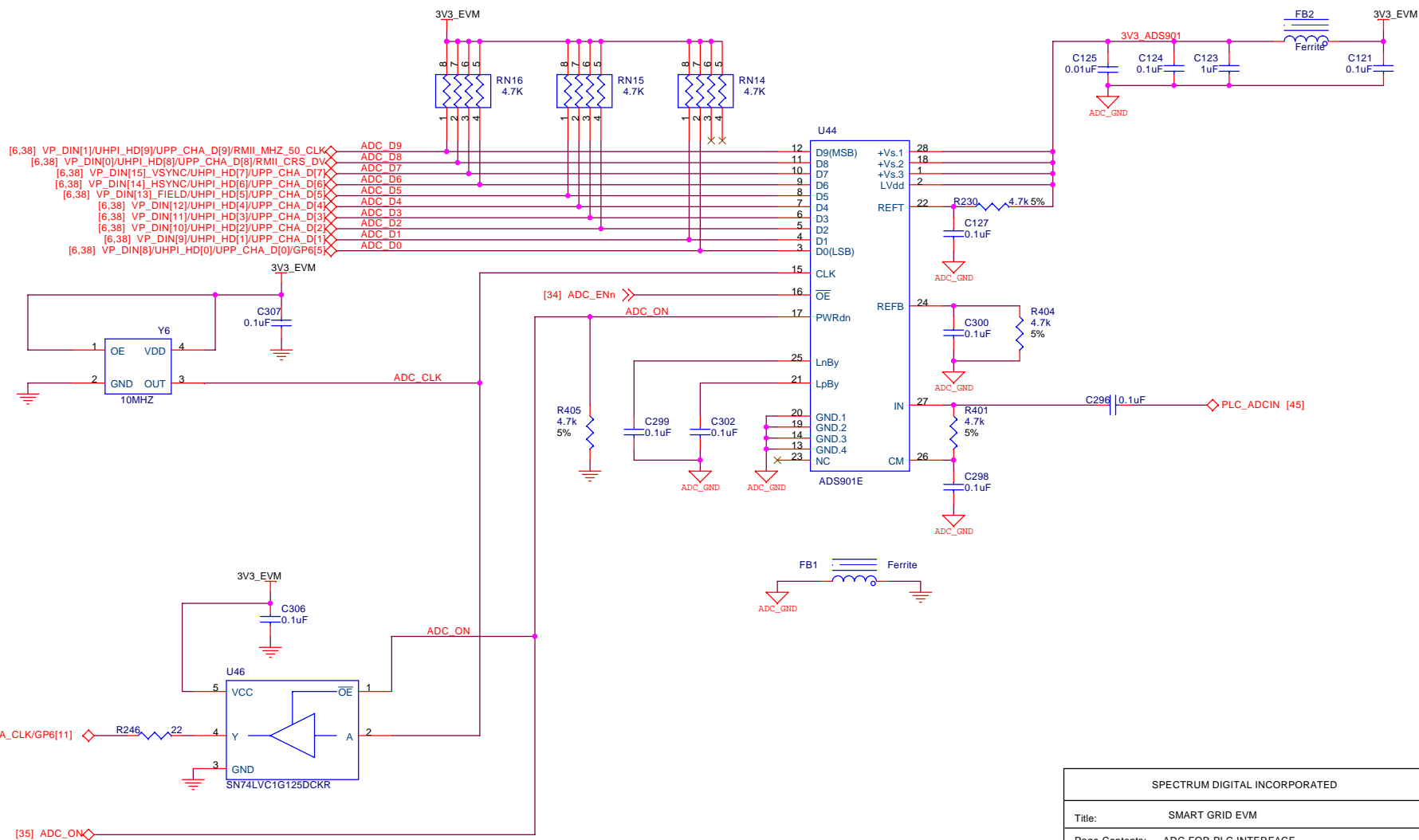
SPECTRUM DIGITAL INCORPORATED			
Title: SMART GRID EVM			
Page Contents: XXX INTERFACE			
Size: B	DWG NO: 514202-0001	Revision: D	
Date: Monday, May 21, 2012	Sheet 40 of 57		



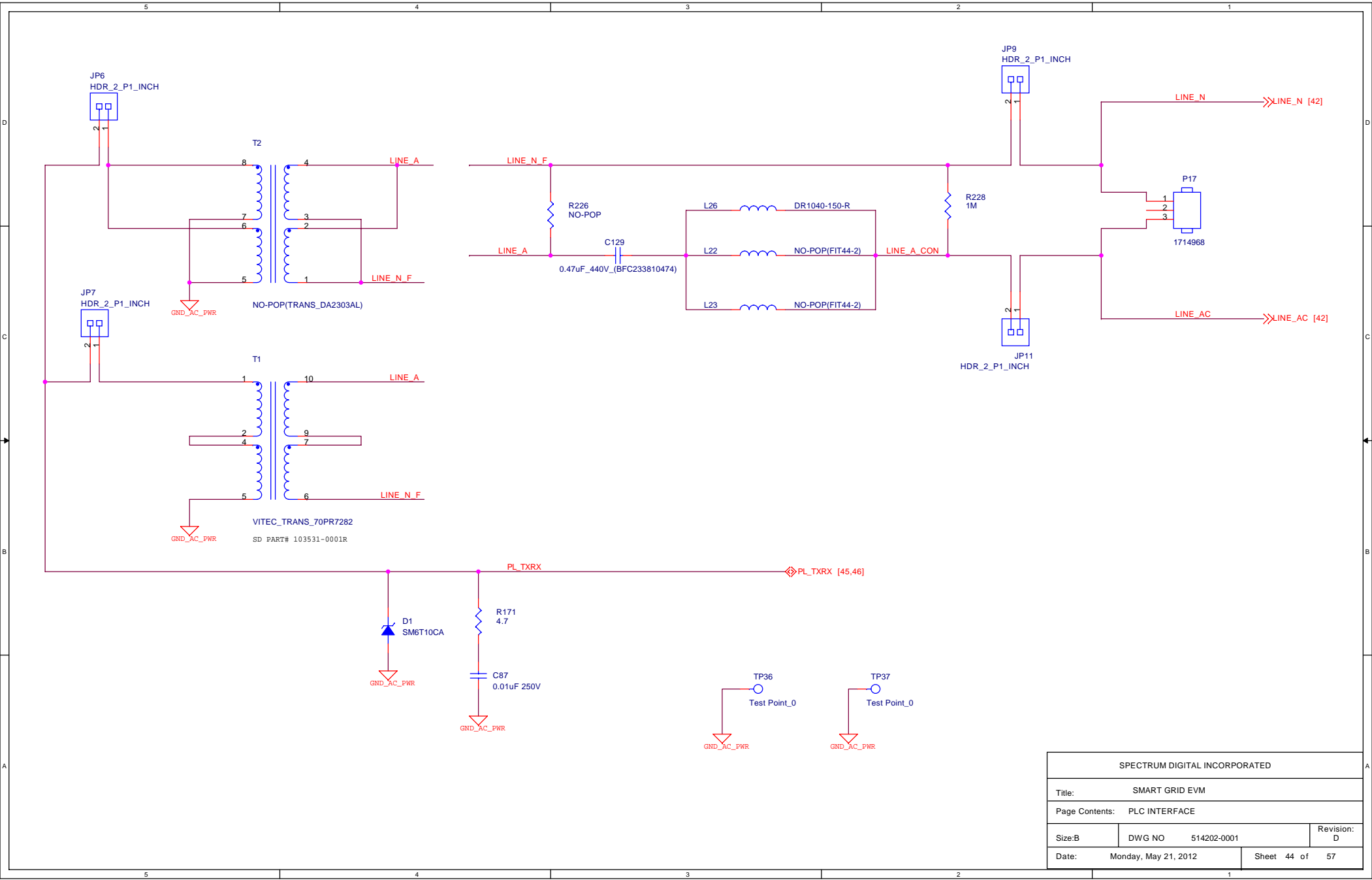
SPECTRUM DIGITAL INCORPORATED			
Title: SMART GRID EVM			
Page Contents: CAN INTERFACE			
Size: B	DWG NO	514202-0001	Revision: A
Date:	Thursday, March 22, 2012	Sheet 41 of	57



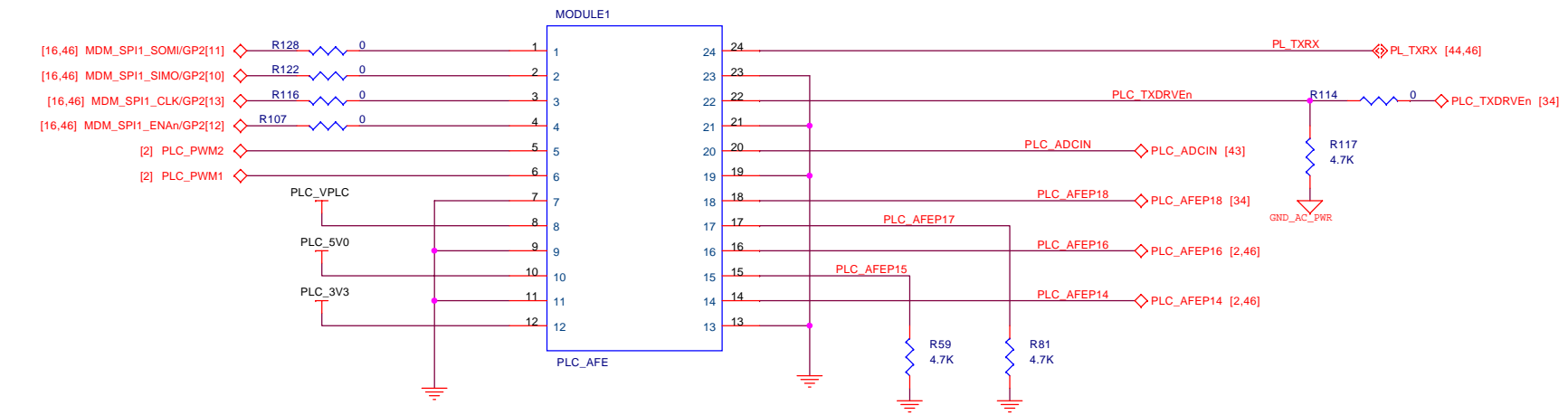
SPECTRUM DIGITAL INCORPORATED			
Title: SMART GRID EVM			
Page Contents: ZERO CROSSING DETECTOR			
Size: B	DWG NO	514202-0001	Revision: A
Date:	Thursday, March 22, 2012		Sheet 42 of 57



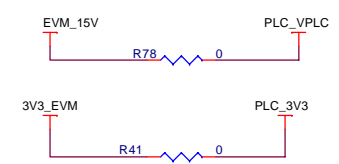
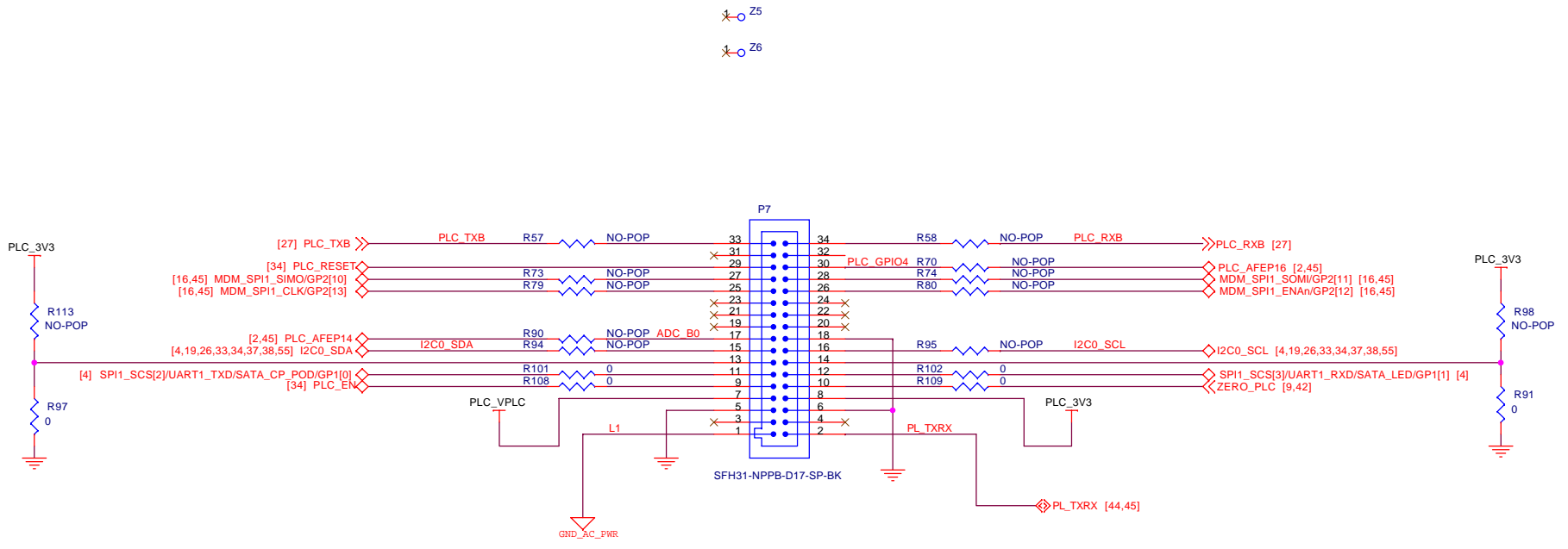
SPECTRUM DIGITAL INCORPORATED			
Title: SMART GRID EVM			
Page Contents: ADC FOR PLC INTERFACE			
Size: B	DWG NO	514202-0001	Revision: A
Date:	Thursday, March 22, 2012		Sheet 43 of 57



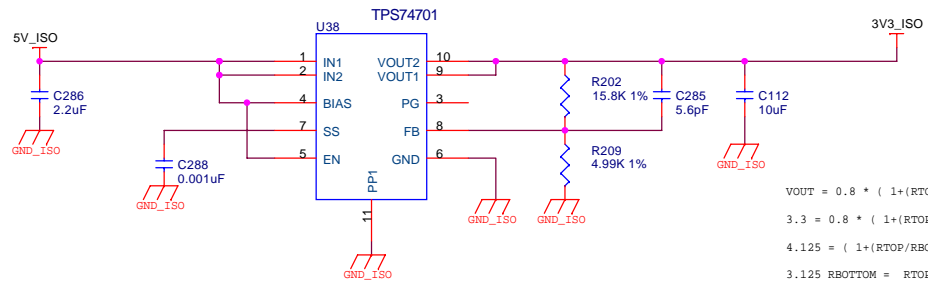
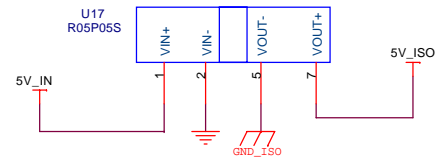
SPECTRUM DIGITAL INCORPORATED			
Title: SMART GRID EVM			
Page Contents: PLC INTERFACE			
Size: B	DWG NO	514202-0001	Revision: D
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SPECTRUM DIGITAL INCORPORATED			
Title: SMART GRID EVM			
Page Contents: PLC MODULE			
Size: B	DWG NO	514202-0001	Revision: A
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SPECTRUM DIGITAL INCORPORATED			
Title: SMART GRID EVM			
Page Contents: PLC MODULE 2			
Size: B	DWG NO	514202-0001	Revision: A
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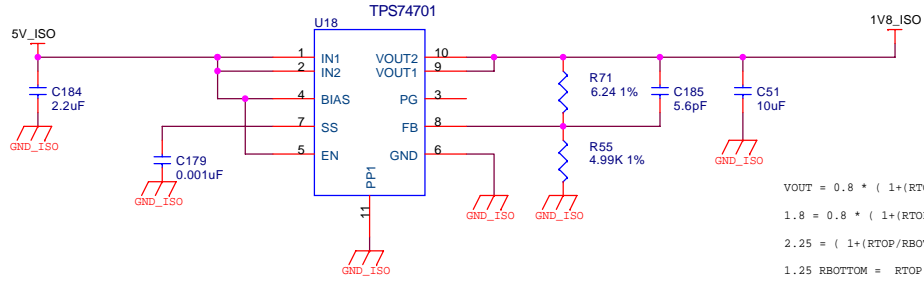
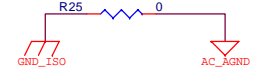
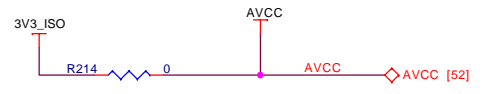


$$V_{OUT} = 0.8 * (1 + (R_{TOP}/R_{BOTTOM}))$$

$$3.3 = 0.8 * (1 + (R_{TOP}/R_{BOTTOM}))$$

$$4.125 = (1 + (R_{TOP}/R_{BOTTOM}))$$

$$3.125 R_{BOTTOM} = R_{TOP}$$

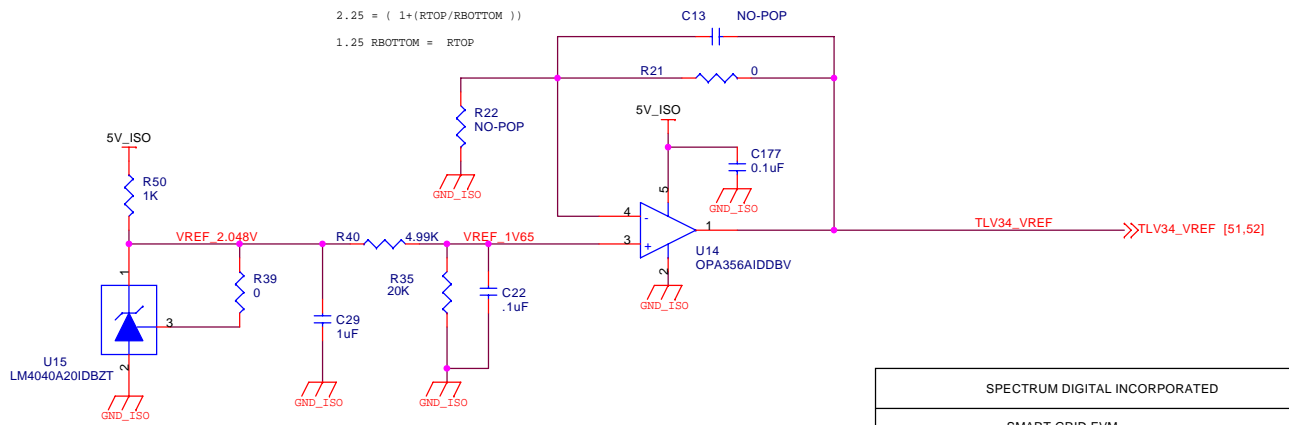


$$V_{OUT} = 0.8 * (1 + (R_{TOP}/R_{BOTTOM}))$$

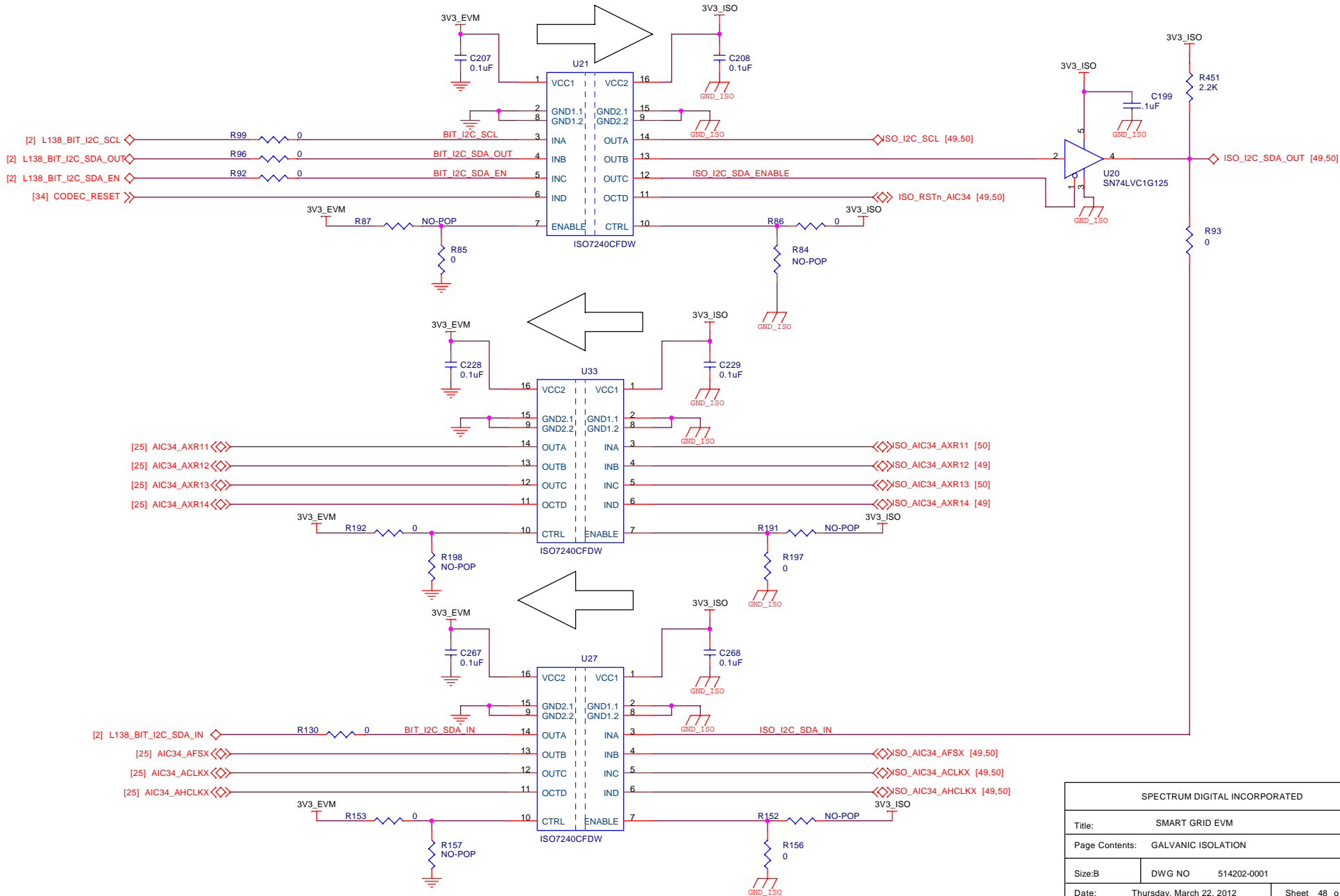
$$1.8 = 0.8 * (1 + (R_{TOP}/R_{BOTTOM}))$$

$$2.25 = (1 + (R_{TOP}/R_{BOTTOM}))$$

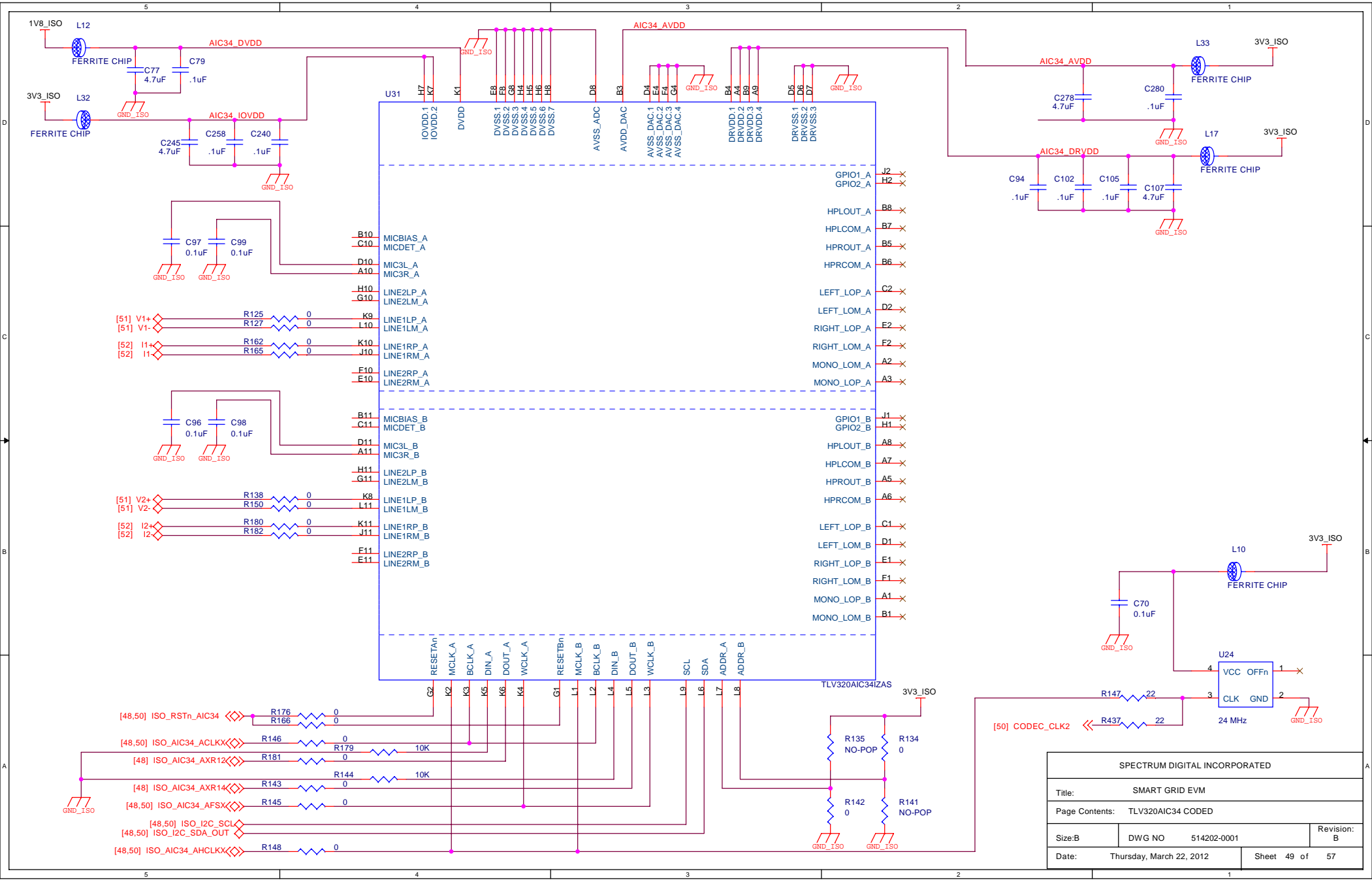
$$1.25 R_{BOTTOM} = R_{TOP}$$



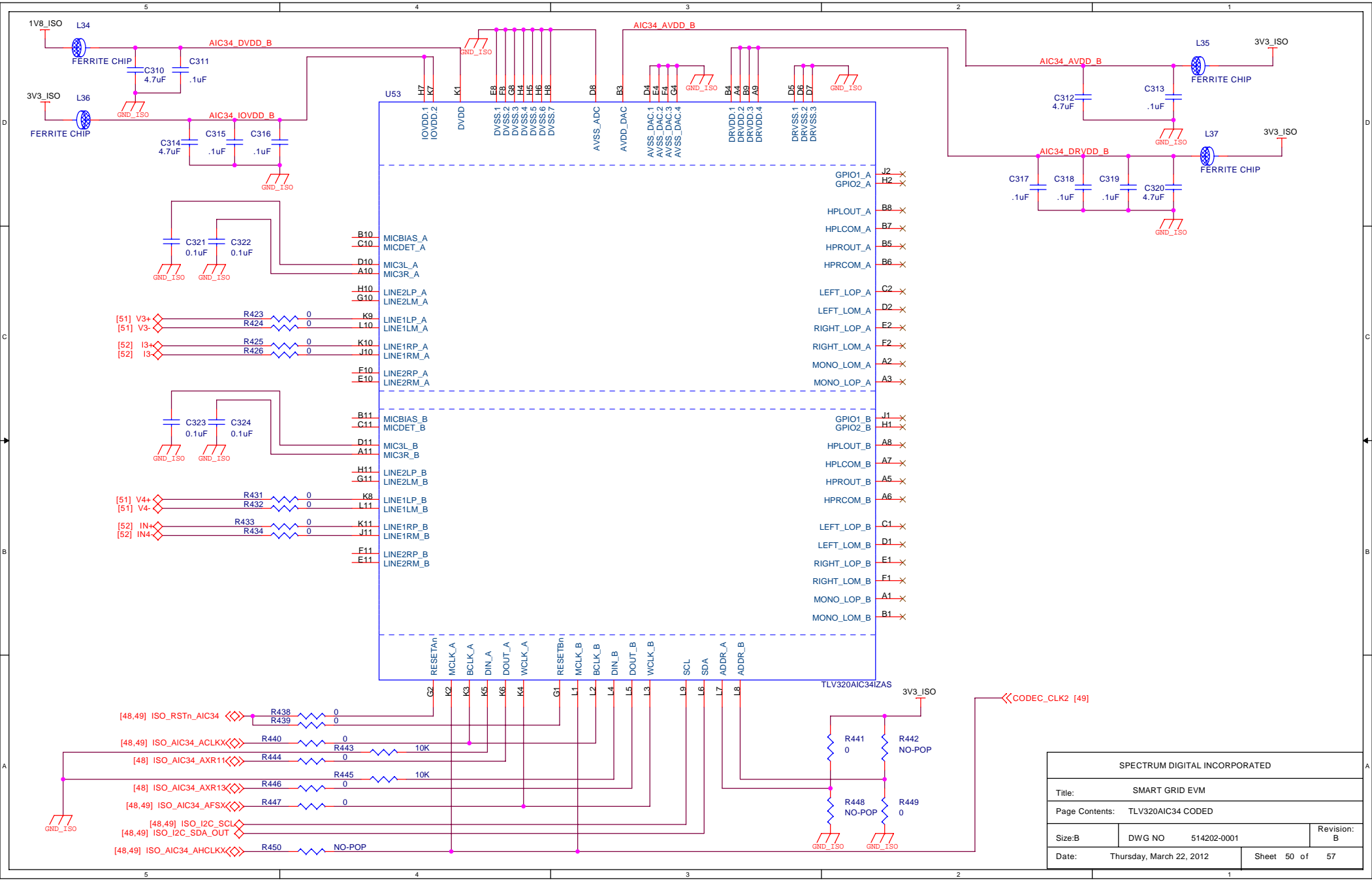
SPECTRUM DIGITAL INCORPORATED			
Title: SMART GRID EVM			
Page Contents: ISOLATION POWER			
Size: B	DWG NO: 514202-0001	Revision: D	
Date: Monday, May 21, 2012	Sheet 47 of 57		



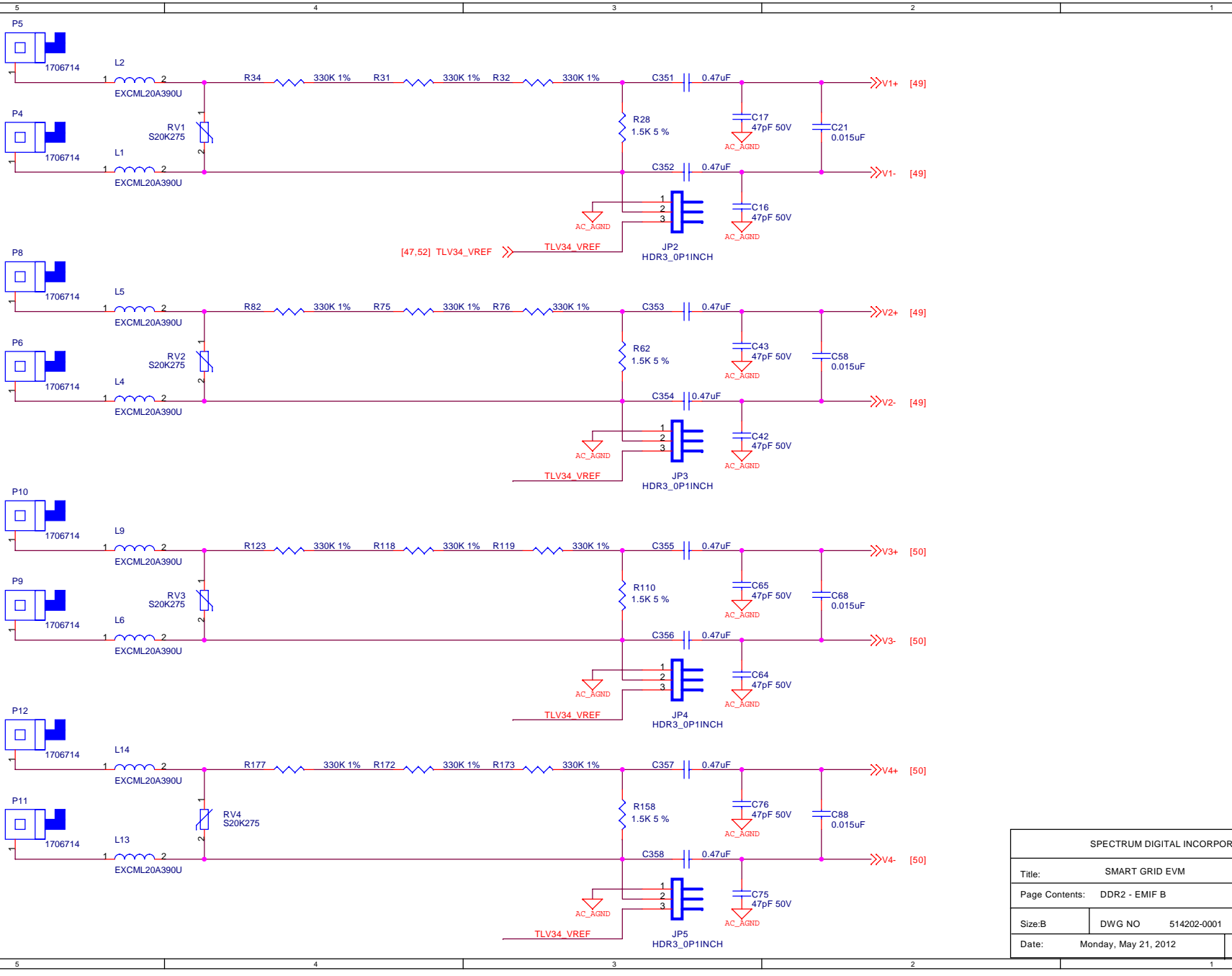
SPECTRUM DIGITAL INCORPORATED			
Title: SMART GRID EVM			
Page Contents: GALVANIC ISOLATION			
Size: B	DWG NO	514202-0001	Revision: B
Date:	Thursday, March 22, 2012	Sheet 48 of	57



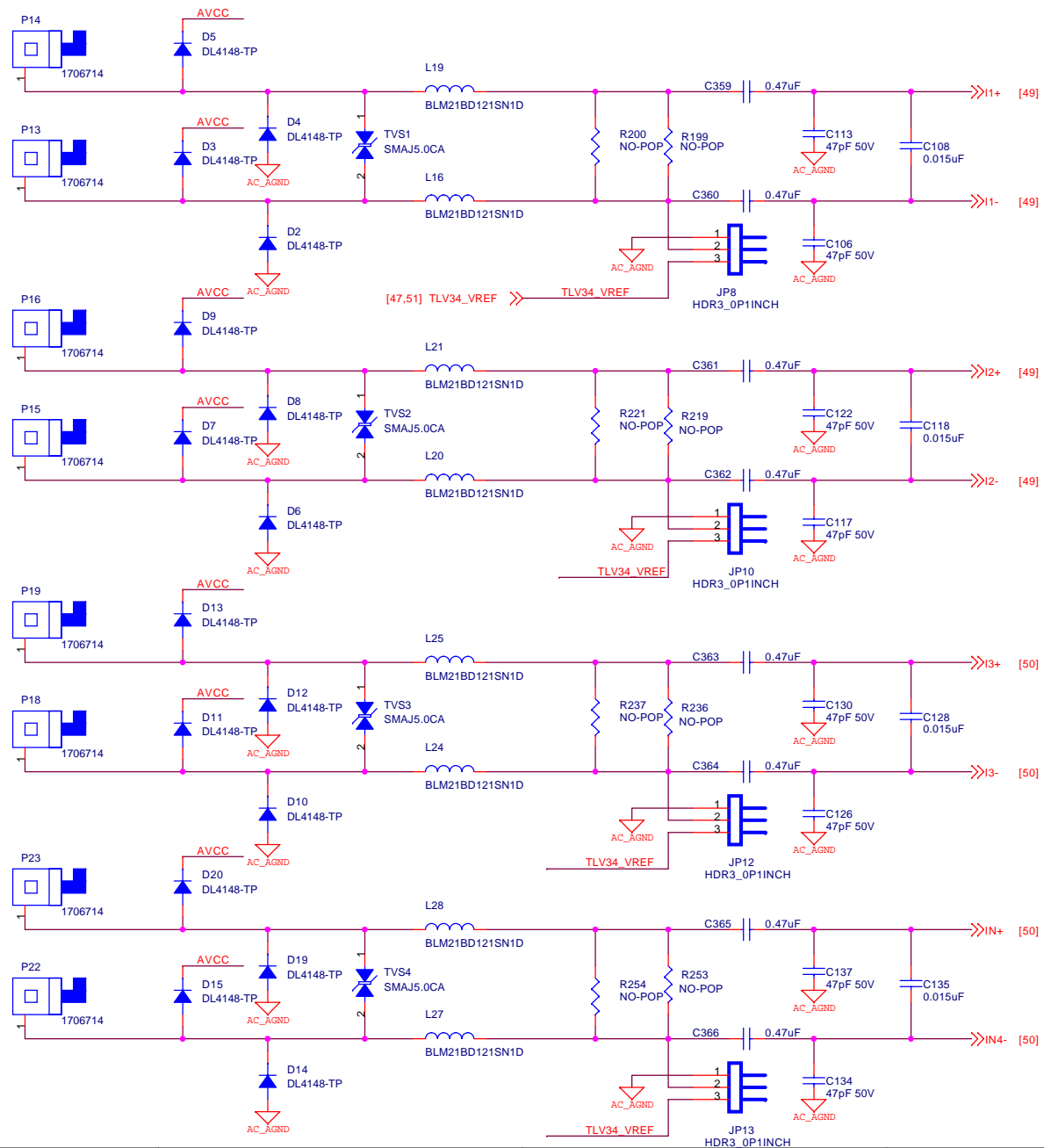
SPECTRUM DIGITAL INCORPORATED			
Title: SMART GRID EVM			
Page Contents: TLV320AIC34 CODED			
Size: B	DWG NO: 514202-0001	Revision: B	
Date: Thursday, March 22, 2012	Sheet 49 of 57		



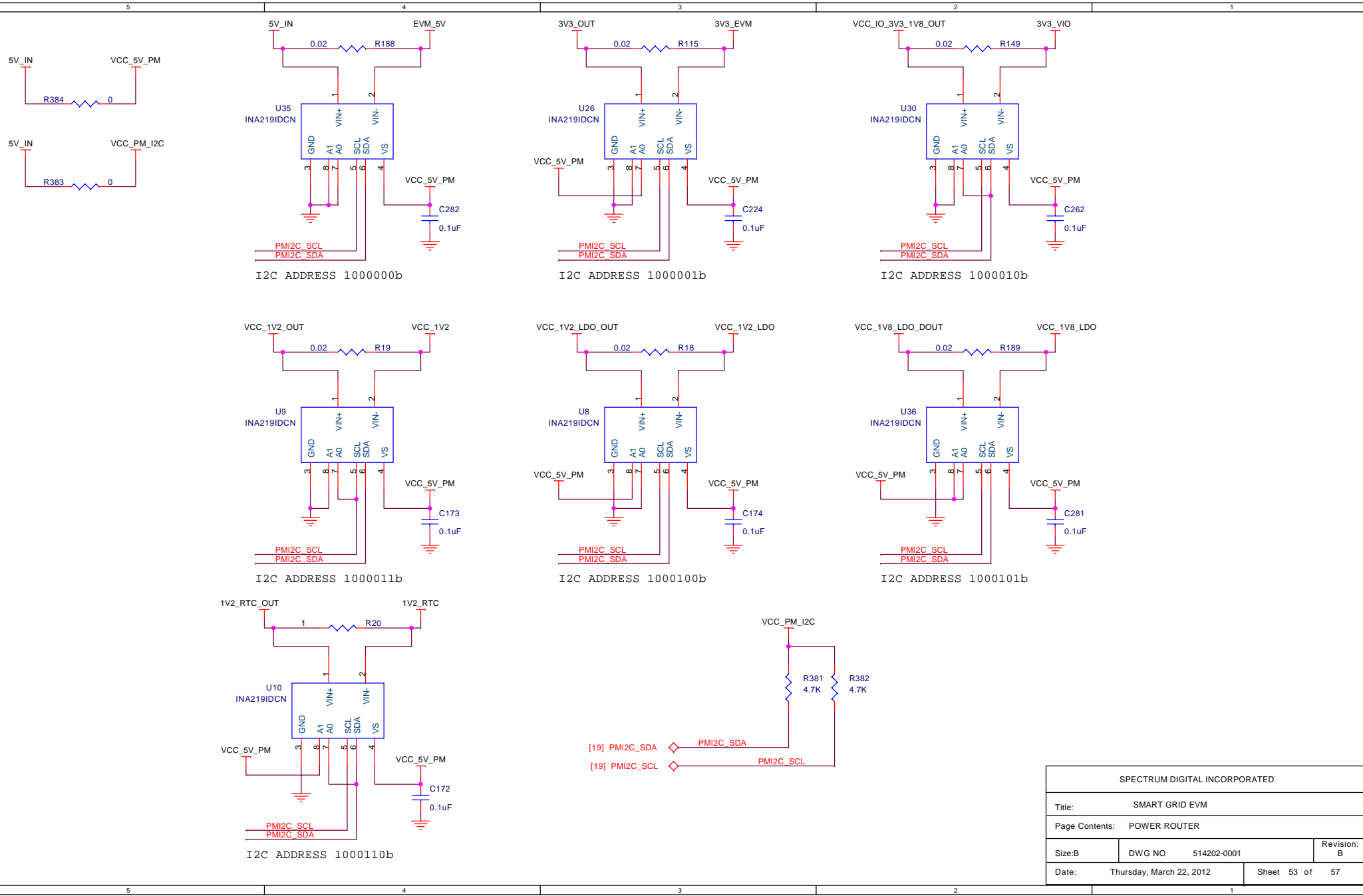
SPECTRUM DIGITAL INCORPORATED			
Title: SMART GRID EVM			
Page Contents: TLV320AIC34 CODED			
Size: B	DWG NO	514202-0001	Revision: B
Date:	Thursday, March 22, 2012	Sheet	50 of 57



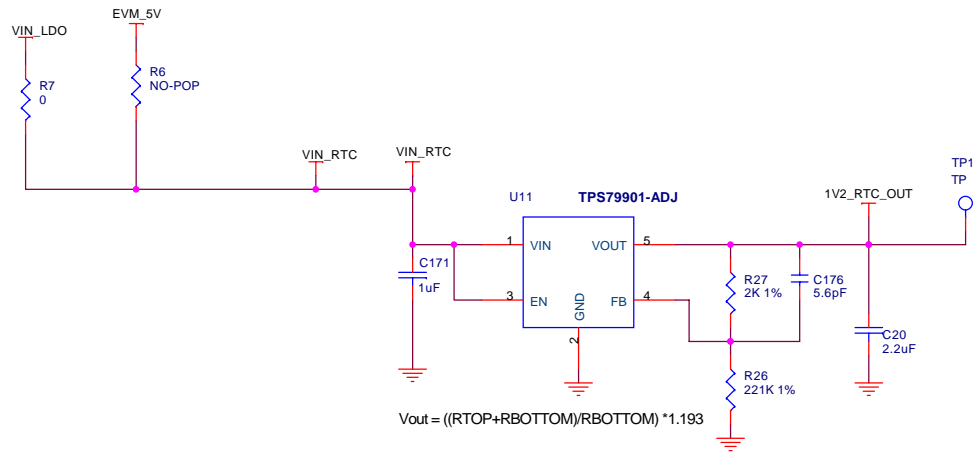
SPECTRUM DIGITAL INCORPORATED			
Title: SMART GRID EVM			
Page Contents: DDR2 - EMIF B			
Size: B	DWG NO	514202-0001	Revision: A
Date:	Monday, May 21, 2012	Sheet	51 of 57



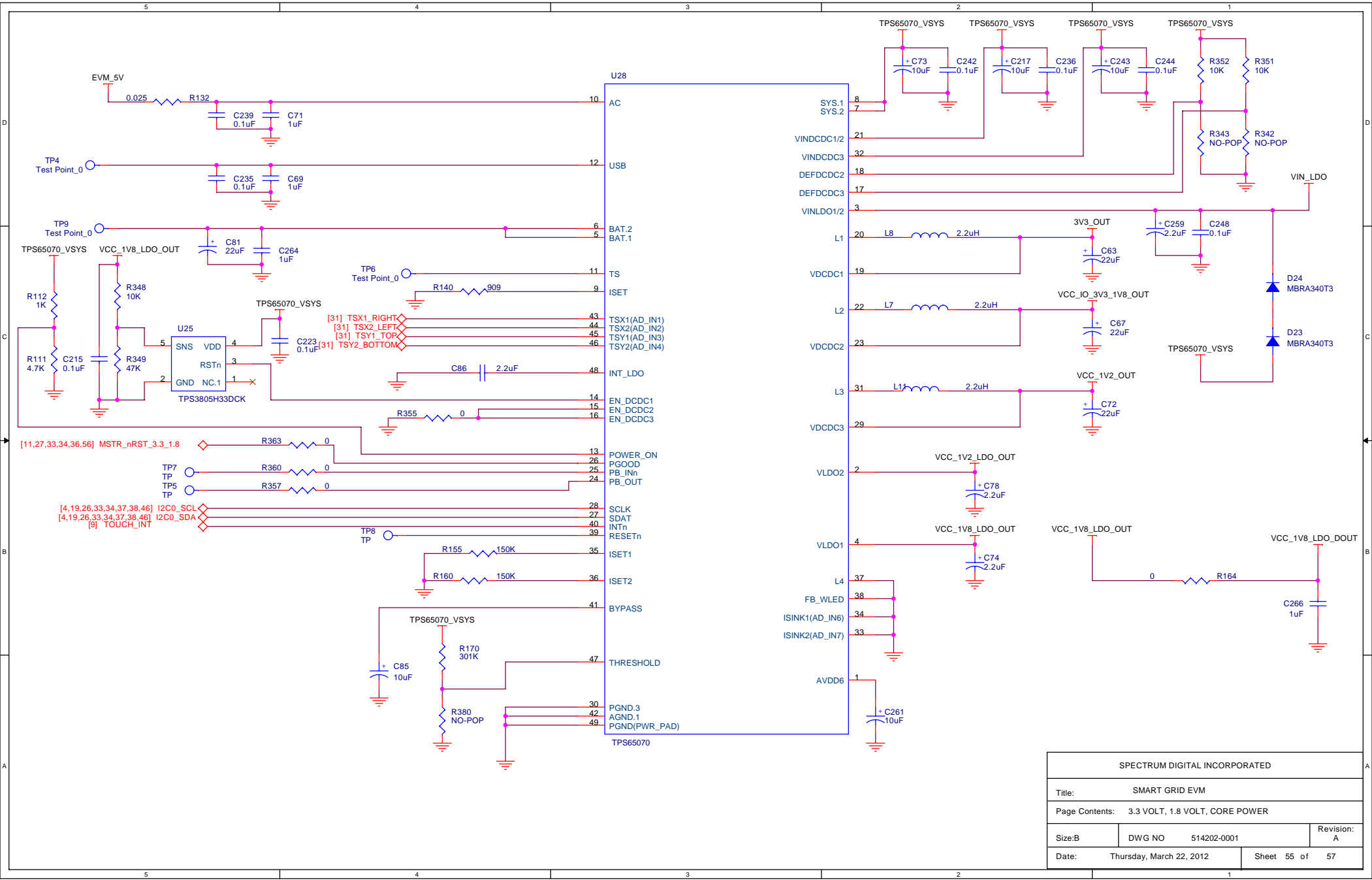
SPECTRUM DIGITAL INCORPORATED			
Title: SMART GRID EVM			
Page Contents: DDR2 - EMIF B			
Size: B	DWG NO: 514202-0001	Revision: A	
Date: Monday, May 21, 2012	Sheet 52 of 57		



SPECTRUM DIGITAL INCORPORATED			
Title: SMART GRID EVM			
Page Contents: POWER ROUTER			
Size: B	DWG NO: 514202-0001	Revision: B	
Date: Thursday, March 22, 2012	Sheet 53 of 57		

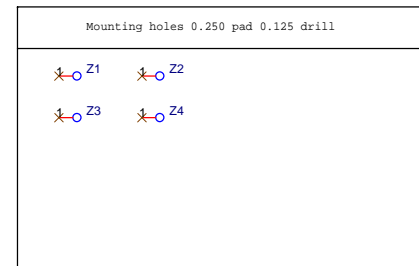
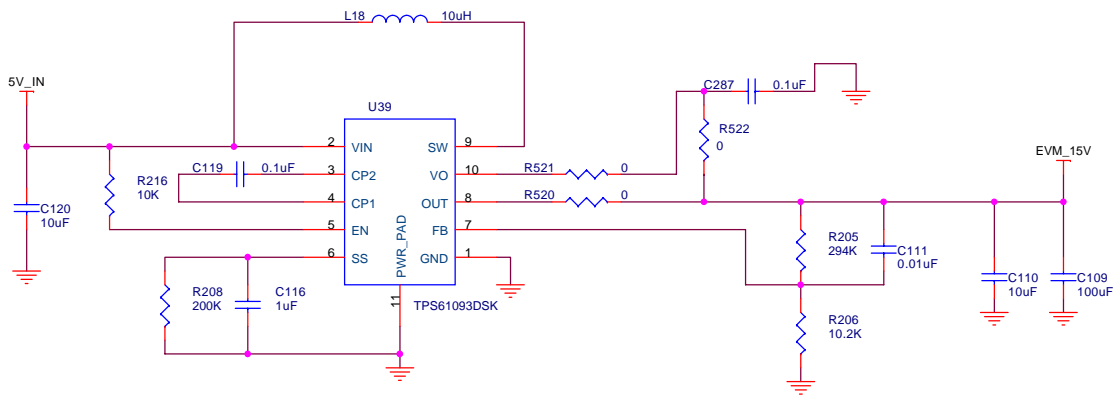
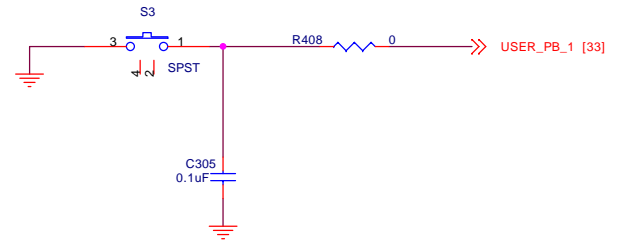
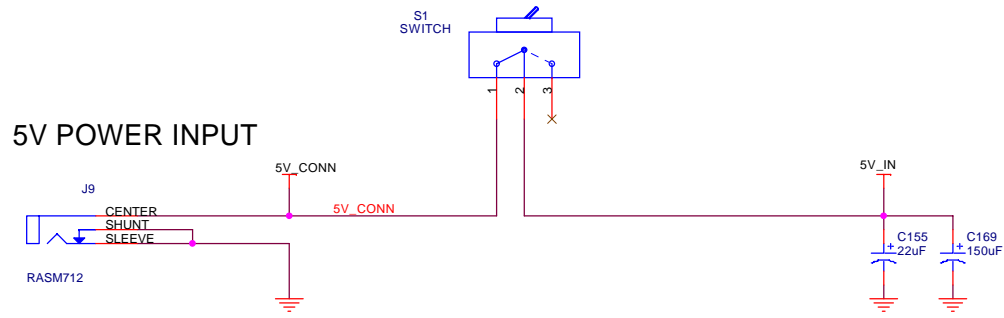


SPECTRUM DIGITAL INCORPORATED		
Title: SMART GRID EVM		
Page Contents: RTC POWER,ETC		
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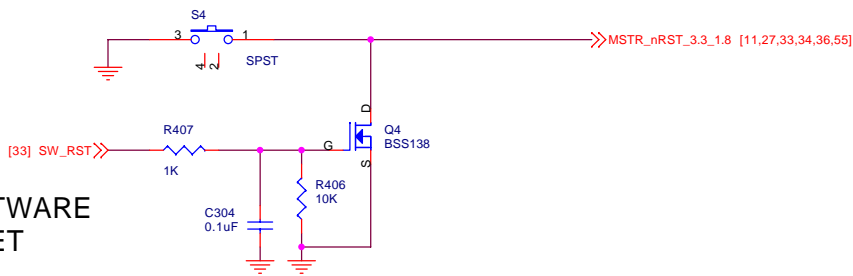


SPECTRUM DIGITAL INCORPORATED			
Title: SMART GRID EVM			
Page Contents: 3.3 VOLT, 1.8 VOLT, CORE POWER			
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5V POWER INPUT



SOFTWARE RESET



SPECTRUM DIGITAL INCORPORATED			
Title: SMART GRID EVM			
Page Contents: POWER			
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ASSEMBLY	REVISION A
PRINTED CIRCUIT BOARD/SCHEMATIC	REVISION A
INITIAL RELEASE	

ASSEMBLY	REVISION B
PRINTED CIRCUIT BOARD/SCHEMATIC	REVISION B
ADDED SECOND CODEC CHANNEL	

ASSEMBLY	REVISION C
PRINTED CIRCUIT BOARD/SCHEMATIC	REVISION C
ADDED USB TO ETHERNET CIRCUIT	

ASSEMBLY	REVISION D
PRINTED CIRCUIT BOARD/SCHEMATIC	REVISION C
COMPLIANCE CHANGES	

ASSEMBLY	REVISION E
PRINTED CIRCUIT BOARD/SCHEMATIC	REVISION D
COMPLIANCE CHANGES, 2.1mm power jack, oscillator instead of crystal on processor, tweak location of display connector	

SPECTRUM DIGITAL INCORPORATED			
Title: SMART GRID EVM			
Page Contents: REVISION HISTORY			
Size: B	DWG NO	514202-0001	Revision: D
Date:	Tuesday, June 05, 2012	Sheet 57 of	57