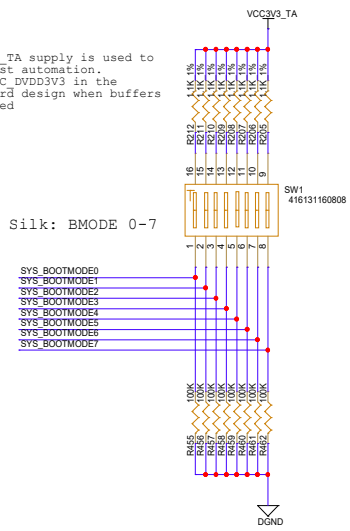


# BOOTMODE CONFIGURATION RESISTORS AND BOOTMODE SWITCHES

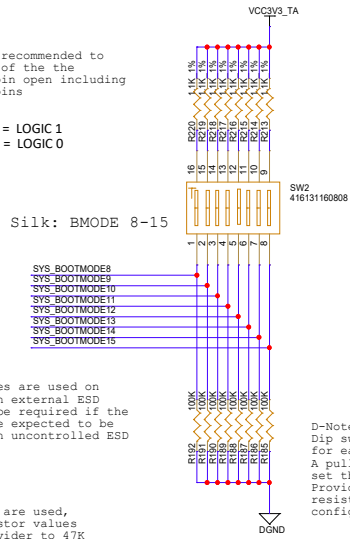
D-Note :-  
VCC3V3\_XDS\_TA supply is used to support test automation. Connect SOC\_DVDD3V3 in the custom board design when buffers are not used



D-Note :-  
it is not recommended to leave any of the the bootmode pin open including reserved pins  
  
SWITCH ON = LOGIC 1  
SWITCH OFF = LOGIC 0

D-Note :-  
When dip switches are used on custom board, an external ESD protection may be required if the DIP switches are expected to be configured in an uncontrolled ESD environment

D-Note :-  
When DIP switch are used, reduce the resistor values used for the divider to 47k and 470R maintaining the ratio



D-Note :-  
Dip switch is optional and used on the SK for ease of configuration  
A pullup or pulldown resistor can be used to set the BOOTMODE configuration  
Provide provision for Pullup and Pulldown resistors for the bootmode pins that have configuration capability

# SOC BOOTMODE INPUTS

## ALTERNATE FUNCTION

ALTERNATE FUNCTION	RA1	RA2	RA3	RA4	OE	BOOTMODE PINS
PRO_PRU0_GPO0	1	8	OE			SYS_BOOTMODE0
PRO_PRU0_GPO1	2	7				SYS_BOOTMODE1
PRO_PRU0_GPO2	3	6				SYS_BOOTMODE2
PRO_PRU0_GPO3	4	5				SYS_BOOTMODE3
PRO_PRU0_GPO7	1	8	OE			SYS_BOOTMODE4
PRO_PRU0_GPO8	2	7				SYS_BOOTMODE5
PRO_PRU0_GPO5	3	6				SYS_BOOTMODE6
PRO_PRU0_GPO4	4	5				SYS_BOOTMODE7
SOC_VOUT0_DATA16	1	8	OE			SYS_BOOTMODE8
SOC_VOUT0_DATA17	2	7				SYS_BOOTMODE9
SOC_VOUT0_DATA18	3	6				SYS_BOOTMODE10
SOC_VOUT0_DATA19	4	5				SYS_BOOTMODE11
SOC_VOUT0_DATA20	1	8	OE			SYS_BOOTMODE12
SOC_VOUT0_DATA21	2	7				SYS_BOOTMODE13
SOC_VOUT0_DATA22	3	6				SYS_BOOTMODE14
SOC_VOUT0_DATA23	4	5				SYS_BOOTMODE15

D-Note :-  
OR resistors are used to isolate the alternate function after boot

## BOOTMODE PINS

U1F	U1F
N22	GP McCoy_CLK
N21	GP McCoy_AD0
N20	GP McCoy_AD1
N19	GP McCoy_AD2
N18	GP McCoy_AD3
N17	GP McCoy_AD4
P18	GP McCoy_AD5
P21	GP McCoy_AD6
P22	GP McCoy_AD7
R19	GP McCoy_AD8
R20	GP McCoy_AD9
R22	GP McCoy_AD10
T22	GP McCoy_AD11
R21	GP McCoy_AD12
T20	GP McCoy_AD13
T21	GP McCoy_AD14
	GP McCoy_AD15
M19	GP McCoy_CSN0
M21	GP McCoy_CSN1
M22	GP McCoy_CSN2
M20	GP McCoy_CSN3
L18	GP McCoy_ADVN_ALE
L19	GP McCoy_BE0N_CLE
M18	GP McCoy_BE1N
K18	GP McCoy_DIR
R18	GP McCoy_WAIT0
R17	GP McCoy_WAIT1
K17	GP McCoy_WPN
K19	GP McCoy_WPN
L17	GP McCoy_OEN_REN

AM62A74AUISIAMBQ1

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Title  
BOOTMODE RESISTOR CONNECTIONS WITHOUT BUFFERS

Size	AM62x, AM64x	Rev	A
C			
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