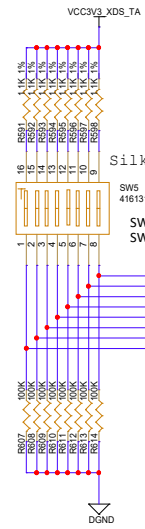


BOOTMODE CONFIGURATION RESISTORS AND BOOTMODE SWITCHES



Silk: BMODE 0-7

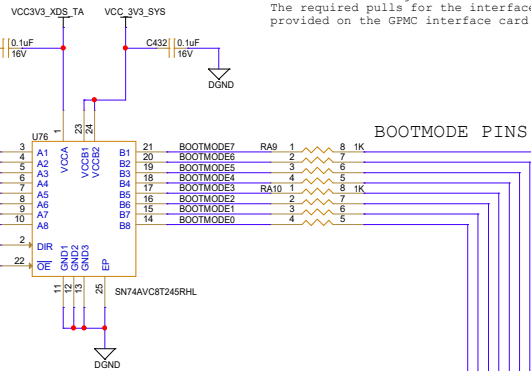
SW5
416131160808
SWITCH ON = LOGIC 1
SWITCH OFF = LOGIC 0

SYS_BOOTMODE7
SYS_BOOTMODE6
SYS_BOOTMODE5
SYS_BOOTMODE4
SYS_BOOTMODE3
SYS_BOOTMODE2
SYS_BOOTMODE1
SYS_BOOTMODE0

DIR = H: A -> B
DIR = L: B -> A
OE = H: output = Hi-Z

BOOT MODE BUFFERS

D-Note :-
SOC IO buffers for signals used for GPMC interface are disabled during reset
The required pulls for the interfaced signals are provided on the GPMC interface card

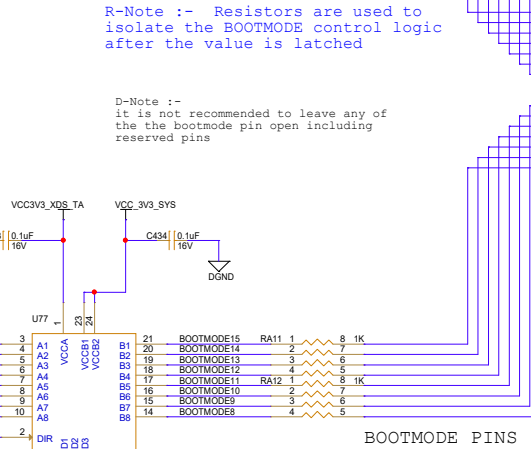


Silk: BMODE 8-15

SW6
416131160808
SWITCH ON = LOGIC 1
SWITCH OFF = LOGIC 0

SYS_BOOTMODE15
SYS_BOOTMODE14
SYS_BOOTMODE13
SYS_BOOTMODE12
SYS_BOOTMODE11
SYS_BOOTMODE10
SYS_BOOTMODE9
SYS_BOOTMODE8

DIR = H: A -> B
DIR = L: B -> A
OE = H: output = Hi-Z



R-Note :- Resistors are used to isolate the BOOTMODE control logic after the value is latched

D-Note :-
it is not recommended to leave any of the bootmode pin open including reserved pins

D-Note :-
When dip switches are used on custom board, an external ESD protection may be required if the DIP switches are expected to be configured in an uncontrolled ESD environment

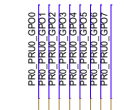
D-Note :-
When DIP switches are used, reduce the resistor values used for the divider to 47K and 470R maintaining the ratio

D-Note :-
VCC3V3_XDS_TA supply is used for test automation.
Connect Soc_DVDD3V3 in the custom board design when buffers are not used

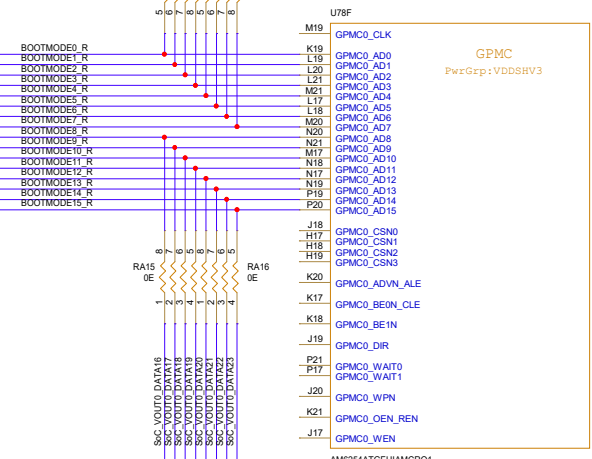
D-Note :-
Dip switch is optional and used on the SK for ease of configuration
A pullup or pulldown resistor can be used to set the BOOTMODE configuration
Provide provision for Pullup and Pulldown resistors for the bootmode pins that have configuration capability

D-Note :-
OR resistors are used to isolate the alternate function after boot

ALTERNATE FUNCTION

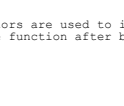


SOC GPMC INTERFACE



D-Note :-
OR resistors are used to isolate the alternate function after boot

ALTERNATE FUNCTION



Designed for TI by Mistral Solutions Pvt Ltd



Title		BOOTMODE RESISTOR CONNECTIONS WITH BUFFERS	
Size	AM62x, AM64x	Rev	A
C		Date:	Tuesday, September 17, 2024
Sheet		1 of 1	