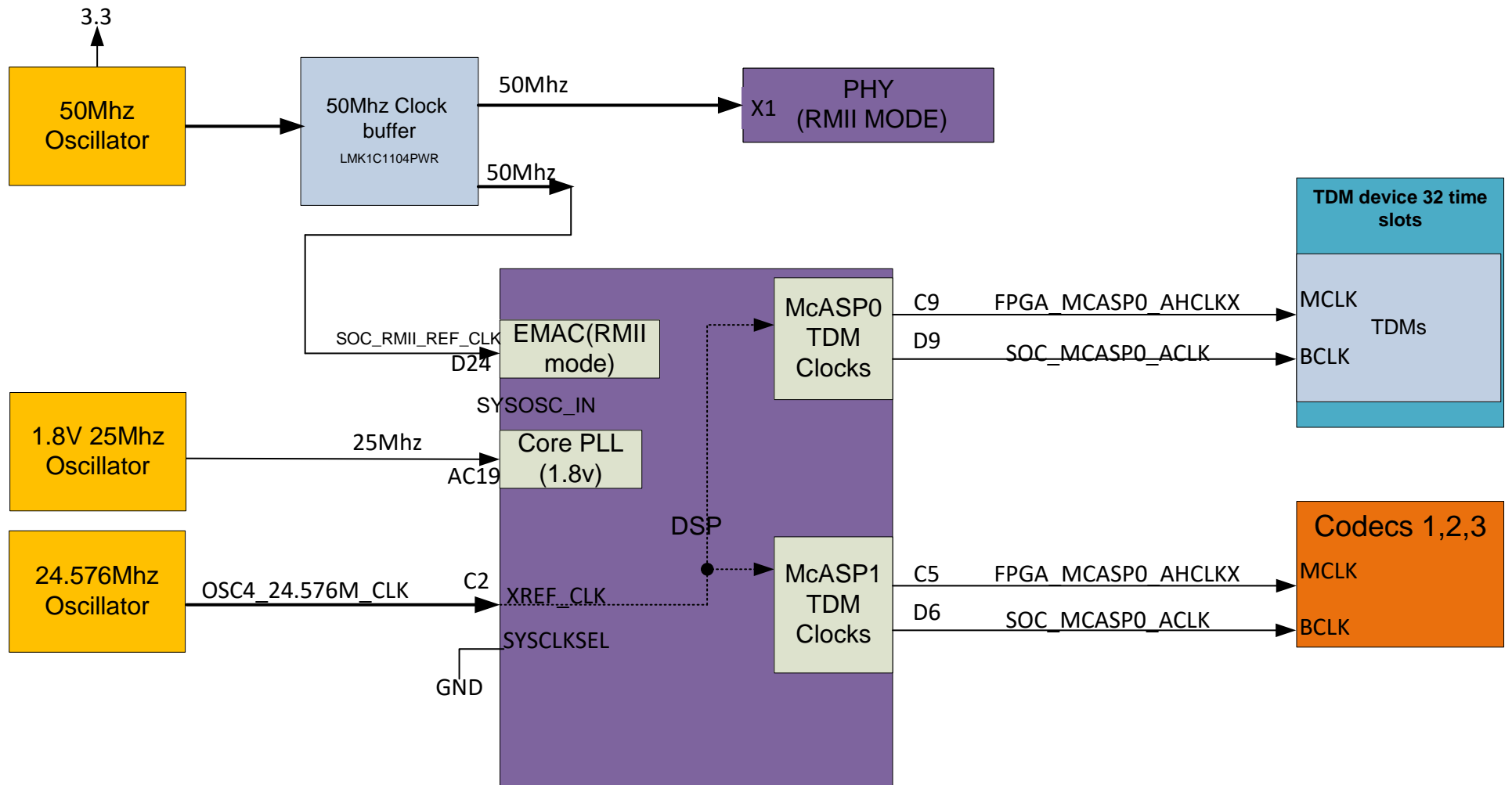


# SOC clocks



Note:  
SOC clock must  
be stable prior to  
PORreset rise.

# SOC TDMs connection to Codecs

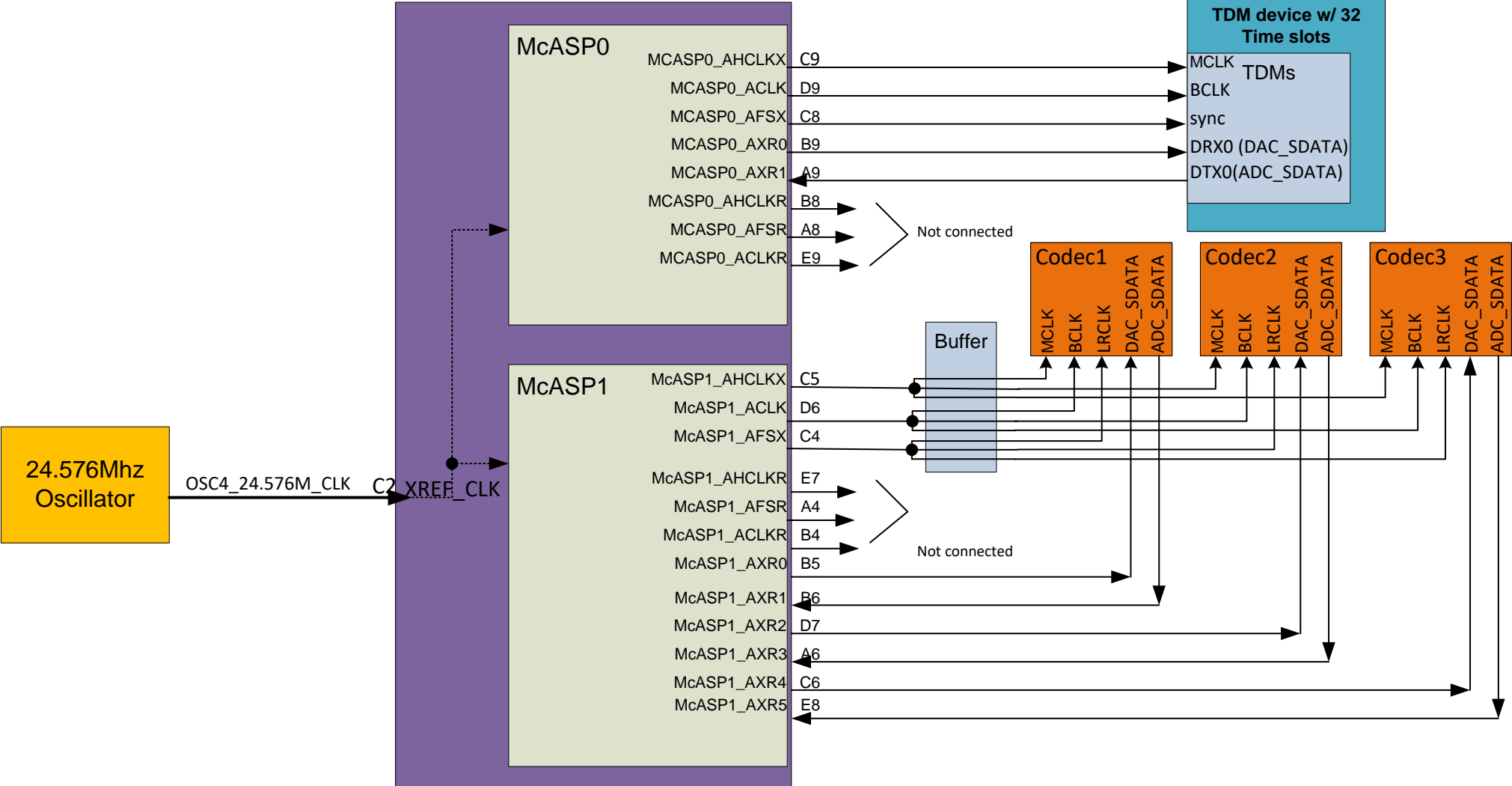


Figure 11-584. McASP Transmit Clock Generator Block Diagram

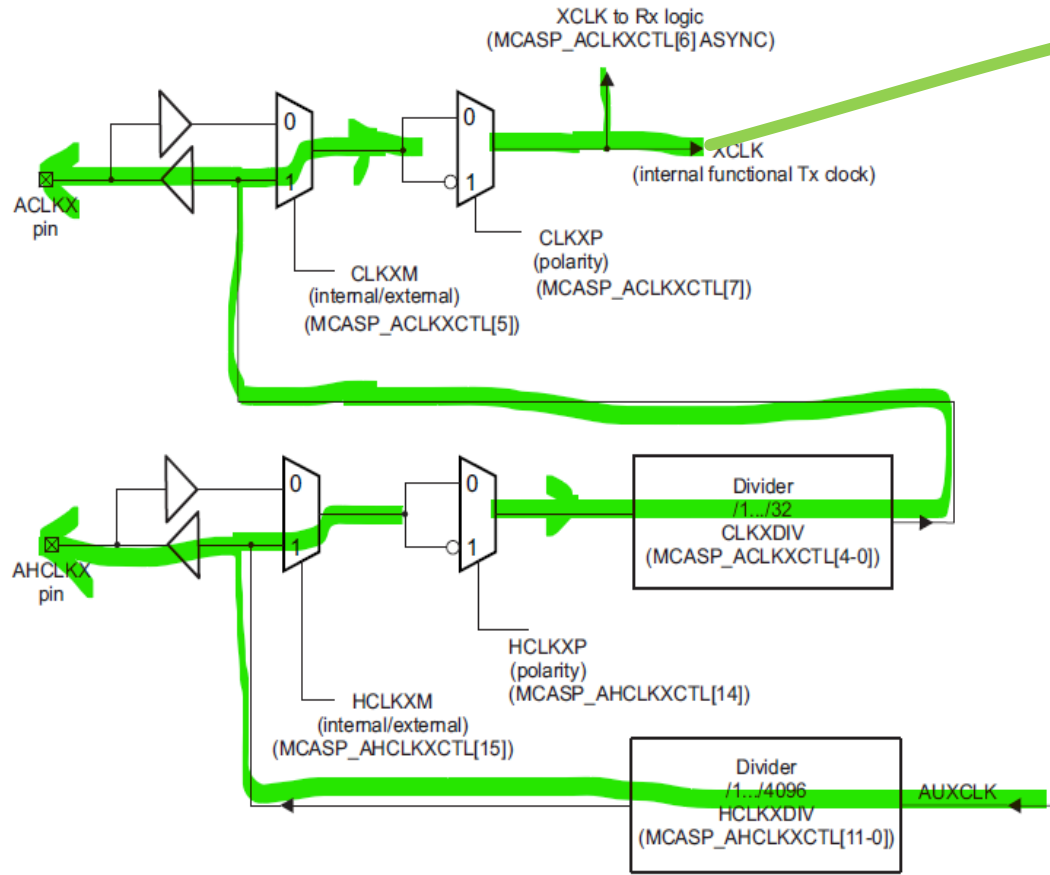


Figure 11-585. McASP Receive Clock Generator Block Diagram

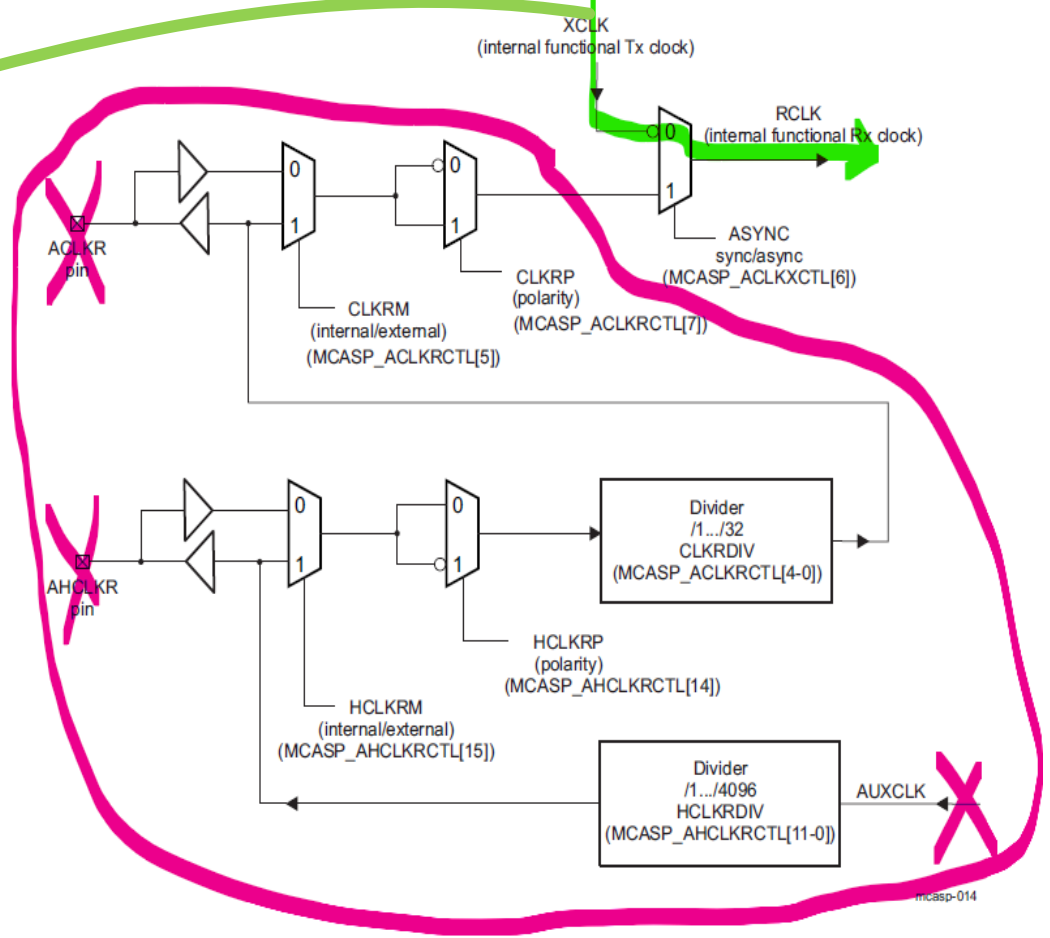


Figure 11-586. McASP Frame Sync Generator Block Diagram

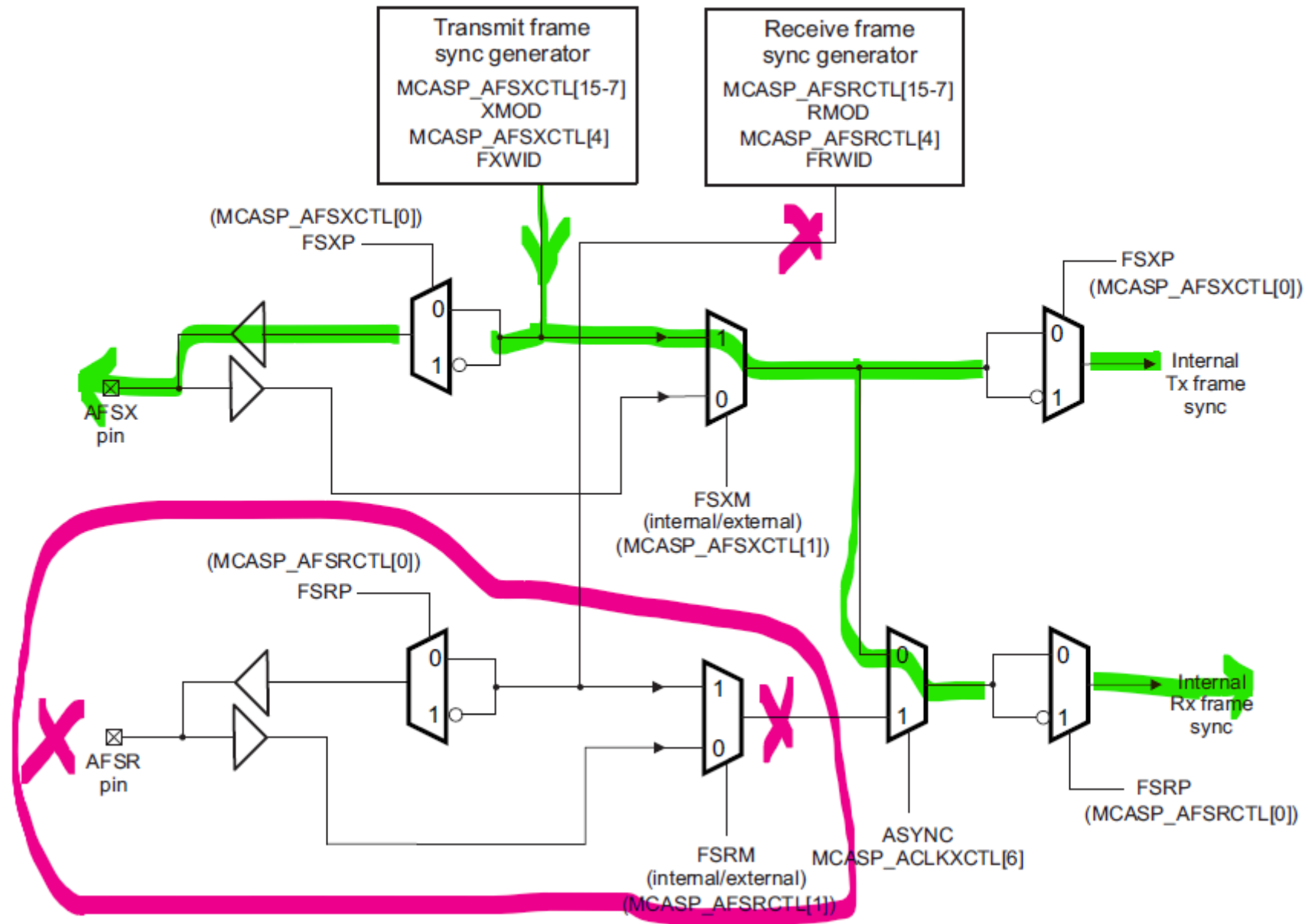


Figure 5-164. Device Audio Modules Clocking

