According to Lee Ritchey, a noted industry high-speed PCB design expert, successful differential signaling does not require:

1. To set each of the routing signal impedances to half the incoming characteristic impedance at the receiver end.

2. That the two lines should be of equal length, to within tolerances of the logic family and the circuit frequency used in the design.

Differential clocks should be routed in parallel and keep the trace length short.