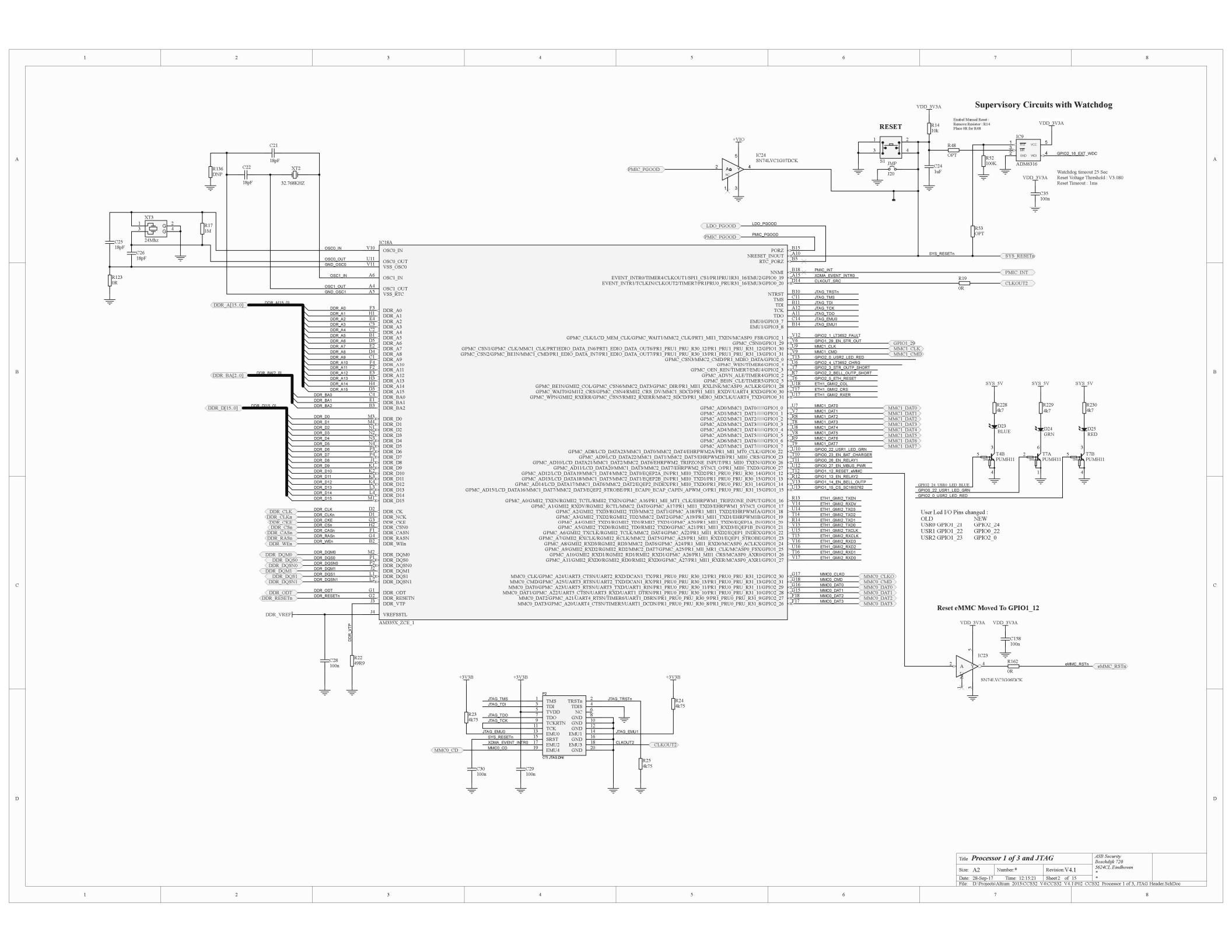
Supervisory Circuits with Watchdog
RESET
Enabel Manual Reset :
Remove Resistor : R14
Place 0R for R48
Watchdog timeout 25 Sec
Reset Voltage Threshold : V3.080
Reset Timeout : 1ms
Reset eMMC Moved To GPIO1_12
User Led I/O Pins changed :
OLD NEW
USR0 GPIO1_21 GPIO2_24
USR1 GPIO1_22 GPIO0_22
USR2 GPIO1_23 GPIO2_0
Title Processor 1 of 3 and JTAG
Size: A2 Number:* Revision:V4.1
Date: 28-Sep-17 Time: 12:15:21 Sheet2 of 15
File: D:\Projects\Altium 2013\CC832 V4\CC832 V4.1\P02 CC832 Processor 1 of 3, JTAG Header.SchDoc
ASB Security
Boschdijk 720
5624CL Eindhoven