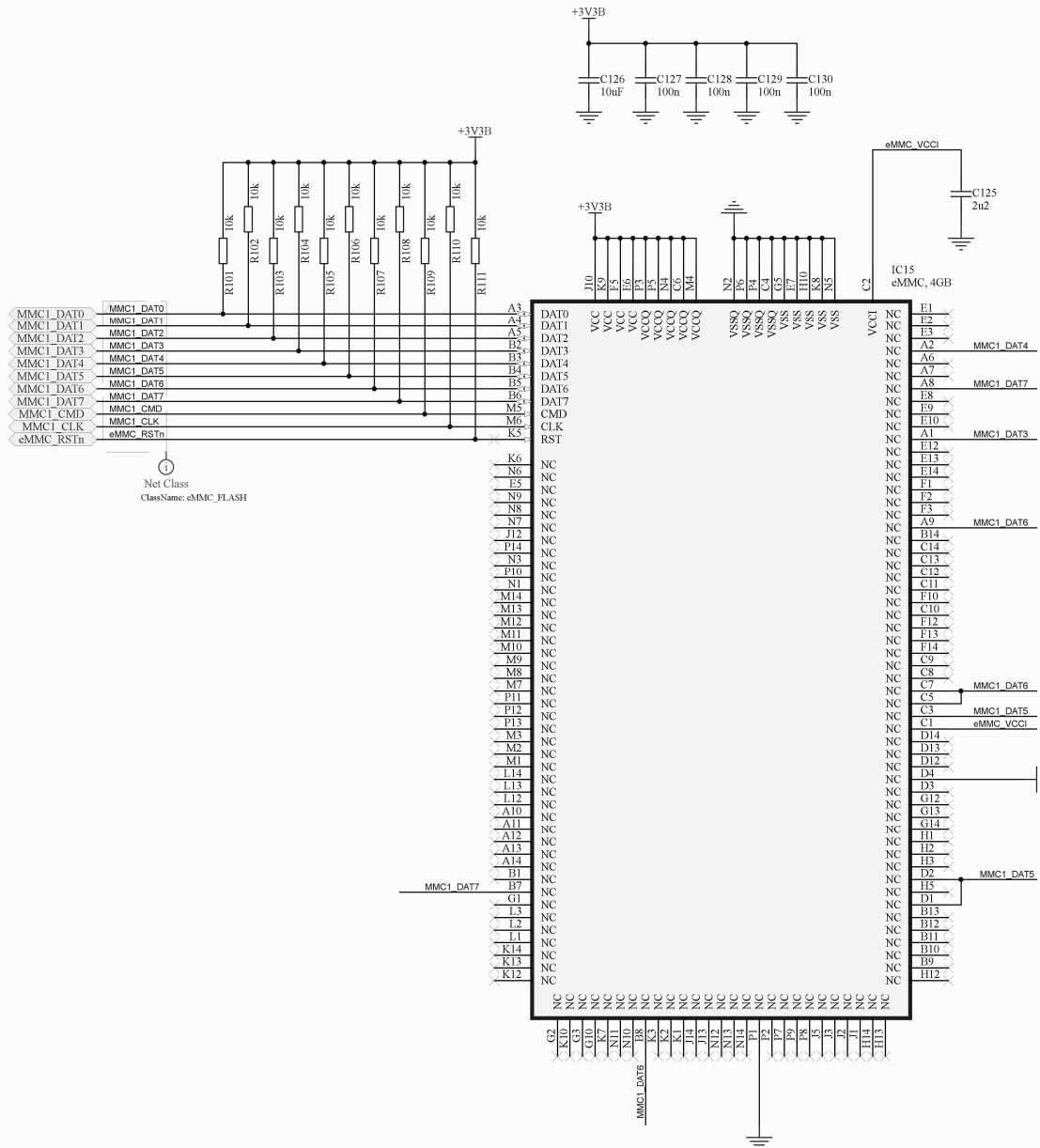


## 32Gb e-MMC Nand Flash



eMMC signals can be fanned out through NC pins. No internal connection is present for NC pins. Micron recommends that eMMC signals not be fanned out through RFU pins.

SEE TN-FC-35 eMMC PCB Design Guide

Micron e.MMC incorporates advanced technology for defect and error management. If a defective block is identified, the device completely replaces the defective block with one of the spare blocks. This process is invisible to the host and does not affect data space allocated for the user.

The device also includes a built-in error correction code (ECC) algorithm to ensure that data integrity is maintained.

To make the best use of these advanced technologies and ensure proper data loading and storage over the life of the device, the host must exercise the following precautions:

- Check the status after WRITE, READ, and ERASE operations.
- Avoid power-down during WRITE and ERASE operations.

Title <b>32Gb e-MMC Flash</b>			ASB Security Boschdijk 720 5624CL Eindhoven * *
Size: A3	Number.*	Revision:V4.1	
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