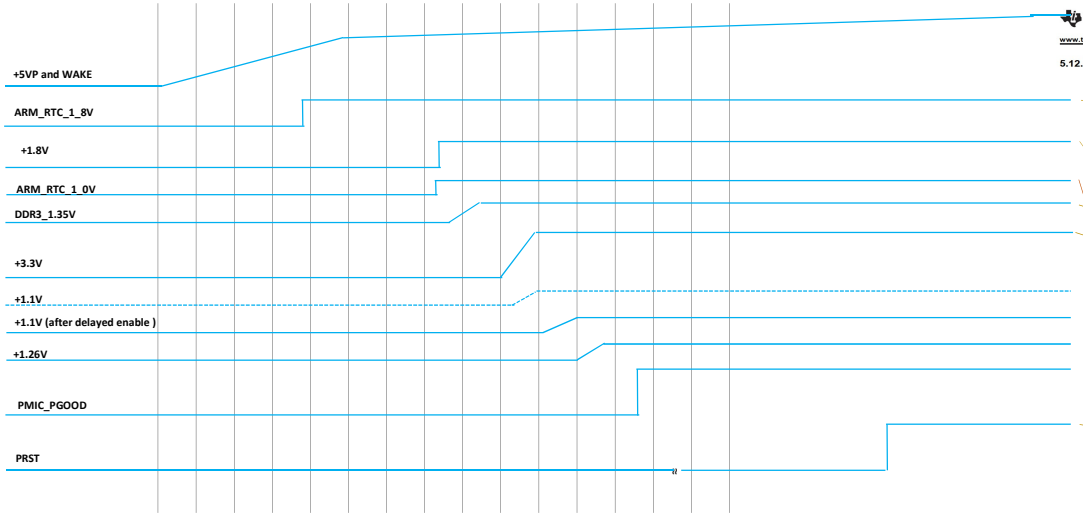
board schematic nets

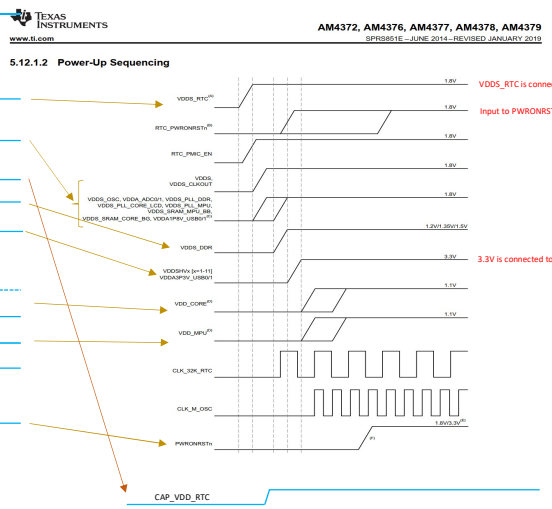
waveforms observed in board (1ms/div)

Connections to AM4372

AM4372 datasheet recomendation

Notes





VDDS_RTC is conneced to ***RTC_KALDO_ENn*** in the board

Input to PWRONRSTn is level shifted and conneced to ***RTC_PWRONRSTn*** in the board

3.3V is connected to ***VDDS3P3V_IOLDO*** in the board

Initially observed that 1.1V is rising along with 3.3V. (Is it a problem ?)
Hence we added delay in enable of 1.1V so that the output starts rising only after 3.3V settles to final value.