

5.2.7 Peripheral Booting

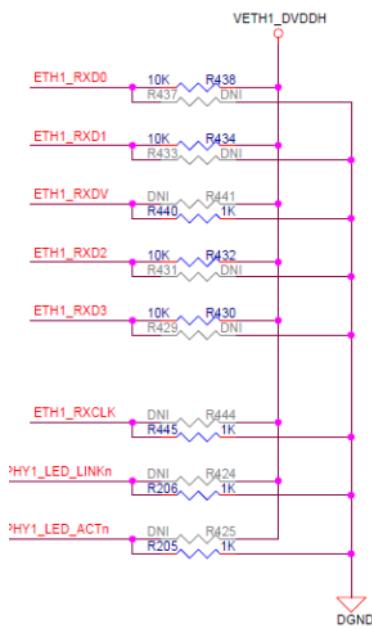
5.2.7.1 Overview

The ROM Code boots from three different peripheral interfaces:

- **EMAC:** 100/10 Mbps Ethernet, using standard TCP/IP network boot protocols BOOTP and TFTP
- **USB:** Full-speed, client mode
- **UART:** 115.2Kbps, 8-bit, no parity, 1 stop bit, no flow control

CHECK if

The purpose of booting from a peripheral interface is to download a boot image from an external host (typically a PC). This booting method is mostly used for programming flash memories connected to the device (for example, in the case of initial flashing, or for firmware updates or servicing).



	27 - RXD3	28 - RXD2	31 - RXD1	32 - RXD0	
MODE3	MODE2	MODE1	MODE0		
1	1	1	0		
R430	R432	R434	R437		
1	1	0	1		
R430	R432	R433	R438	RGMII Mode-Advertise 1000 Base-T-FD-HD	Will Try this

1.1.1 Mode Definition

Table 1-3 shows the Mode and its Description.

Table 1-3. Mode Definition

Mode [3:0]	Description	ON AM335x
0000	1000 BASE-T, RGMII	
0001	1000 BASE-T, SGMII	
0010	1000 BASE-X, RGMII, 50Ω	
0011	1000 BASE-X, SGMII, 75Ω	
0100	Converter mode between 1000 BASE-X and 1000 BASE-T media, 50Ω	
0101	Converter mode between 1000 BASE-X and 1000 BASE-T media, 75Ω	
0110	100 BASE-FX, RGMII, 50Ω	
0111	Converter mode between 100 BASE-FX and 100 BASE-TX media, 50Ω	
1011	RGMII, copper fiber auto-detection	
1110	100 BASE-FX, RGMII mode, 75Ω	
1111	Converter mode between 100 BASE-FX and 100 BASE-TX media, 75Ω	
Others	Reserved	