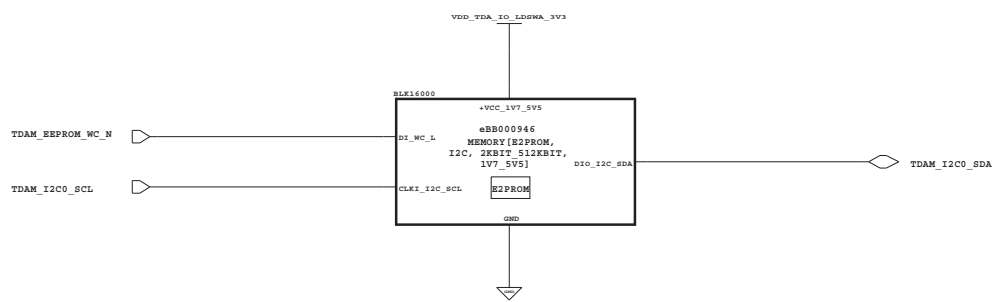


16. EEPROM



@VL_MO	@VL_MO	@VL_DB@VL_RE@VL_MO	@VL_RE
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@VL_MO	@VL_MO	@VL_DB@VL_RE@VL_MO	@VL_RE
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@VL_MO	@VL_MO	@VL_DB@VL_RE@VL_MO	@VL_RE
@VL_MO	@VL_MO	@VL_DB@VL_RE@VL_MO	@VL_RE
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@VL_MO	@VL_MO	@VL_DB@VL_RE@VL_MO	@VL_RE
@VL_MO	@VL_MO	@VL_DB@VL_RE@VL_MO	@VL_RE
@VL_MO	@VL_MO	@VL_DB@VL_RE@VL_MO	@VL_RE
@VL_MO	@VL_MO	@VL_DB@VL_RE@VL_MO	@VL_RE
@VL_MO	@VL_MO	@VL_DB@VL_RE@VL_MO	@VL_RE
AUTH.	DATE	DRANK	REV

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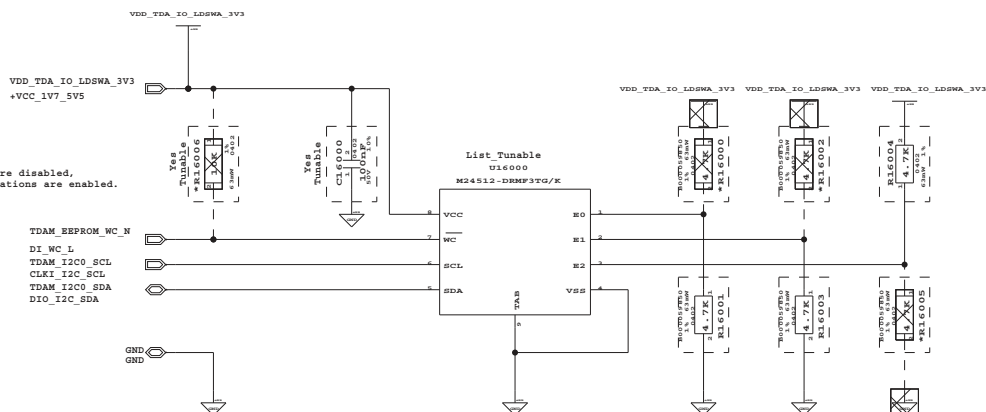


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DRAWING NAME: @VL_AS		DATE: @VL_CR	
PRODUCT: Definition		CUSTOMER: @VL_CU	
VARIANT: @VL_PR		CONTROLLED BY: @VL_eB	
BRANCH NO.: @VL_LA		APPROVED BY: @VL_eB	
@VL_PR		@VL_PR	
DRAWING NUMBER:		SHEET: 28 of 98	
@VL PL-ESF020		@VL DR A3	

eBB000946

MEMORY[E2PROM, I2C, 2KBIT_512KBIT, 1V7_5V5] (M24xxx, NV24Cxx, 24FCxxx, GT24Cxxx)

PU by 10k RESISTOR TO WC
When MCU_EEPROM_WC_N is high, the WRITE operations are disabled,
When MCU_EEPROM_WC_N is low or float, the WRITE operations are enabled.



List-Tunables for U1:

ST	MICROCHIP	ONSEMI	GIANTEC
M24C02	24FC02		GT24C02C
M24C04	24FC04		GT24C04B
M24C08	24FC08	NV24C04	GT24C08C
M24C16	24FC16	NV24C16	GT24C16C
M24C32	24FC32	NV24C32	GT24C32B
M24C64	24FC64	NV24C64	GT24C64B
M24128	24FC128		GT24C128B
M24256	24FC256		GT24C256B
M24512	24FC512		GT24C512B

When accessing Memory Addr = 0x54
When accessing ID page Addr = 0x3C

Table 2. Device select code

	Device type identifier ⁽¹⁾				Chip Enable address ⁽²⁾			RW
	b7	b6	b5	b4	b3	b2	b1	b0
When accessing the memory	1	0	1	0	E2	E1	E0	RW
When accessing the Identification page	1	0	1	1	E2	E1	E0	RW

- The most significant bit, b7, is sent first.
- E0, E1 and E2 bits are compared with the value read on input pins E0,E1,E2.

Plotted Date : 25/11/25 19:22:01



TITLE: eBB000946		DRAWN BY: BHARATHIRAJA SAMPATH	
DRAWING NAME: Definition		DATE: 18-08-2022	
PRODUCT: MEMORY[E2PROM, I2C, 2KBIT_512KBIT, 1V7_5V5] (M24xxx, NV24Cxx, 24FCxxx, GT24Cxxx)		CUSTOMER:	
VARIANT:		CONTROLLED BY:	
BRANCH NO. :		APPROVED BY:	
CEE NO. :			

BSAMPATH	18/08/2025		Microchip and GIANTEC soures added as List-tunable	3
BSAMPATH	21-08-2024		NV24C series added	2
BSAMPATH	26-08-2022		DRAWING CREATION	1
AUTH.	DATE	DRAWN	REV	MODIFICATIONS

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DRAWING NUMBER:

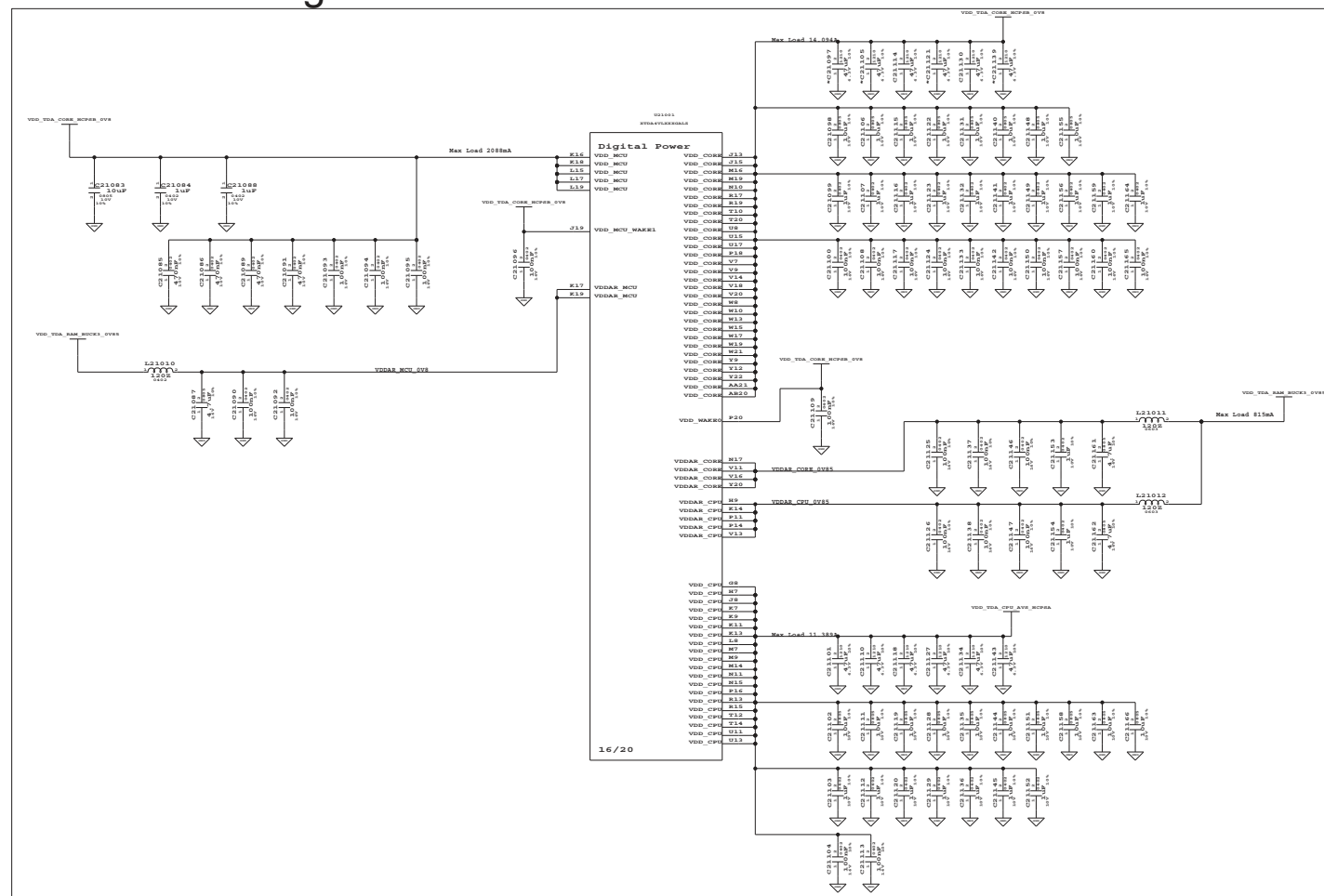
eBB000946-ESF020 1 1

SHEET:

SIZE:

A3

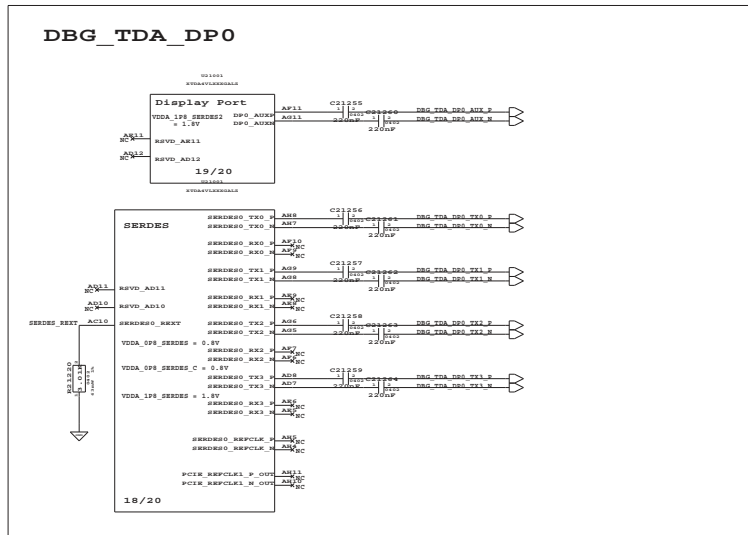
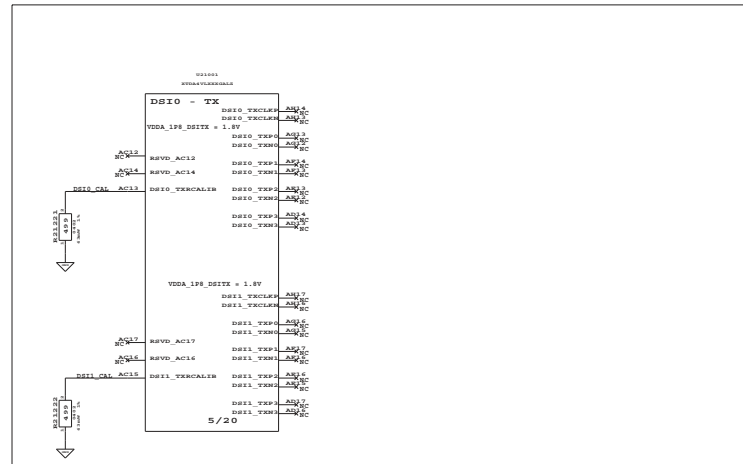
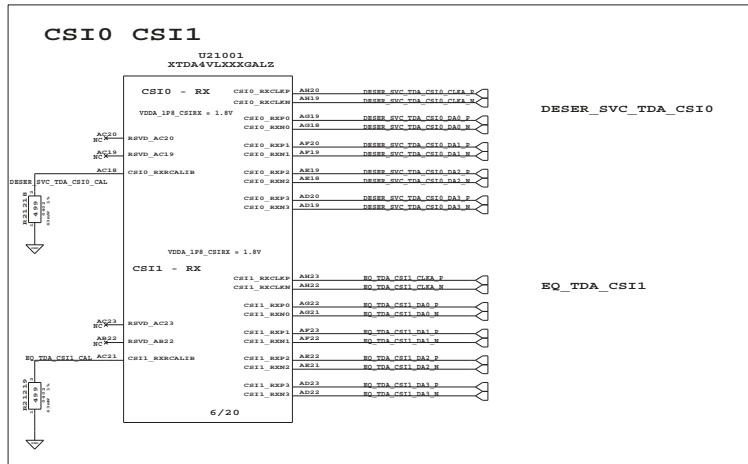
22.TDA4AL - Digital PWR

[illegible]

Plotted Date : 25/11/25 19:22:13					
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DRAWING NAME: Definition			DATE: @VL_CR		
PRODUCT: @VL_PR			CUSTOMER: @VL_CU		
VARIANT: @VL_VA			CONTROLLED BY:		
SEARCH NO.: @VL_LA			APPROVED BY: @VL_eB		
CCKE NO.: @VL_LA					
@VL_PR			@VL_PR		
@VL_PL-ESF020			@VL_DR 40 of 98		
			SIZE A2		

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26. TDA4AL Video I/F

[illegible]

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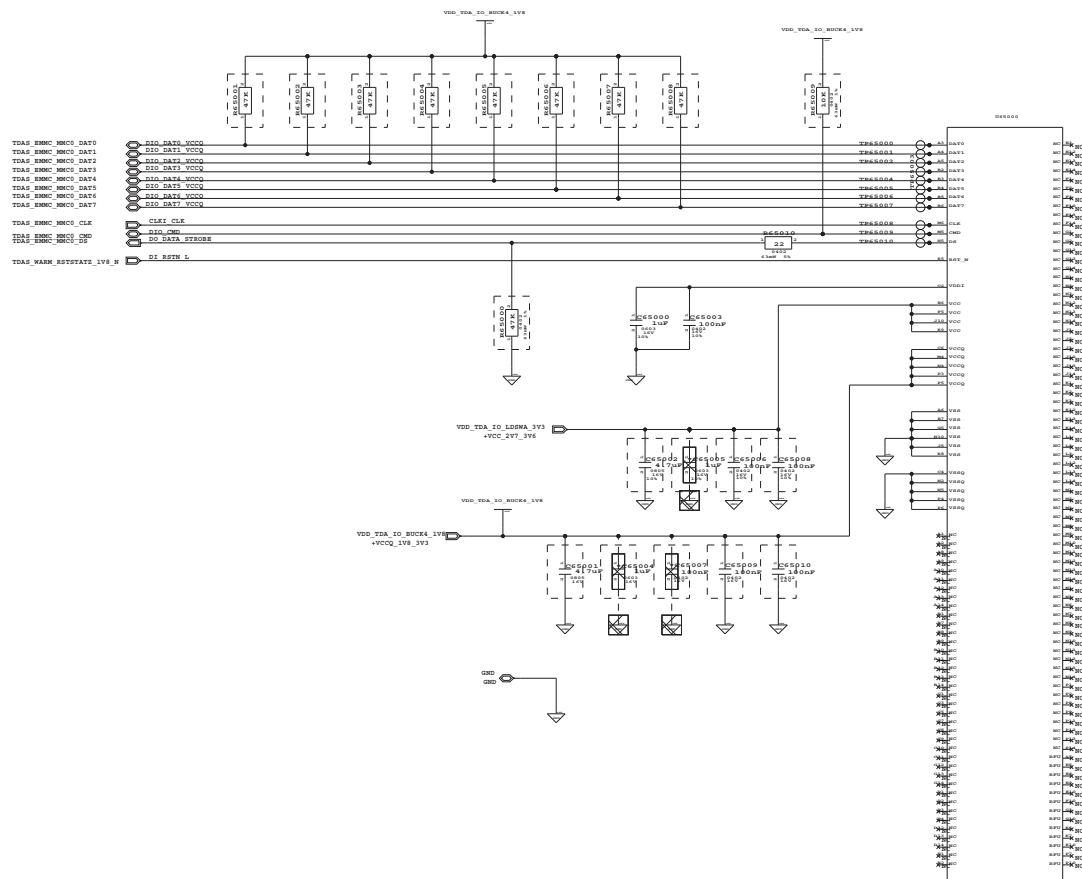


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DRAWING NAME:		DATE:	
Definition		@VL_CR	
PRODUCT:		CUSTOMER:	
@VL_PR		@VL_CU	
VARIANT:		CONTROLLED BY:	
@VL_VA		@VL_CB	
BRANCH NO.:		APPROVED BY:	
@VL_LA	CHK NO.:	@VL_CB	
@VL_PR	@VL_LA		
	@VL_PR		

@VL PL-ESF020

@VL	DR 44 of 98	A2
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MEMORY [FLASH, MMC, 8GB 32GB, 2V7 3V6, NAND, BGA] (KLMxGxGEUF-B04x)



```
List-Tunables:
1. KLM8G1GKUF-B04Q
2. KLMAG2GKUF-B04Q
3. KLM8G4GKUF-B04Q
```

Yes over the component indicates the component is removable(Spare)



TITLE:		DRAWN BY: BHARATHIRAJA SAMPATH	
eBB001619		DATE: 04-03-2024	
		CUSTOMER:	
DRAWING NAME: Definition		CONTROLLED BY:	
PRODUCT: MEMORY (FLASH, MMC, SGB_32GB, 2V7_3V6, NAND, SGA) (KLMxGwGRUF-B04x)			
VARIANT:			
SEARCH NO.:	CHK NO.:	APPROVED BY:	
		KARTHICKRAJA KANTHARAJA	
	@VL_PR	SIGN:	
001619-ESF020		Page 97 of 98	
01		A2	

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	DRAWING NUMBER.
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eBB001619-ESF020

01

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