芯片：TI-AM6254A

设备树关键配置：

main\_mdio1\_pins\_default: main-mdio1-pins-default {

 pinctrl-single,pins = <

 AM62X\_IOPAD(0x160, PIN\_OUTPUT, 0) /\* (AD24/V17) MDIO0\_MDC \*/

 AM62X\_IOPAD(0x15c, PIN\_INPUT, 0) /\* (AB22/U16) MDIO0\_MDIO \*/

 >;

 };

 main\_rgmii1\_pins\_default: main-rgmii1-pins-default {

 pinctrl-single,pins = <

 AM62X\_IOPAD(0x14c, PIN\_INPUT, 1) /\* (AB17/W15) RMII1\_RD0 \*/

 AM62X\_IOPAD(0x150, PIN\_INPUT, 1) /\* (AC17/Y16) RMII1\_RD1 \*/

 AM62X\_IOPAD(0x158, PIN\_INPUT, 7) /\* (AA15/Y15) GPIO0\_84\*/

 AM62X\_IOPAD(0x148, PIN\_INPUT, 1) /\* (AD17/AA16) RMII1\_RXC \*/

 AM62X\_IOPAD(0x144, PIN\_INPUT, 1) /\* (AE17/W14) RMII1\_RX\_CTL \*/

 AM62X\_IOPAD(0x134, PIN\_OUTPUT, 1) /\*(AE20/U14) RMII1\_TD0 \*/

 AM62X\_IOPAD(0x138, PIN\_OUTPUT, 1) /\*(AD20/AA19) RMII1\_TD1 \*/

 AM62X\_IOPAD(0x130, PIN\_OUTPUT, 1) /\* (AE19/W16) RMII1\_TXC \*/

 AM62X\_IOPAD(0x12c, PIN\_OUTPUT, 1) /\* (AD19/V15) RMII1\_TX\_CTL \*/

 >;

 };

&cpsw3g {

 pinctrl-names = "default";

 pinctrl-0 = <&main\_rgmii1\_pins\_default>;

};

&cpsw\_port1 {

 phy-mode = "rmii";

 phy-handle = <&cpsw3g\_phy0>;

};

&cpsw3g\_mdio {

 status = "okay";

 pinctrl-names = "default";

 pinctrl-0 = <&main\_mdio1\_pins\_default>;

 cpsw3g\_phy0: ethernet-phy@1 {

 reg = <1>;

 ti,rx-internal-delay = <DP83867\_RGMIIDCTL\_2\_00\_NS>;

 ti,fifo-depth = <DP83867\_PHYCR\_FIFO\_DEPTH\_4\_B\_NIB>;

 ti,min-output-impedance;

 };

};

&cpsw3g {

 pinctrl-names = "default";

 pinctrl-0 = <&main\_rgmii1\_pins\_default &main\_rgmii2\_pins\_default>;

 cpts@3d000 {

 /\* MAP HW3\_TS\_PUSH to GENF1 \*/

 ti,pps = <2 1>;

 };

};

&cpsw\_port2 {

 phy-mode = "rmii";

 phy-handle = <&cpsw3g\_phy1>;

};

&cpsw3g\_mdio {

 reset-gpios = <&main\_gpio0 83 GPIO\_ACTIVE\_LOW &main\_gpio1 5 GPIO\_ACTIVE\_LOW>;

 reset-delay-us = <100>; /\* set reset hold time to 100us \*/

 cpsw3g\_phy1: ethernet-phy@3 {

 reg = <3>;

 ti,rx-internal-delay = <DP83867\_RGMIIDCTL\_2\_00\_NS>;

 ti,fifo-depth = <DP83867\_PHYCR\_FIFO\_DEPTH\_4\_B\_NIB>;

 ti,min-output-impedance;

 };

};

main\_rgmii2\_pins\_default: main-rgmii2-pins-default {

 pinctrl-single,pins = <

 AM62X\_IOPAD(0x184, PIN\_INPUT, 1) /\* (AE23) RMII2\_RD0 \*/

 AM62X\_IOPAD(0x188, PIN\_INPUT, 1) /\* (AB20) RMII2\_RD1 \*/

 AM62X\_IOPAD(0x190, PIN\_INPUT, 7) /\* (AE22) GPIO1\_6\*/

 AM62X\_IOPAD(0x180, PIN\_INPUT, 1) /\* (AD23) RMII2\_RXC \*/

 AM62X\_IOPAD(0x17c, PIN\_INPUT, 1) /\* (AD22) RMII2\_RX\_CTL \*/

 AM62X\_IOPAD(0x16c, PIN\_OUTPUT, 1) /\* (Y18) RMII2\_TD0 \*/

 AM62X\_IOPAD(0x170, PIN\_OUTPUT, 1) /\* (AA18) RMII2\_TD1 \*/

 AM62X\_IOPAD(0x168, PIN\_OUTPUT, 1) /\* (AE21) RMII2\_TXC \*/

 AM62X\_IOPAD(0x164, PIN\_OUTPUT, 1) /\* (AA19) RMII2\_TX\_CTL \*/

 >;

 };

main\_phy\_reset\_pins\_default: main\_phy\_reset\_pins\_default {

 pinctrl-single,pins = <

 AM62X\_IOPAD(0x018c, PIN\_OUTPUT, 7) /\* (E24) OSPI0\_CSn3.GPIO0\_14 \*/

 AM62X\_IOPAD(0x0154, PIN\_OUTPUT, 7) /\* (E24) OSPI0\_CSn3.GPIO0\_14 \*/

 >;

 };

启动日志关键打印：



问题现象：

1. 配置相应IP，ping PC端失败



1. Wireshark抓取报文，可见cpu侧已经正常发送报文，且PC端已回复ARP报文



1. 使用ethtool查看，rx\_good\_frame始终为0，tx\_good\_frame为正常计数

