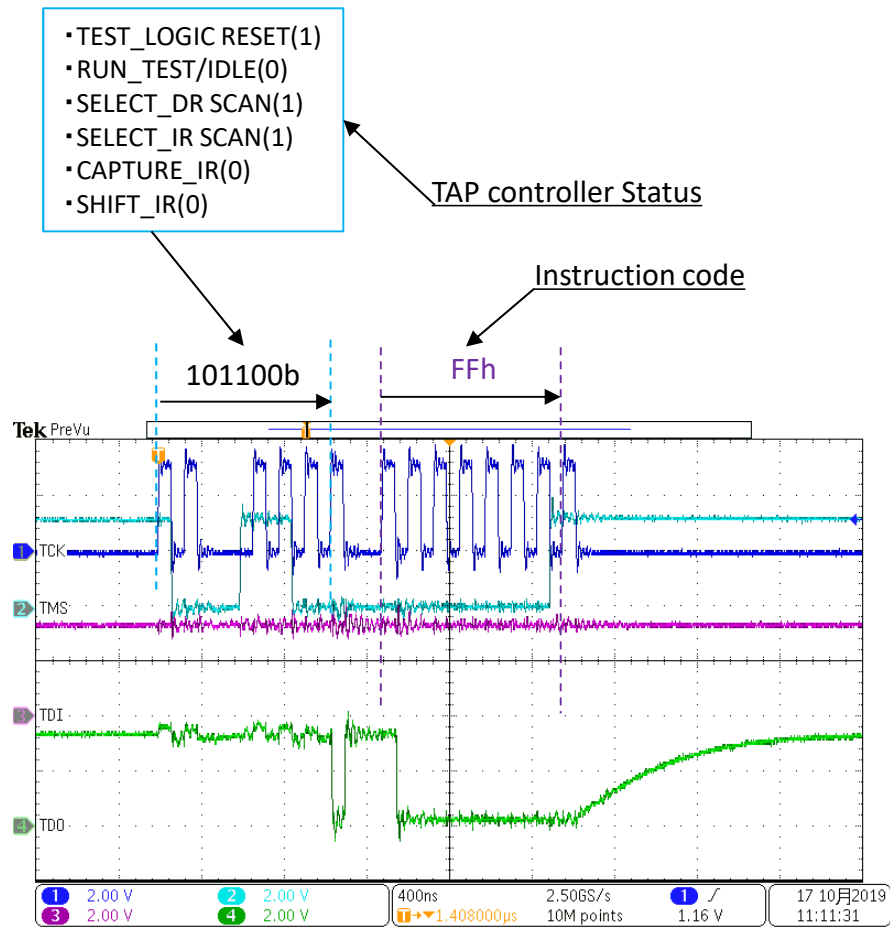
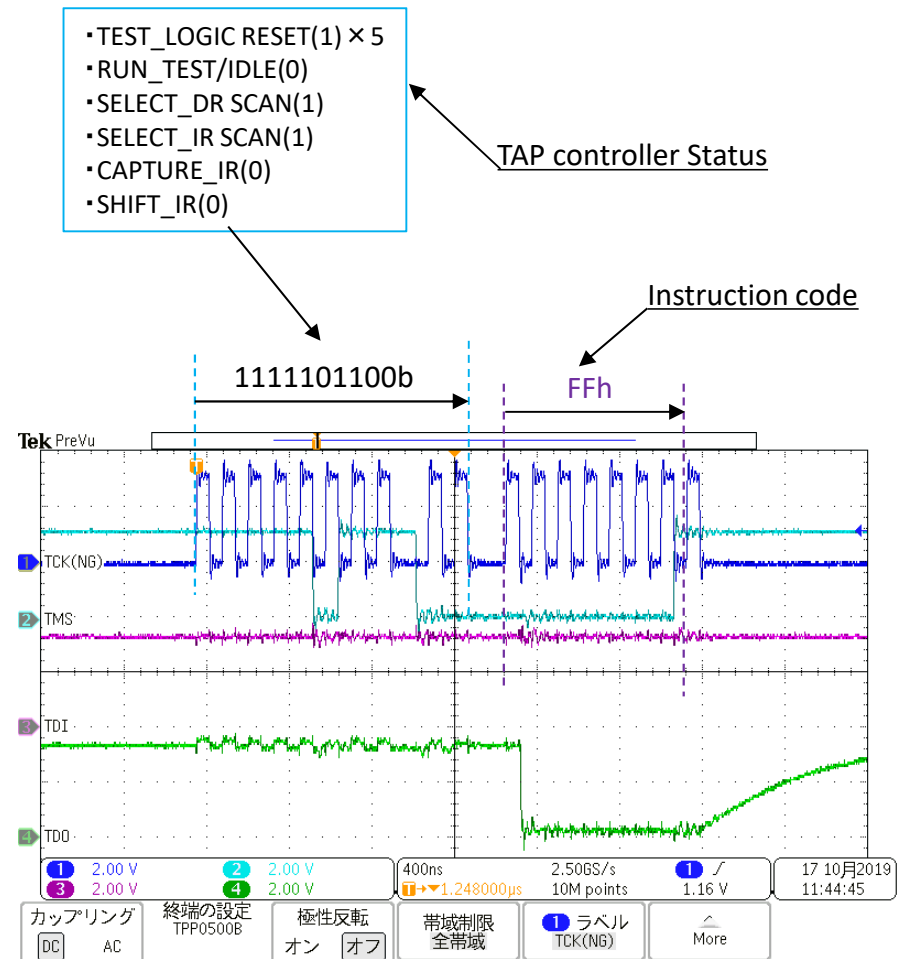


① Bypass instruction

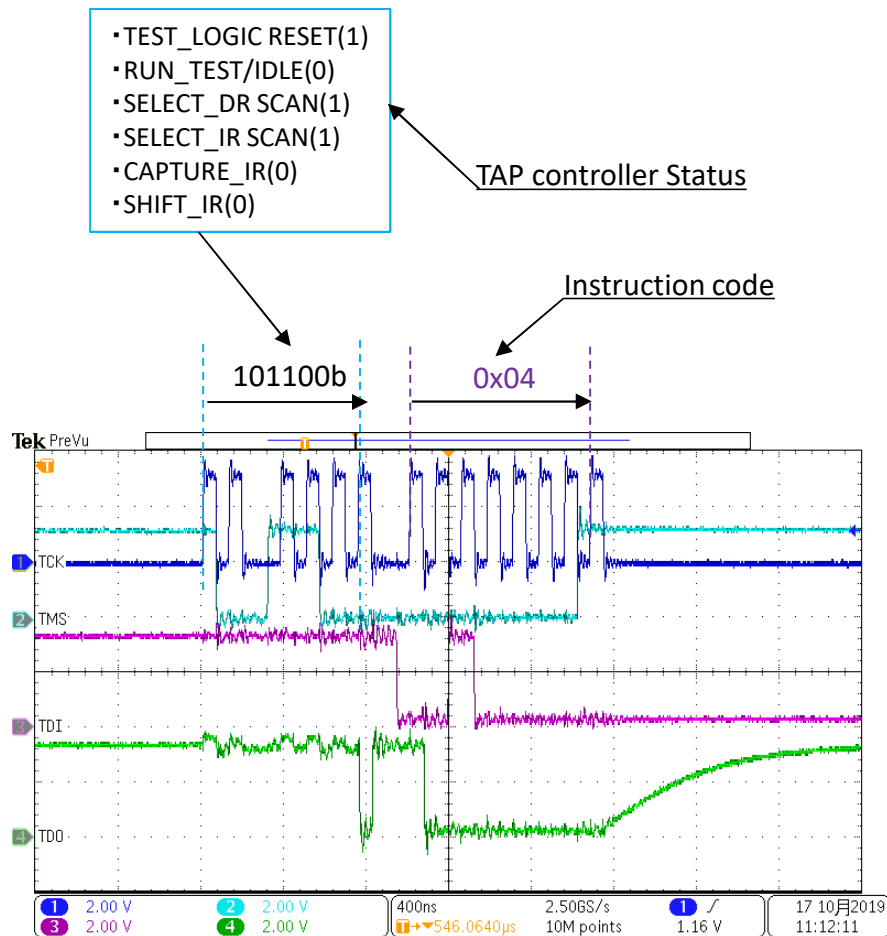


(a) normal DSP

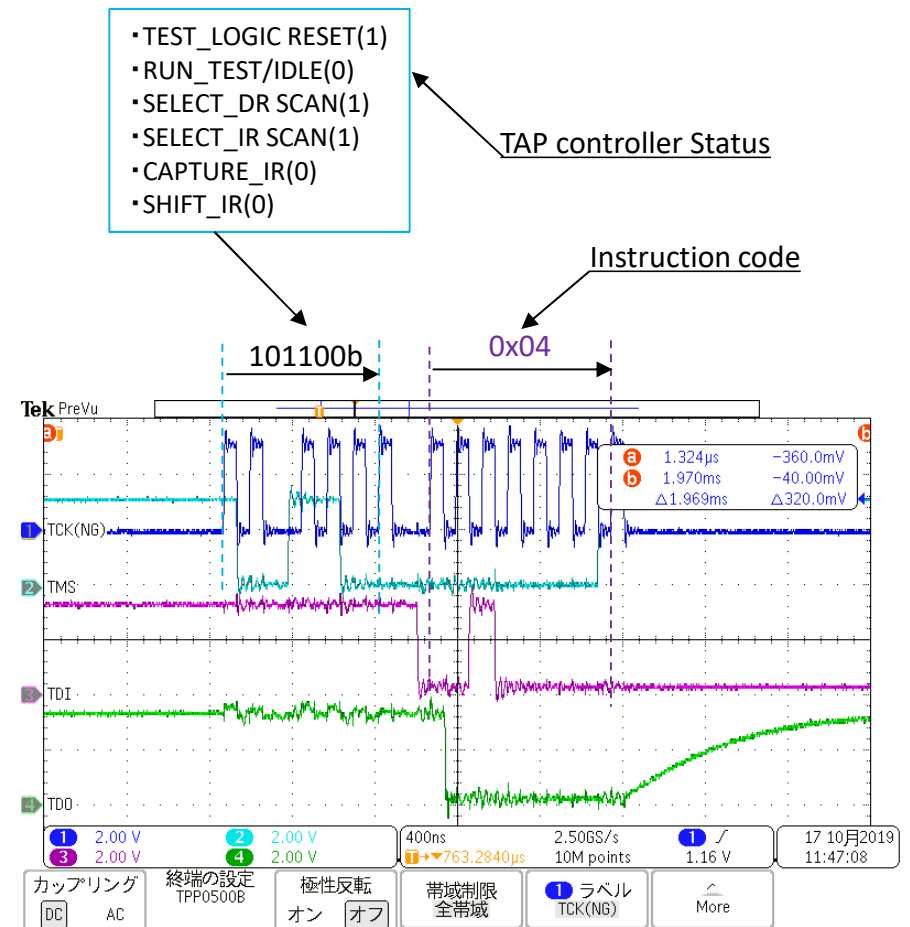


(b) specific DSP

② IDCODE instruction



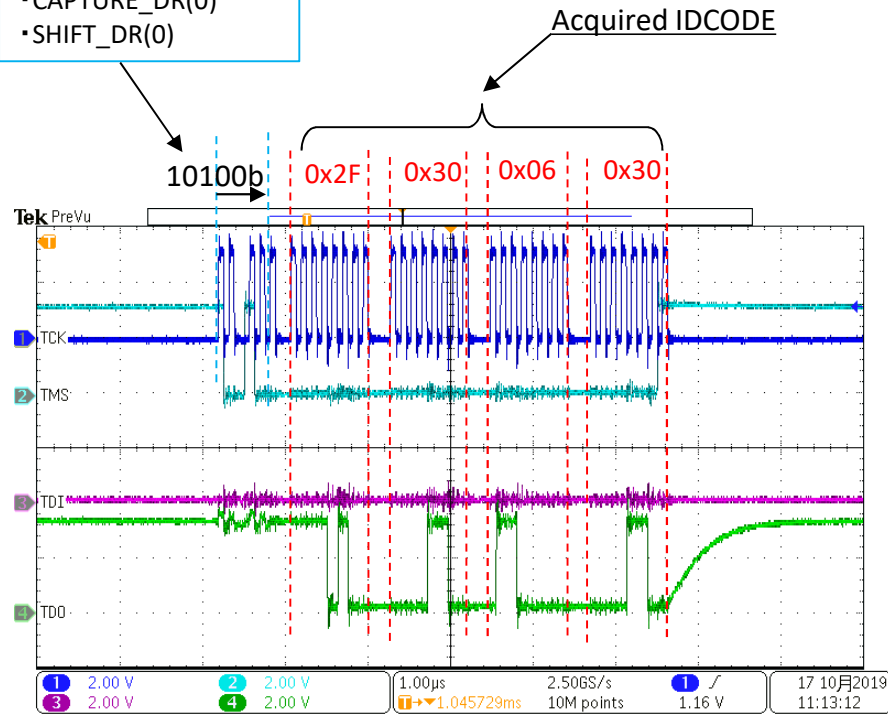
(a) normal DSP



(b) specific DSP

③ Acquired IDCODE

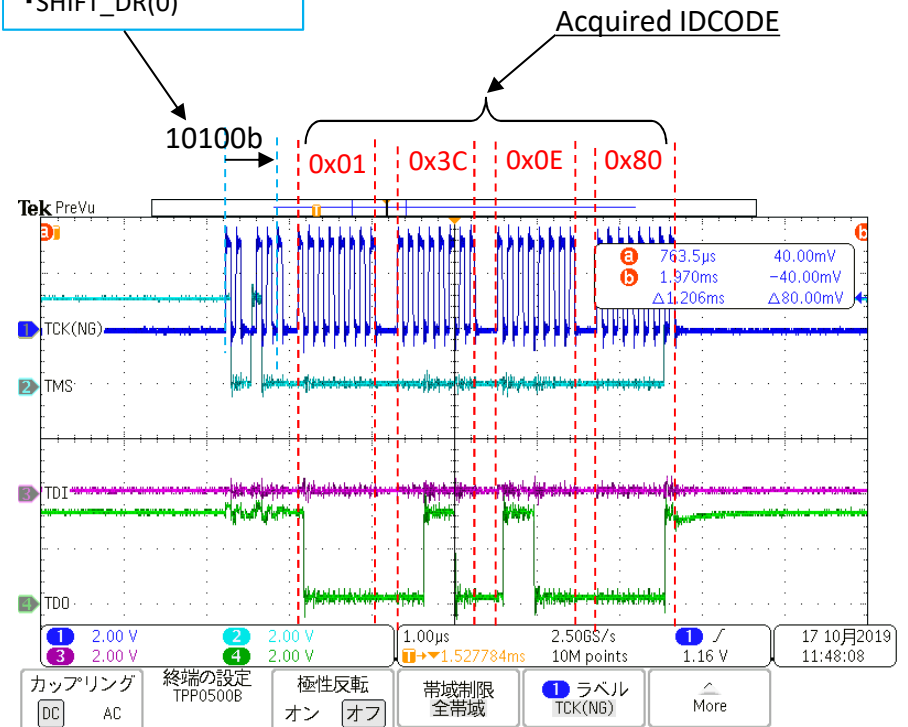
- TEST_LOGIC RESET(1)
- RUN_TEST/IDLE(0)
- SELECT_DR SCAN(1)
- CAPTURE_DR(0)
- SHIFT_DR(0)



The acquired IDCODE(0x3006302F) matches the expected IDCODE(0x3006302F).

(a) normal DSP

- TEST_LOGIC RESET(1)
- RUN_TEST/IDLE(0)
- SELECT_DR SCAN(1)
- CAPTURE_DR(0)
- SHIFT_DR(0)



The acquired IDCODE(0x800E3C01) does not match the expected IDCODE(0x3006302F).

(b) specific DSP