TMS320VC5410AZGU12 Request for confirmation of power-on sequence

1.Purpose
   Currently, we are considering a device that uses the TMS320VC5410AZGU12 for DSP.
   Check the power-on sequence for supplying power to this DSP.

2. DSP peripheral
   The power supply to DSP is + 3.3V / + 1.5V for the following pins.

         ・DVDD：+3.3V
         ・CVDD：+1.5V

3. Question matters
   When I asked your company about the sequence, I received the following answer.
   There is a word "simultaneous" in the answer, but I would like to confirm the following.

[Question matters]
   ①If "Start up at the same time and Shut down at the same time" is illustrated,
     which is the right figure-a) or b) in Fig. 3.1?
        ＞As our recognition, "simultaneous" assumes a) in Fig. 3.1.
   ②If the "simultaneous" defined by your company is b) in Fig. 3.1, should the voltage
     of DVDD not exceed the voltage of CDVD even for a moment?
   ③If the simultaneous timing is difficult, if you show the recommended timing of your company,
     is there any problem in recognition of c) in Figure 3.1?

[Content of your response]（E2E support forums：TMS320VC5410A: The power sequne of TMS320VC5410A）
         http://e2e.ti.com/support/processors/f/791/p/812391/3007189

         Hello Yokota,
         there are no strong requirements for power up/down sequence but here are some points which should be followed:
         The 5410A CVdd operates at1.5V and DVdd operates at 3.3V.
         Both supplies may be powered up/down simultaneously.
         If it is impossible to power-up/down the CVdd and DVdd simultaneously, CVdd must be powered up first, DVdd second.
         For power-down, DVdd must be powered down first, CVdd second.
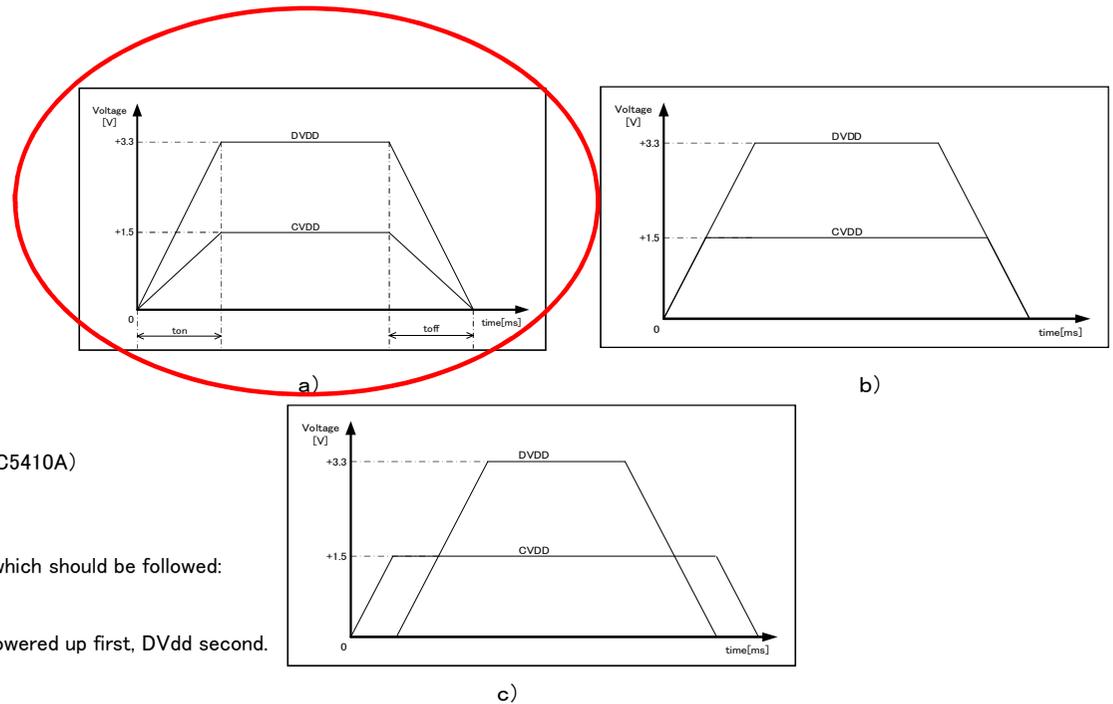   Hope this answers your question
         BR
         Michail

Best regards,

a)

b)

c)

Figure 3.1 Power-on sequence example