

TRACE32 ARM (POWER DEBUG USB)

File Edit View Var Break Run CPU Misc Trace Perf Cgv TI-AMox AM335x Linux_AM335x Window Help

Register/spotlight

N	R0	60	R8	1	S	Stack
Z	R1	44E09014	R9	9DF46EB8		
C	R2	44E35000	R10	0A		
V	R3	0	R11	1		
Q	R4	1	R12	9FFD38E0		
0	R5	0	R13	9DF38680		
1	R6	9FFCC010	R14	9FF90F60		
2	R7	9FFB8242	PC	9FF90EEC		
3	SPSR	0	CPSR	01B3		

60: B:VarFrame/Locals/Caller

serial_in_shift(inline)
ns16550_readb.isra.1(
?
@ addr = 0x44E09014
@ addr = 0x44E09014
-001 ns16550_serial_pending(
?
-002 fgetc.part.0(
· file = 0x0
-003 cread_line(inline)
-003 cli_readline_into_buffer(
@ prompt = 0x9FFB8242,
@ buffer = 0x9FFD4284,
· timeout = 0x0
@ p = 0x9FFD4284
· timeout = 0x0
@ buf = 0x9FFD4284
@ prompt = 0x9FFB8242
· num = 0x0

B:PER, "Control Module"

CM_PER_TPTC2_CLKCTRL	00030000	STBYST	Func	IDLEST
CM_PER_SPINLOCK_CLKCTRL <td>00030000</td> <td>IDLEST</td> <td>Disabled</td> <td>MODULEMODE</td>	00030000	IDLEST	Disabled	MODULEMODE
CM_PER_MAILBOX0_CLKCTRL <td>00030000</td> <td>IDLEST</td> <td>Disabled</td> <td>MODULEMODE</td>	00030000	IDLEST	Disabled	MODULEMODE
CM_PER_L4HS_CLKSTCTRL <td>0000007A</td> <td>CLKACTIVITY_CPSW_5MHZ_GCLK</td> <td>Active</td> <td>CLKACTIVITY_CPSW_50MH</td>	0000007A	CLKACTIVITY_CPSW_5MHZ_GCLK	Active	CLKACTIVITY_CPSW_50MH
CM_PER_L4HS_CLKCTRL <td>00000002</td> <td>CLKACTIVITY_L4HS_GCLK</td> <td>Active</td> <td>CLKTRCTRL</td>	00000002	CLKACTIVITY_L4HS_GCLK	Active	CLKTRCTRL
CM_PER_OCPWP_L3_CLKSTCTRL <td>00000012</td> <td>IDLEST</td> <td>Func</td> <td>MODULEMODE</td>	00000012	IDLEST	Func	MODULEMODE
CM_PER_OCPWP_L3_CLKCTRL <td>00000012</td> <td>CLKACTIVITY_OCPWP_L3_GCLK</td> <td>Gated</td> <td>CLKACTIVITY_OCPWP_L3</td>	00000012	CLKACTIVITY_OCPWP_L3_GCLK	Gated	CLKACTIVITY_OCPWP_L3

B:PER, "Interprocessor Communication"

Interprocessor Communication

MAILBOX

SPINLOCK

SPINLOCK_REV	????????	CLOCKACTIVITY	SIDLEMODE	ENWAKEUP	SO
SPINLOCK_SYSCFG <td>????????</td> <td>NUMLOCKS</td> <td>IU7</td> <td>IU6</td> <td>IU</td>	????????	NUMLOCKS	IU7	IU6	IU
SPINLOCK_SYSTAT <td>????????</td> <td>IU3</td> <td>IU2</td> <td>IU1</td> <td>IU</td>	????????	IU3	IU2	IU1	IU

Lock Registers

SPINLOCK_LOCK_REG_0	????????	TAKEN
SPINLOCK_LOCK_REG_1 <td>????????</td> <td>TAKEN</td>	????????	TAKEN
SPINLOCK_LOCK_REG_2 <td>????????</td> <td>TAKEN</td>	????????	TAKEN
SPINLOCK_LOCK_REG_3 <td>????????</td> <td>TAKEN</td>	????????	TAKEN
SPINLOCK_LOCK_REG_4 <td>????????</td> <td>TAKEN</td>	????????	TAKEN
SPINLOCK_LOCK_REG_5 <td>????????</td> <td>TAKEN</td>	????????	TAKEN
SPINLOCK_LOCK_REG_6 <td>????????</td> <td>TAKEN</td>	????????	TAKEN
SPINLOCK_LOCK_REG_7 <td>????????</td> <td>TAKEN</td>	????????	TAKEN
SPINLOCK_LOCK_REG_8 <td>????????</td> <td>TAKEN</td>	????????	TAKEN
SPINLOCK_LOCK_REG_9 <td>????????</td> <td>TAKEN</td>	????????	TAKEN
SPINLOCK_LOCK_REG_10 <td>????????</td> <td>TAKEN</td>	????????	TAKEN
SPINLOCK_LOCK_REG_11 <td>????????</td> <td>TAKEN</td>	????????	TAKEN
SPINLOCK_LOCK_REG_12 <td>????????</td> <td>TAKEN</td>	????????	TAKEN
SPINLOCK_LOCK_REG_13 <td>????????</td> <td>TAKEN</td>	????????	TAKEN
SPINLOCK_LOCK_REG_14 <td>????????</td> <td>TAKEN</td>	????????	TAKEN
SPINLOCK_LOCK_REG_15 <td>????????</td> <td>TAKEN</td>	????????	TAKEN
SPINLOCK_LOCK_REG_16 <td>????????</td> <td>TAKEN</td>	????????	TAKEN
SPINLOCK_LOCK_REG_17 <td>????????</td> <td>TAKEN</td>	????????	TAKEN

components trace Data Var List PERF SYSTEM Step Go Break symbol Frame Register FPU MMX MMU TRANSLATION CACHE other previous

NST:9FF90EEC [\\u-boot\\ns16550\\ns16550_readb.isra.1+0x0C [serial_in_shift] stopped MDX UP

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CM_PER_L4HS_CLKSTCTRL <td>0000007A</td> <td>CLKACTIVITY_CPSW_5MHZ_GCLK</td> <td>Active</td> <td>CLKACTIVITY_CPSW_50MH</td>	0000007A	CLKACTIVITY_CPSW_5MHZ_GCLK	Active	CLKACTIVITY_CPSW_50MH
CM_PER_L4HS_CLKCTRL <td>00000002</td> <td>CLKACTIVITY_L4HS_GCLK</td> <td>Active</td> <td>CLKTRCTRL</td>	00000002	CLKACTIVITY_L4HS_GCLK	Active	CLKTRCTRL
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CM_PER_OCPWP_L3_CLKCTRL <td>00000012</td> <td>CLKACTIVITY_OCPWP_L3_GCLK</td> <td>Gated</td> <td>CLKACTIVITY_OCPWP_L3</td>	00000012	CLKACTIVITY_OCPWP_L3_GCLK	Gated	CLKACTIVITY_OCPWP_L3

B:PER, "Interprocessor Communication"

Interprocessor Communication

MAILBOX

SPINLOCK

SPINLOCK_REV	50020000	CLOCKACTIVITY	SIDLEMODE	ENWAKEUP	SO
SPINLOCK_SYSCFG <td>00000011</td> <td>Not required</td> <td>Smart-idle</td> <td>Low</td> <td>Low</td>	00000011	Not required	Smart-idle	Low	Low
SPINLOCK_SYSTAT <td>04000001</td> <td>NUMLOCKS<td>04</td><td>Low</td><td>Low</td></td>	04000001	NUMLOCKS <td>04</td> <td>Low</td> <td>Low</td>	04	Low	Low
<td></td> <td>IU3</td> <td>IU2</td> <td>IU1</td> <td>IU</td>		IU3	IU2	IU1	IU

Lock Registers

SPINLOCK_LOCK_REG_0	00000000	TAKEN	Not taken
SPINLOCK_LOCK_REG_1 <td>00000000</td> <td>TAKEN</td> <td>Not taken</td>	00000000	TAKEN	Not taken
SPINLOCK_LOCK_REG_2 <td>00000000</td> <td>TAKEN</td> <td>Not taken</td>	00000000	TAKEN	Not taken
SPINLOCK_LOCK_REG_3 <td>00000000</td> <td>TAKEN</td> <td>Not taken</td>	00000000	TAKEN	Not taken
SPINLOCK_LOCK_REG_4 <td>00000000</td> <td>TAKEN</td> <td>Not taken</td>	00000000	TAKEN	Not taken
SPINLOCK_LOCK_REG_5 <td>00000000</td> <td>TAKEN</td> <td>Not taken</td>	00000000	TAKEN	Not taken
SPINLOCK_LOCK_REG_6 <td>00000000</td> <td>TAKEN</td> <td>Not taken</td>	00000000	TAKEN	Not taken
SPINLOCK_LOCK_REG_7 <td>00000000</td> <td>TAKEN</td> <td>Not taken</td>	00000000	TAKEN	Not taken
SPINLOCK_LOCK_REG_8 <td>00000000</td> <td>TAKEN</td> <td>Not taken</td>	00000000	TAKEN	Not taken
SPINLOCK_LOCK_REG_9 <td>00000000</td> <td>TAKEN</td> <td>Not taken</td>	00000000	TAKEN	Not taken
SPINLOCK_LOCK_REG_10 <td>00000000</td> <td>TAKEN</td> <td>Not taken</td>	00000000	TAKEN	Not taken
SPINLOCK_LOCK_REG_11 <td>00000000</td> <td>TAKEN</td> <td>Not taken</td>	00000000	TAKEN	Not taken
SPINLOCK_LOCK_REG_12 <td>00000000</td> <td>TAKEN</td> <td>Not taken</td>	00000000	TAKEN	Not taken
SPINLOCK_LOCK_REG_13 <td>00000000</td> <td>TAKEN</td> <td>Not taken</td>	00000000	TAKEN	Not taken
SPINLOCK_LOCK_REG_14 <td>00000000</td> <td>TAKEN</td> <td>Not taken</td>	00000000	TAKEN	Not taken
SPINLOCK_LOCK_REG_15 <td>00000000</td> <td>TAKEN</td> <td>Not taken</td>	00000000	TAKEN	Not taken
SPINLOCK_LOCK_REG_16 <td>00000000</td> <td>TAKEN</td> <td>Not taken</td>	00000000	TAKEN	Not taken
SPINLOCK_LOCK_REG_17 <td>00000000</td> <td>TAKEN</td> <td>Not taken</td>	00000000	TAKEN	Not taken

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