

Project Title : Sitara AM335x Board for LiComm Network MPU

PCB Name : BeagleBone LiComm Rev.2.0

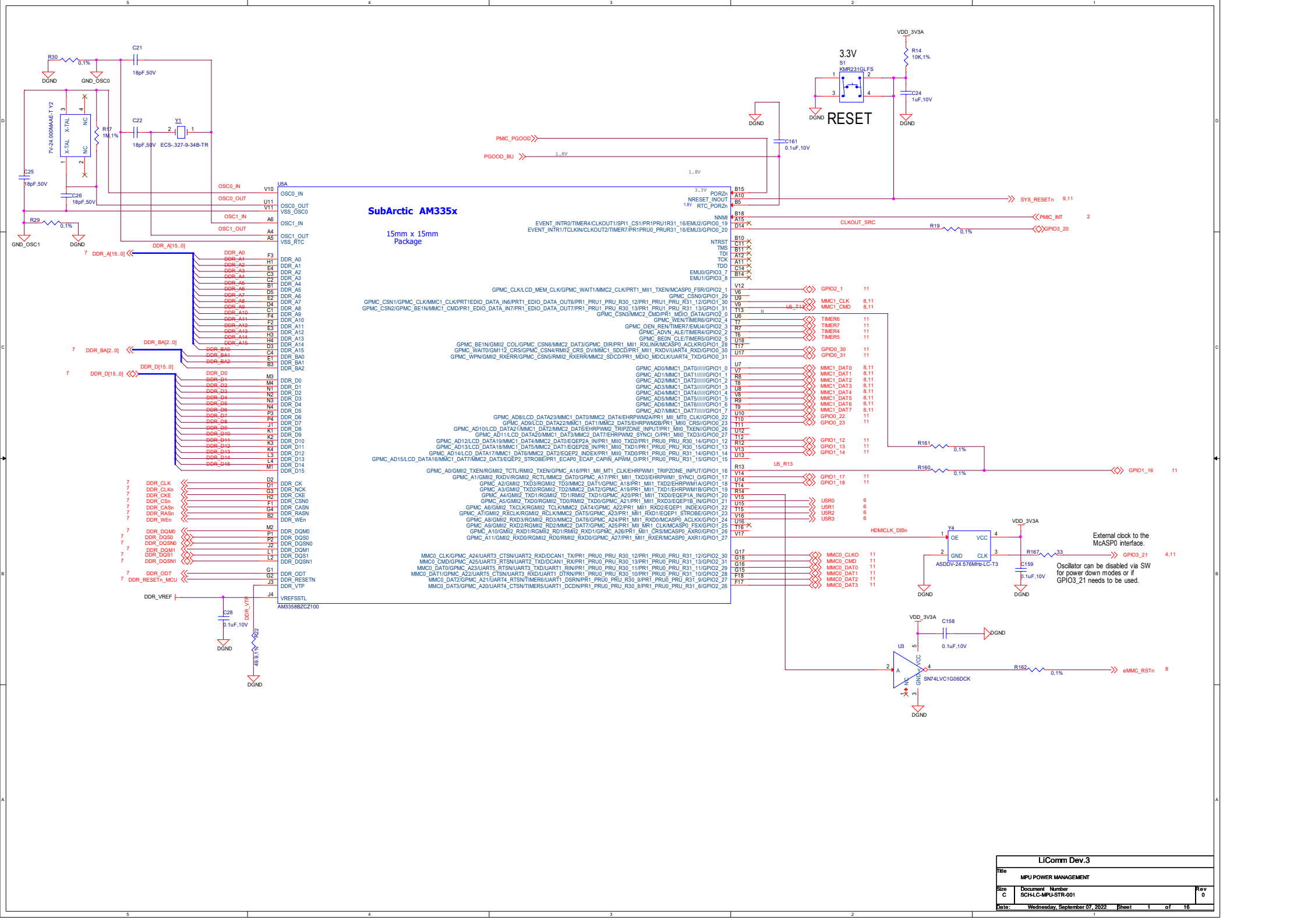
2022.08.16.

REV	Description	DATE	BY
V1.00	Initial production Release. Sitara Processor : AM3358BZCZ100.	03/16/2016	CMK
V1.10	P8,P9 커넥터 뿔 수정 USB HOST 경설 계 , eMMC FLASH 수정	06/09/2016	CMK
V2.00	PMIC Change : TPS65218D0PHPT Bootring Memory : eMMC => micro SD	08/16/2022	JBj

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3	PROCESSOR 1 OF 3, JTAG HEADER
4	PROCESSOR 2 OF 3, UAB PORTS
5	PROCESSOR 3 OF 3
6	LED, CONFIGURATION AND BUTTON
7	DDR3 MEMORY
8	eMMC FLASH
9	10/100 ETHERNET
10	EXP CONN, uSD
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NOTE: PCB Revision for this board is Rev 1

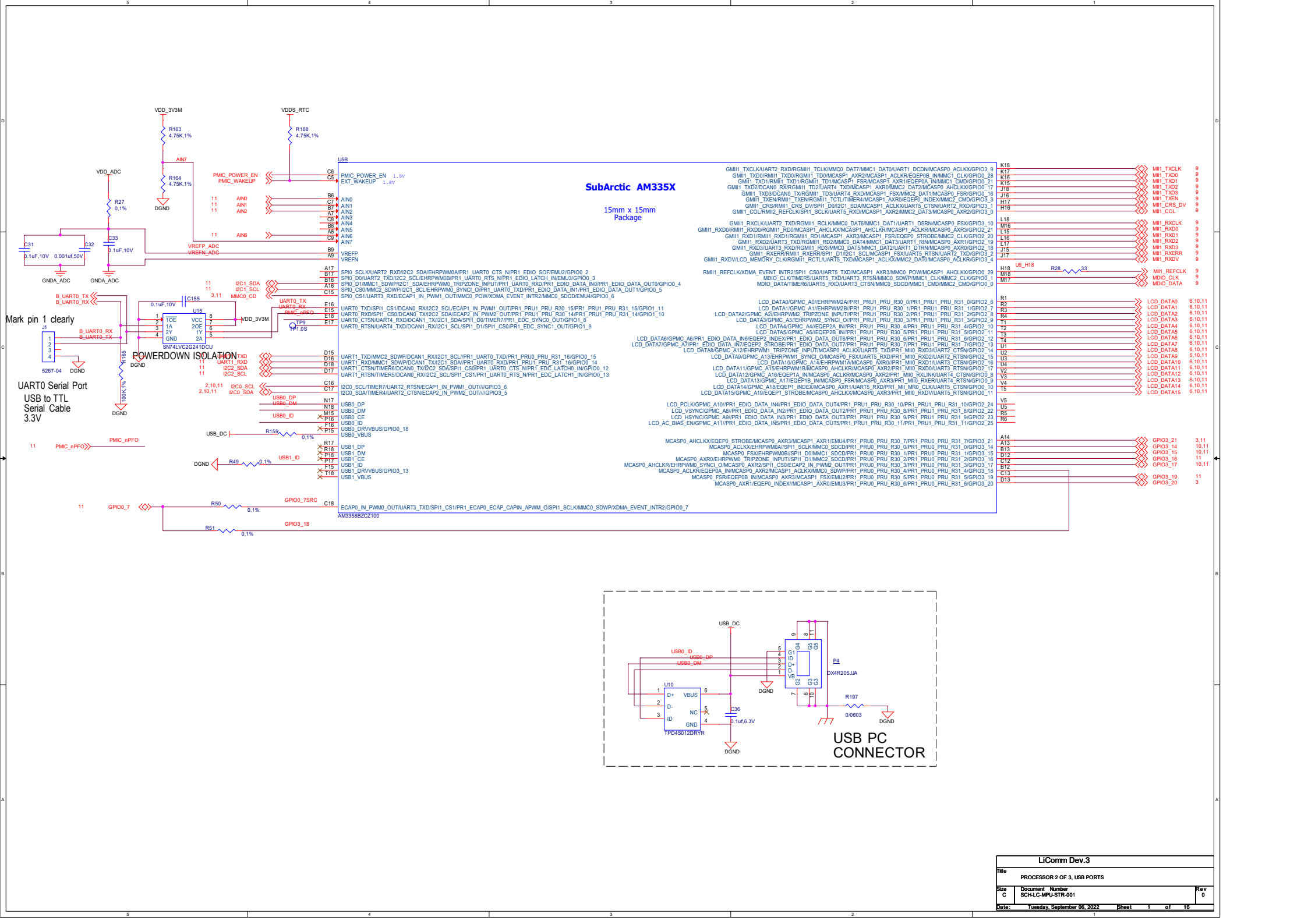
LiComm Dev.3			
Title BeagleBone LiComm (Sitara AM335x)			
Size B	Document Number SCH.LC-MPU-STR-001	Rev 0	
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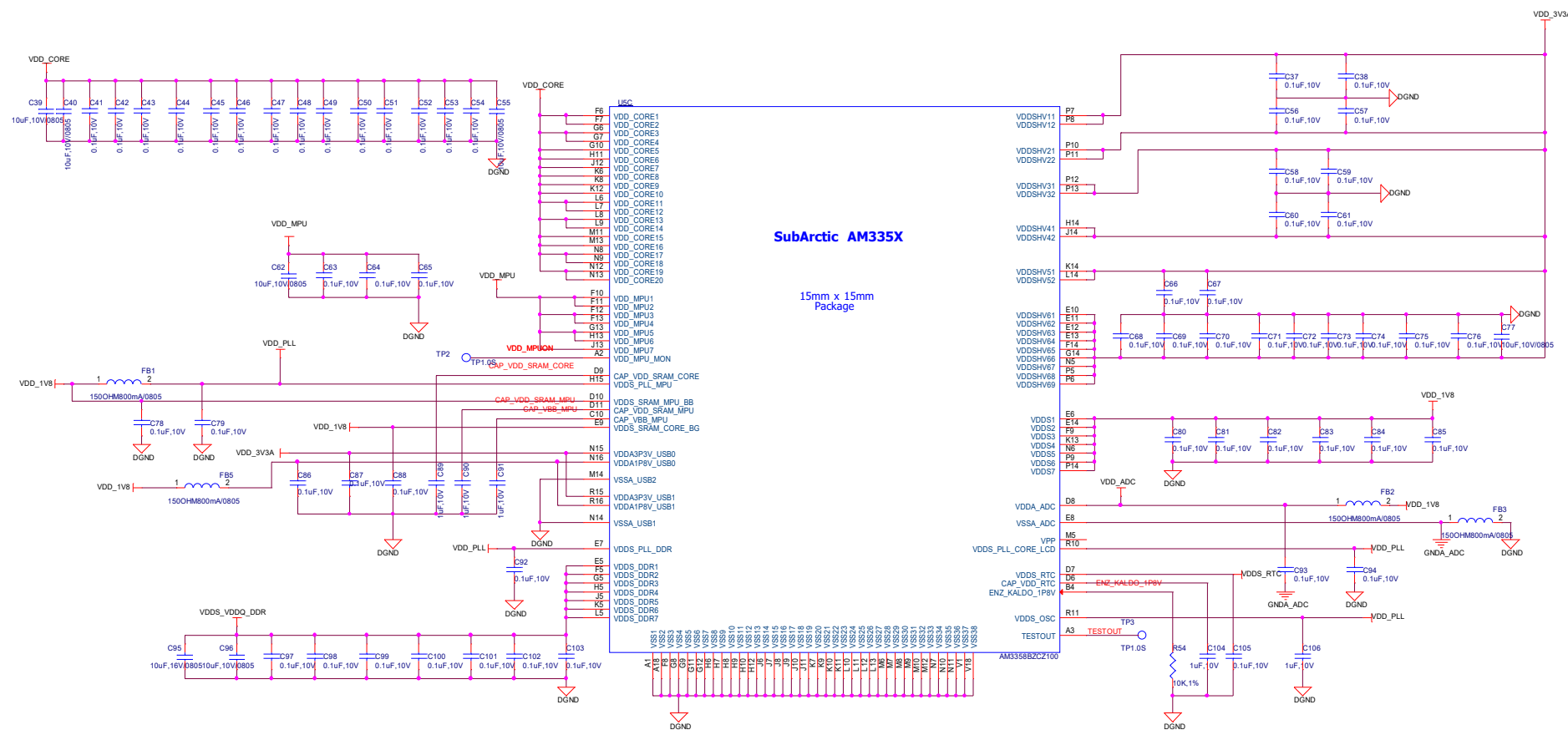


SubArctic AM335x

15mm x 15mm Package

Pin	Signal	Function
V10	OSC0_IN	OSC0_IN
V11	OSC0_OUT	OSC0_OUT
V12	VSS_OSC0	VSS_OSC0
A6	OSC1_IN	OSC1_IN
A5	OSC1_OUT	OSC1_OUT
A5	VSS_RTC	VSS_RTC
F3	DDR_A0	DDR_A0
H1	DDR_A1	DDR_A1
F6	DDR_A2	DDR_A2
C3	DDR_A3	DDR_A3
A3	DDR_A4	DDR_A4
C2	DDR_A5	DDR_A5
B1	DDR_A6	DDR_A6
D5	DDR_A7	DDR_A7
E2	DDR_A8	DDR_A8
D4	DDR_A9	DDR_A9
C1	DDR_A10	DDR_A10
F4	DDR_A11	DDR_A11
F2	DDR_A12	DDR_A12
H3	DDR_A13	DDR_A13
H4	DDR_A14	DDR_A14
D3	DDR_A15	DDR_A15
C4	DDR_BA0	DDR_BA0
E1	DDR_BA1	DDR_BA1
B3	DDR_BA2	DDR_BA2
M3	DDR_D0	DDR_D0
N4	DDR_D1	DDR_D1
N2	DDR_D2	DDR_D2
N3	DDR_D3	DDR_D3
N1	DDR_D4	DDR_D4
P3	DDR_D5	DDR_D5
P4	DDR_D6	DDR_D6
K1	DDR_D7	DDR_D7
J1	DDR_D8	DDR_D8
K2	DDR_D9	DDR_D9
K3	DDR_D10	DDR_D10
K4	DDR_D11	DDR_D11
L3	DDR_D12	DDR_D12
L4	DDR_D13	DDR_D13
L1	DDR_D14	DDR_D14
M1	DDR_D15	DDR_D15
D2	DDR_CLK	DDR_CLK
G1	DDR_CLKn	DDR_CLKn
H2	DDR_CKE	DDR_CKE
H3	DDR_CKE	DDR_CKE
F1	DDR_CS0	DDR_CS0
G1	DDR_CS0n	DDR_CS0n
B2	DDR_RAS0	DDR_RAS0
B1	DDR_RAS0n	DDR_RAS0n
M2	DDR_WE0	DDR_WE0
P1	DDR_DOM0	DDR_DOM0
P2	DDR_DOM0	DDR_DOM0
J2	DDR_DOS0	DDR_DOS0
L1	DDR_DOS1	DDR_DOS1
L2	DDR_DOS1	DDR_DOS1
G1	DDR_ODT	DDR_ODT
G2	DDR_RESETn	DDR_RESETn
J2	DDR_VTP	DDR_VTP
J4	VREFSSTL	VREFSSTL

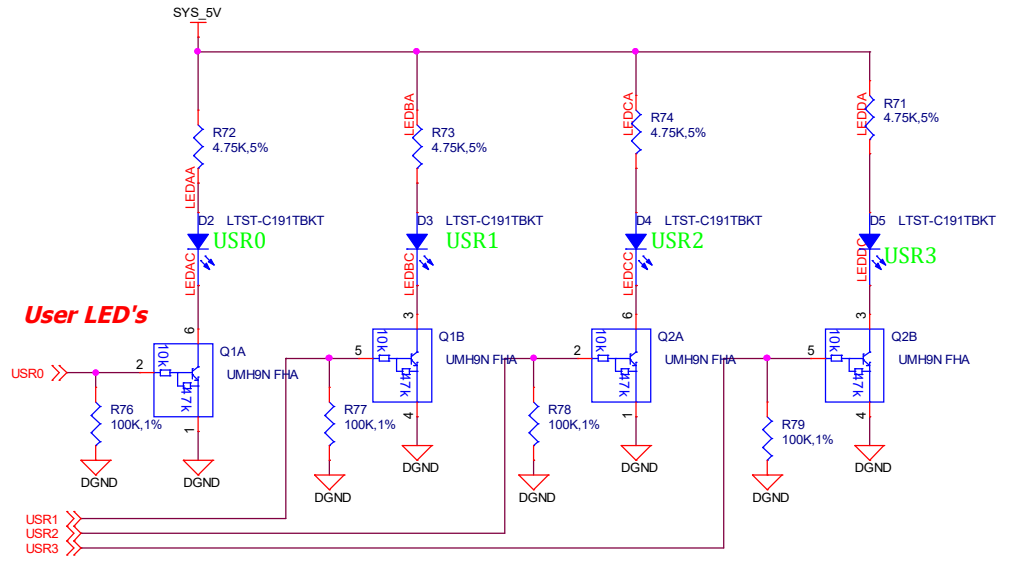
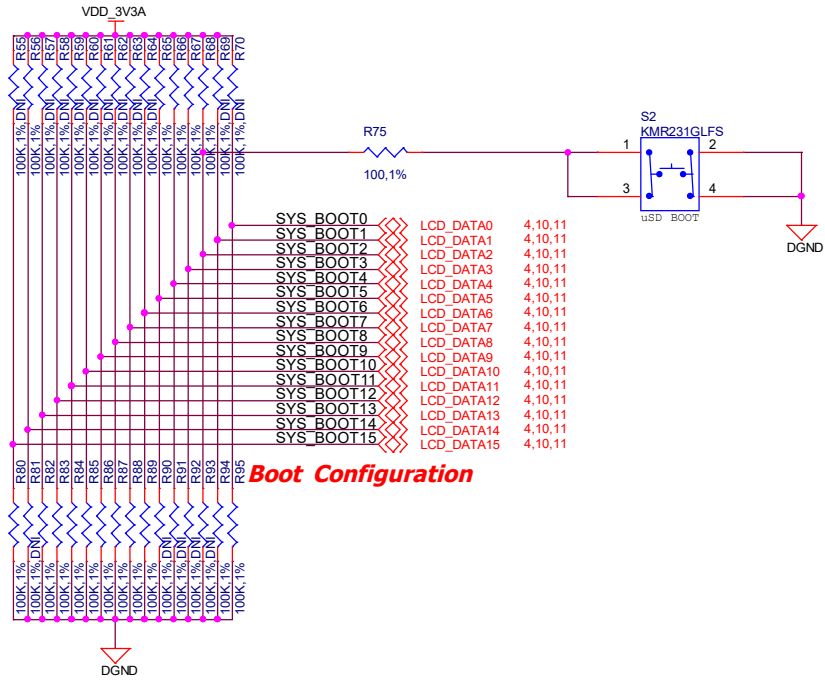




SubArctic AM335X

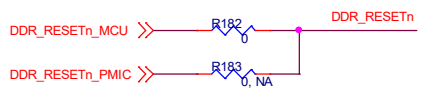
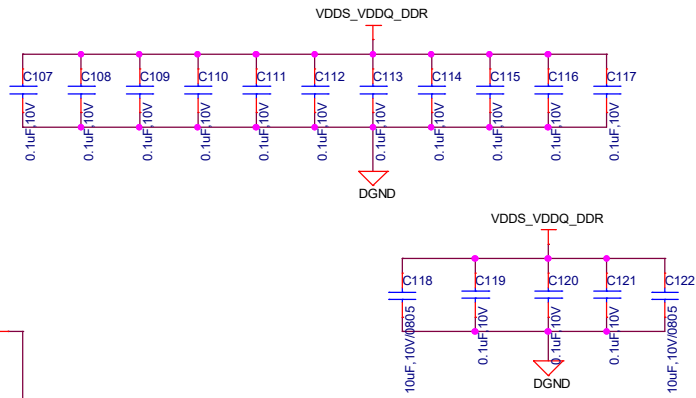
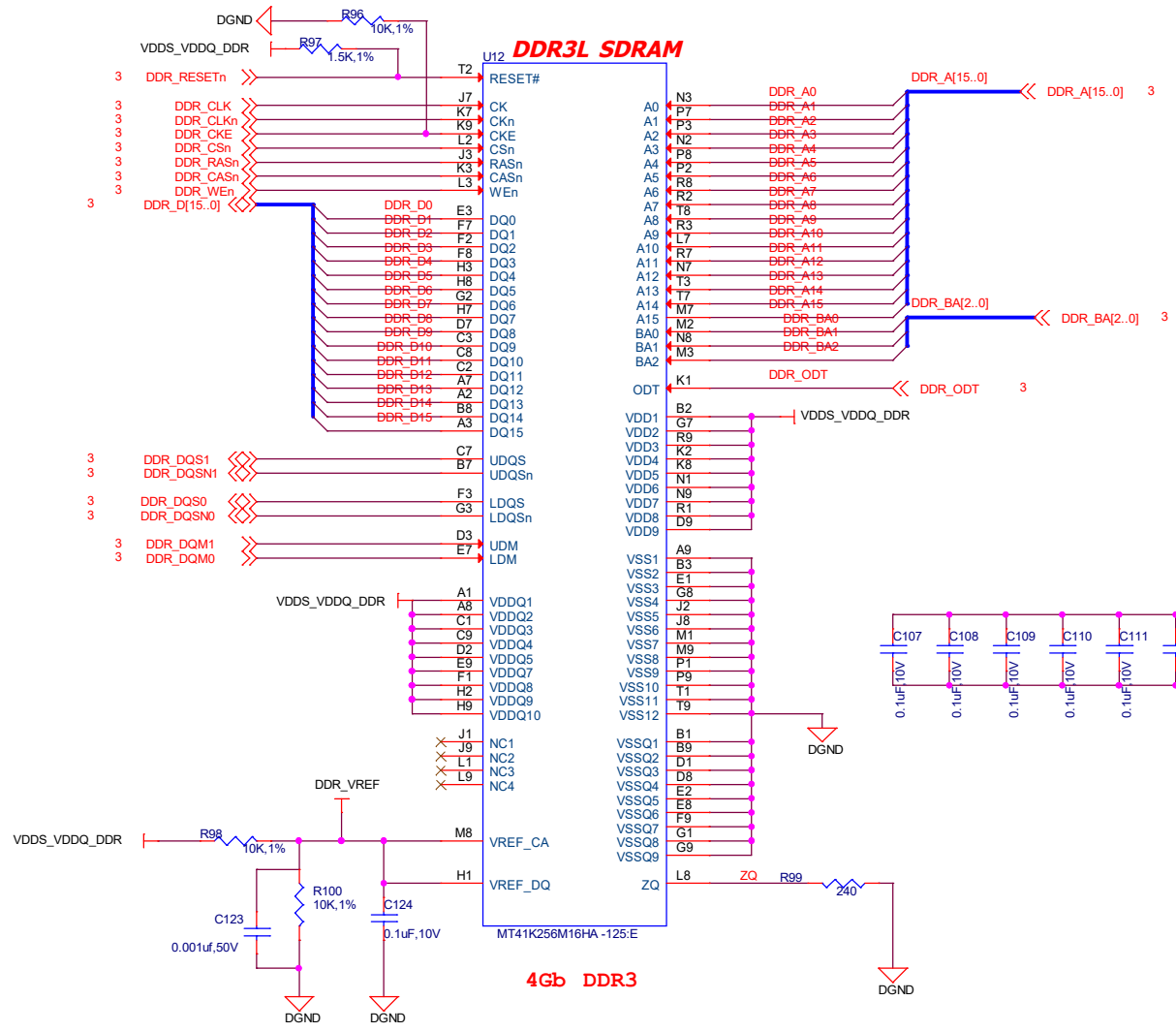
15mm x 15mm Package

LiComm Dev.3			
Title PROCESSOR 3 OF 3			
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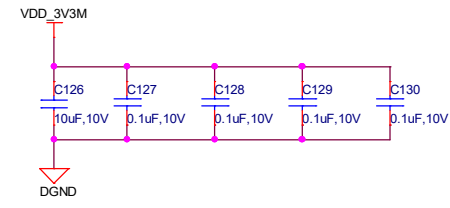
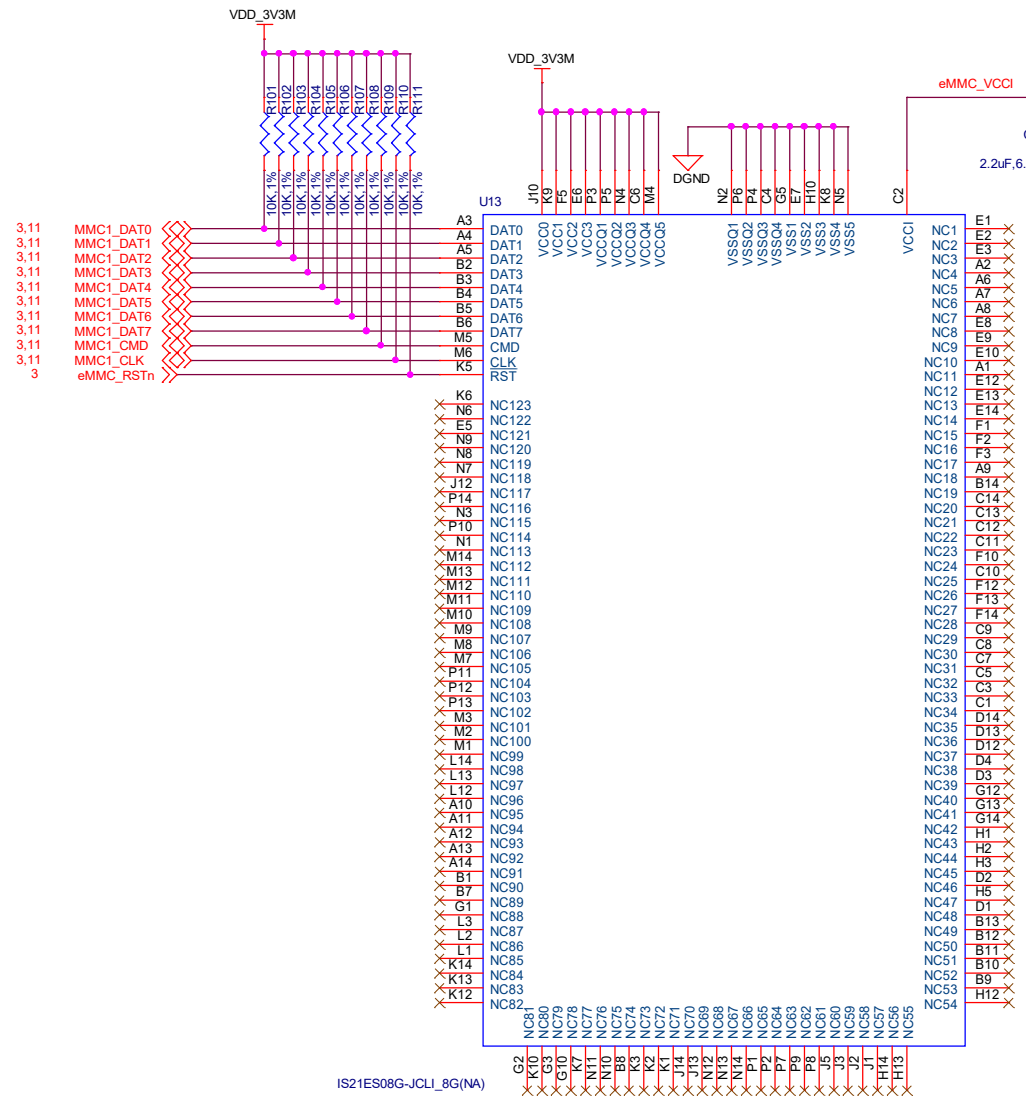


SYSBOOT[15:14]	SYSBOOT[13:12]	SYSBOOT[11:10]	SYSBOOT[9]	SYSBOOT[8]	SYSBOOT[7:6]	SYSBOOT[5]	SYSBOOT[4:0]	Boot Sequence			
00b = 19.2MHz 01b = 24MHz 10b = 25MHz 11b = 26MHz	00b (all other values reserved)	Don't care for ROM code	Don't care for ROM code	Don't care for ROM code	Don't care for ROM code	0 = CLKOUT1 disabled 1 = CLKOUT1 enabled	11100b	MMC1	MMC0	UART0	USB0[5]
00b = 19.2MHz 01b = 24MHz 10b = 25MHz 11b = 26MHz	00b (all other values reserved)	Don't care for ROM code	Don't care for ROM code	Don't care for ROM code	Don't care for ROM code	0 = CLKOUT1 disabled 1 = CLKOUT1 enabled	11000b	SPI0	MMC0	USB0[5]	UART0

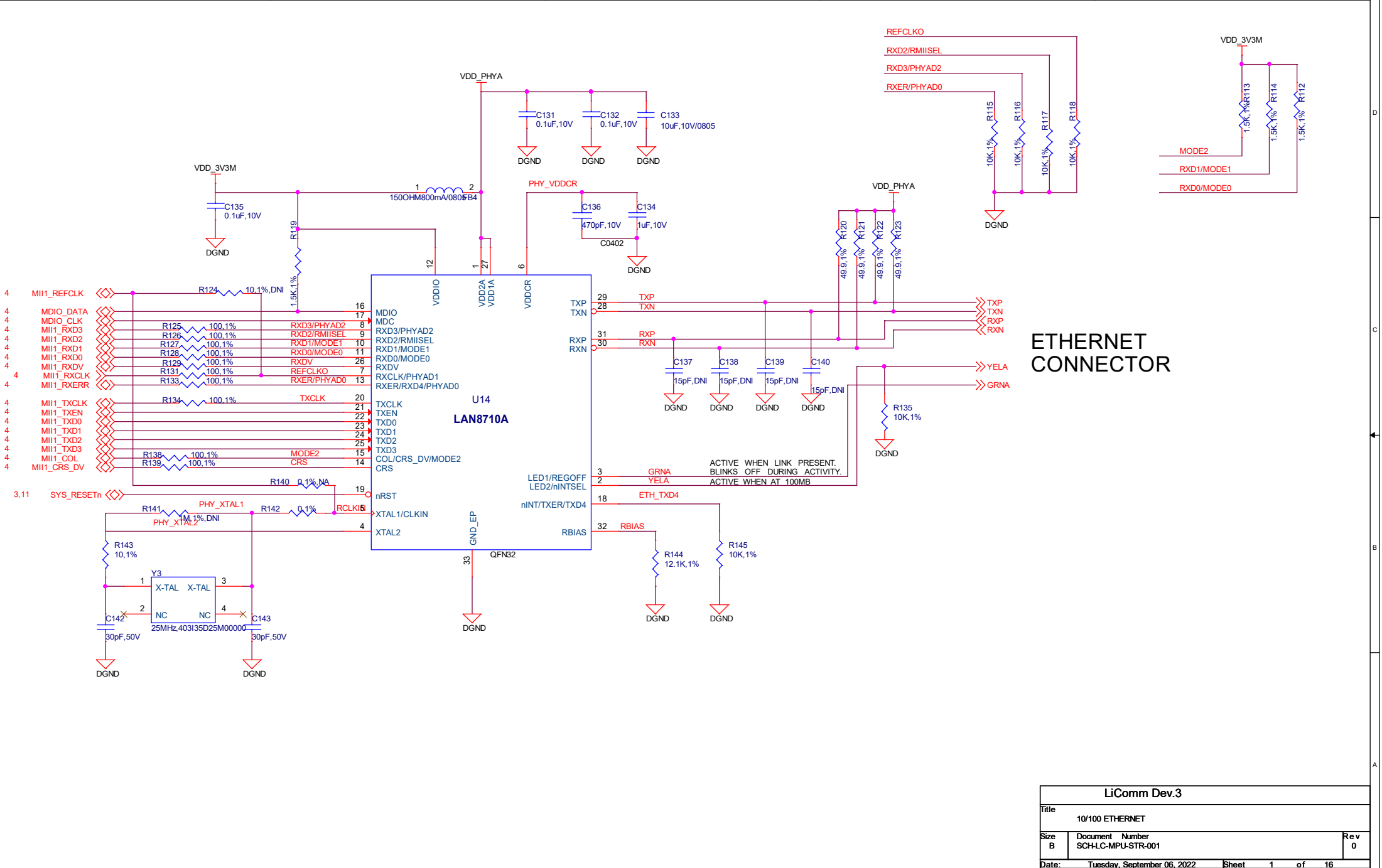
LiComm Dev.3			
Title LED, CONFIGURATION AND BUTTON			
Size B	Document Number SCH.LC-MPU-STR-001	Rev 0	
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LiComm Dev.3		
Title DDR3 MEMORY		
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Title		
eMMC FLASH		
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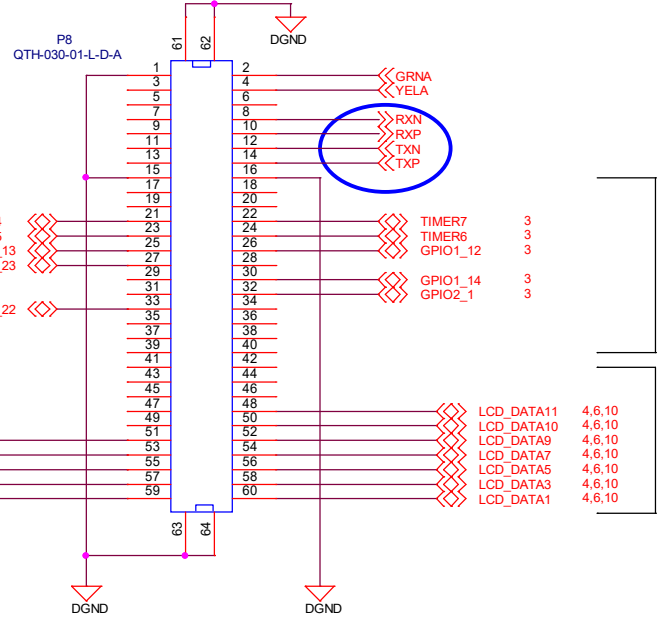


ETHERNET CONNECTOR

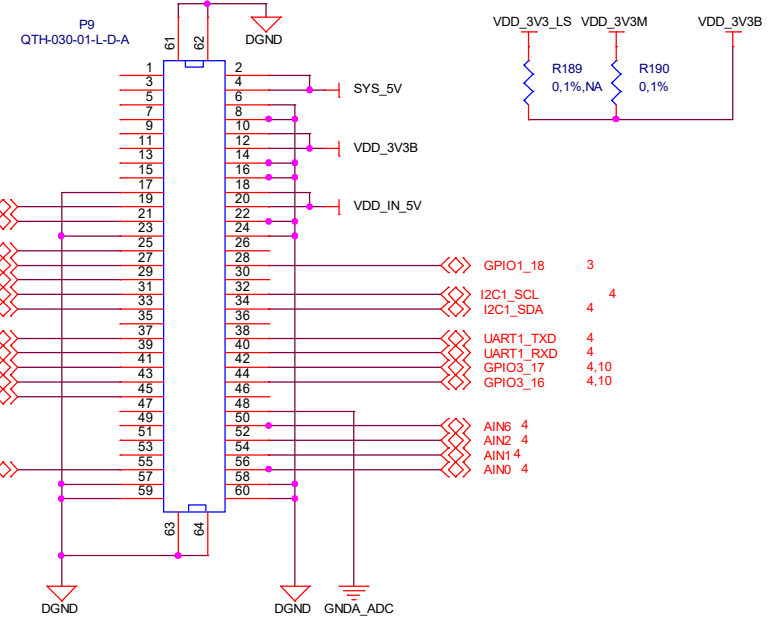
LiComm Dev.3		
Title	10/100 ETHERNET	
Size	Document Number	Rev
B	SCH.LC-MPU-STR-001	0
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CAUTION: USED ON BOARD

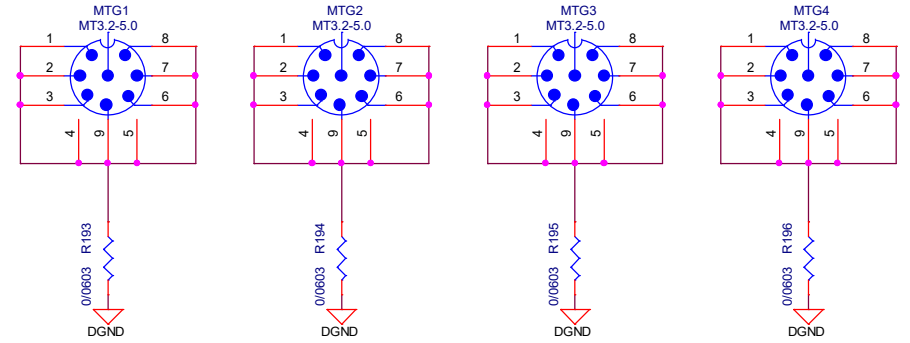
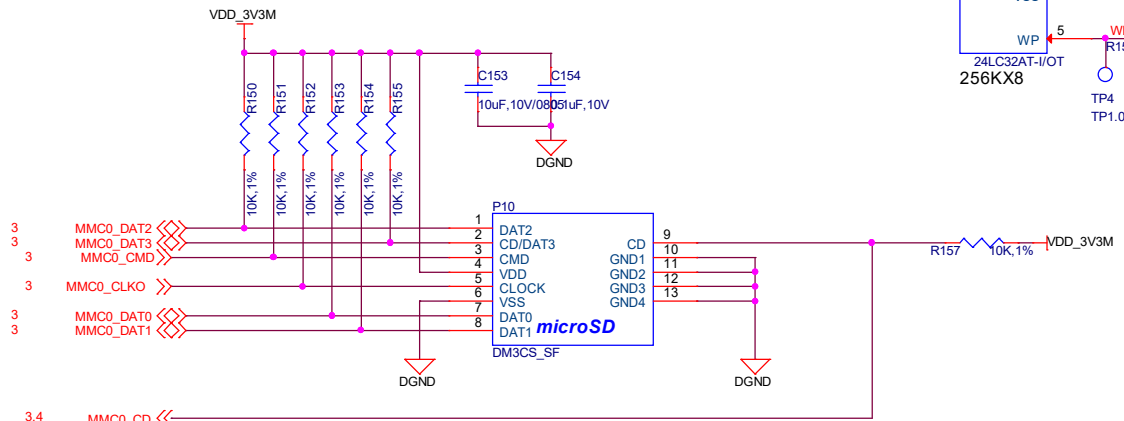
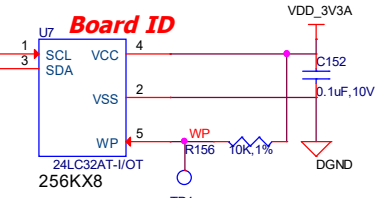
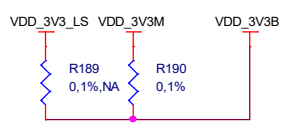
CAUTION: USED ON BOARD



EXPANSION HEADER



EXPANSION HEADER



LiComm Dev.3		
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