Text Compare
Produced: 2024-10-24 8:44:47 AM

Mode: All, Ignoring Unimportant
Left file: C:\ti\16GB.txt Right file: C:\ti\64GB.txt

U-Boot 2024.04-dirty (Oct 21 2024 - 16:18:21 +0200) SoC: AM64X SR2.0 HS-FS Model: DCS_SOM - Distributed Control System - System On Module EEPROM not available at 0x50, trying to read at 0x51 Reading on-board EEPROM at 0x51 failed -121 DRAM: 2 GiB Core: 82 devices, 30 uclasses, devicetree: separate NAND: 0 MiB MMC: mmc@fa10000: 0 Loading Environment from nowhere... OK In: serial@2820000 Out: serial@2820000 Err: serial@2820000 Failed to probe prueth driver [setup_board_eeprom_env] EEPROM not available at 0x50, trying to read at 0x51 Reading on-board EEPROM at 0x51 failed -121 [setup_board_eeprom_env]: invalid eeprom [setup_board_eeprom_env] Net: eth0: ethernet@8000000port@1	=	U-Boot 2024.04-dirty (Oct 21 2024 - 16:18:21 +0200) SoC: AM64X SR2.0 HS-FS Model: DCS_SOM - Distributed Control System - System On Module EEPROM not available at 0x50, trying to read at 0x51 Reading on-board EEPROM at 0x51 failed -121 DRAM: 2 GiB Core: 82 devices, 30 uclasses, devicetree: separate NAND: 0 MiB MMC: mmc@fa10000: 0 Loading Environment from nowhere... OK In: serial@2820000 Out: serial@2820000 Err: serial@2820000 Failed to probe prueth driver [setup_board_eeprom_env] EEPROM not available at 0x50, trying to read at 0x51 Reading on-board EEPROM at 0x51 failed -121 [setup_board_eeprom_env]: invalid eeprom [setup_board_eeprom_env] Net: eth0: ethernet@8000000port@1
Warning: ethernet@8000000port@2 (eth1) using random MAC address - fe:99:d4:65:b4:2e	<>	Warning: ethernet@8000000port@2 (eth1) using random MAC address - 6a:7e:72:b2:37:4b
, eth1: ethernet@8000000port@2	=	, eth1: ethernet@8000000port@2
Hit any key to stop autoboot: 100 99 98 0 => mmc info Device: mmc@fa10000 Manufacturer ID: 13 OEM: 4e Name: S0J56X Bus Speed: 200000000 Mode: HS200 (200MHz) Rd Block Len: 512 MMC version 5.1 High Capacity: Yes Capacity: 14.8 GiB Bus Width: 8-bit Erase Group Size: 512 KiB HC WP Group Size: 8 MiB User Capacity: 14.8 GiB WRREL Boot Capacity: 31.5 MiB ENH RPMB Capacity: 4 MiB ENH Boot area 0 is not write protected Boot area 1 is not write protected => mmc list mmc@fa10000: 0 (eMMC)	<>	Hit any key to stop autoboot: 100 99 0

=> mmc part ## Unknown partition table type 0		
=> mmc dev 0; switch to partitions #0, OK mmc0(part 0) is current device	=	=> mmc dev 0; switch to partitions #0, OK mmc0(part 0) is current device
=> mmc rescan; => mmc dev 0 1;	<>	=> mmc rescan 11; bus_mode requested is not supported => mmc rescan 10; => mmc rescan 11 bus_mode requested is not supported => mmc rescan 10 => mmc dev 0 1 11; bus_mode requested is not supported => mmc dev 0 1 10;
switch to partitions #1, OK mmc0(part 1) is current device => tftp ${loadaddr} tiboot3.bin; link up on port 1, speed 1000, full duplex Using ethernet@8000000port@1 device TFTP from server 192.168.40.166; our IP address is 192.168.40.50 Filename 'tiboot3.bin'. Load address: 0x82000000 Loading: *### ### 0 Bytes	=	switch to partitions #1, OK mmc0(part 1) is current device => tftp ${loadaddr} tiboot3.bin; link up on port 1, speed 1000, full duplex Using ethernet@8000000port@1 device TFTP from server 192.168.40.166; our IP address is 192.168.40.50 Filename 'tiboot3.bin'. Load address: 0x82000000 Loading: *### ### 0 Bytes
6.7 MiB/s	<>	6.5 MiB/s
done Bytes transferred = 617502 (96c1e hex) => mmc write ${loadaddr} 0x0 0x800; MMC write: dev # 0, block # 0, count 2048 ... 2048 blocks written: OK => tftp ${loadaddr} tispl.bin; link up on port 1, speed 1000, full duplex Using ethernet@8000000port@1 device TFTP from server 192.168.40.166; our IP address is 192.168.40.50 Filename 'tispl.bin'. Load address: 0x82000000 Loading: *### ### ### ##### 0 Bytes	=	done Bytes transferred = 617502 (96c1e hex) => mmc write ${loadaddr} 0x0 0x800; MMC write: dev # 0, block # 0, count 2048 ... 2048 blocks written: OK => tftp ${loadaddr} tispl.bin; link up on port 1, speed 1000, full duplex Using ethernet@8000000port@1 device TFTP from server 192.168.40.166; our IP address is 192.168.40.50 Filename 'tispl.bin'. Load address: 0x82000000 Loading: *### ### ### ##### 0 Bytes
3.2 MiB/s	<>	6.9 MiB/s
done Bytes transferred = 1022155 (f98cb hex) => mmc write ${loadaddr} 0x800 0x1000; MMC write: dev # 0, block # 2048, count 4096 ... 4096 blocks written: OK => tftp ${loadaddr} u-boot.img; link up on port 1, speed 1000, full duplex Using ethernet@8000000port@1 device TFTP from server 192.168.40.166; our IP address is 192.168.40.50	=	done Bytes transferred = 1022155 (f98cb hex) => mmc write ${loadaddr} 0x800 0x1000; MMC write: dev # 0, block # 2048, count 4096 ... 4096 blocks written: OK => tftp ${loadaddr} u-boot.img; link up on port 1, speed 1000, full duplex Using ethernet@8000000port@1 device TFTP from server 192.168.40.166; our IP address is 192.168.40.50

Filename 'u-boot.img'. Load address: 0x82000000 Loading: *### ### ### ### ##### 0 Bytes		Filename 'u-boot.img'. Load address: 0x82000000 Loading: *### ### ### ### ##### 0 Bytes
6.2 MiB/s	<>	6.8 MiB/s
done Bytes transferred = 1353799 (14a847 hex) => mmc write ${loadaddr} 0x1800 0x2000; MMC write: dev # 0, block # 6144, count 8192 ... 8192 blocks written: OK => mmc partconf 0 1 1 1;	=	done Bytes transferred = 1353799 (14a847 hex) => mmc write ${loadaddr} 0x1800 0x2000; MMC write: dev # 0, block # 6144, count 8192 ... 8192 blocks written: OK => mmc partconf 0 1 1 1;
=> mmc bootbus 0 2 0 0; Set to BOOT_BUS_WIDTH = 0x2, RESET = 0x0, BOOT_MODE = 0x0	<>	=> mmc bootbus 0 2 0 1; Set to BOOT_BUS_WIDTH = 0x2, RESET = 0x0, BOOT_MODE = 0x1
=>	=	=>