

[illegible]

Pin configuration diagram for DM3855AAR21F. The diagram shows the pin numbers (1-32) and their corresponding functions. The functions are grouped into several categories:

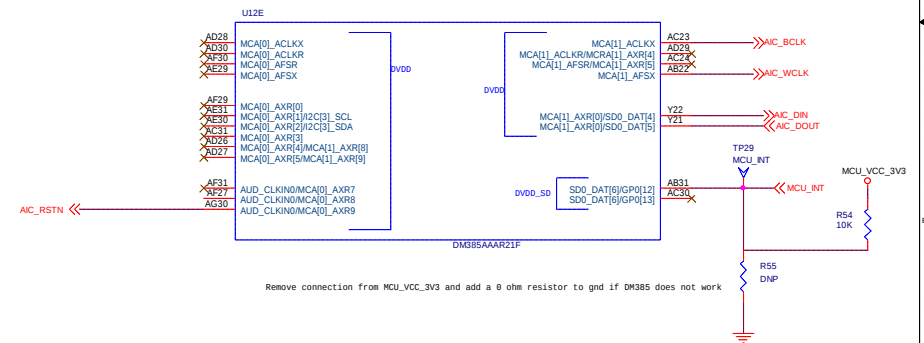
- GPMMC_D0-D8:** GPMMC_D0, GPMMC_D1, GPMMC_D2, GPMMC_D3, GPMMC_D4, GPMMC_D5, GPMMC_D6, GPMMC_D7, GPMMC_D8.
- GPMMC_D9-D15:** GPMMC_D9, GPMMC_D10, GPMMC_D11, GPMMC_D12, GPMMC_D13, GPMMC_D14, GPMMC_D15.
- GPMMC_A16-A23:** GPMMC_A16, GPMMC_A17, GPMMC_A18, GPMMC_A19, GPMMC_A20, GPMMC_A21, GPMMC_A22, GPMMC_A23.
- GPMMC_A24-A27:** GPMMC_A24, GPMMC_A25, GPMMC_A26, GPMMC_A27.
- GPMMC_A28-A31:** GPMMC_A28, GPMMC_A29, GPMMC_A30, GPMMC_A31.

The diagram also shows the connection of pins to various internal blocks, including GPMMC, GPMMC_A16-A23, GPMMC_A24-A27, and GPMMC_A28-A31. The diagram is labeled "DM3855AAR21F" at the bottom.

NAND BUS Opt	GPMC_D12
16-bit Bus Wide	1
8-bit Bus Wide	0

The schematic diagram illustrates the SD card interface. The SD_BOOT pin is connected to a 0.0 ohm resistor (R149) and a 10K pull-up resistor (R52) to PM_VDDA_1V8. The SD_BOOT pin is also connected to the DMP pin of the SD card. The SD card is connected to the GPMC D2, D3, D10, D11, D13, D14, D15, D6, D7, D8, and D9 pins of the GPMC.

	BTMODE4	BTMODE3	BTMODE2	BTMODE1	BTMODE0
NAND Boot	1	0	0	1	1
SD Boot	1	0	1	1	1



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