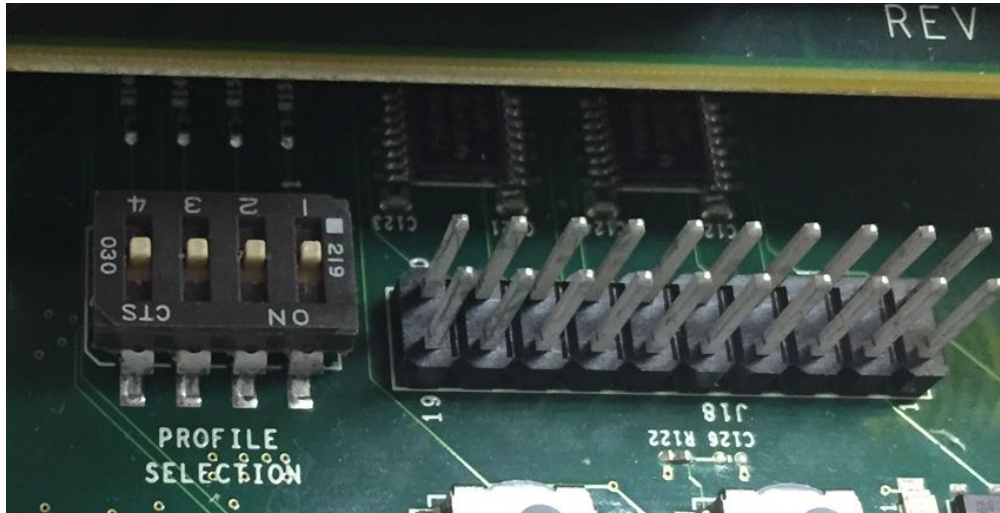
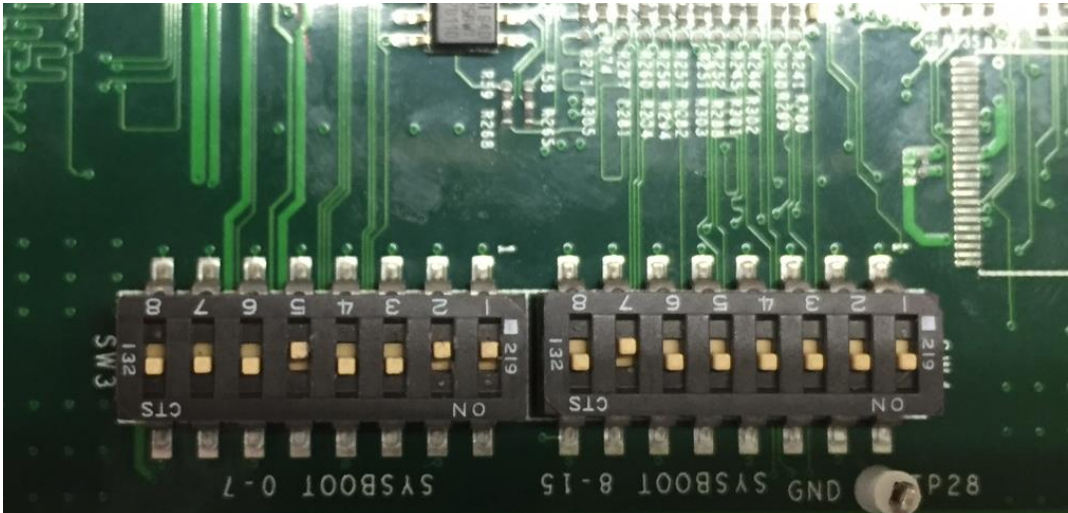




Profile[3:0]=0000



BOOT[4:0]=10011

Table 26-7. SYSBOOT Configuration Pins[4] (continued)

00b = 19.2MHz 01b = 24MHz 10b = 25MHz 11b = 26MHz	00b (all other values reserved)	For NAND boot: must be 00b	0 = ECC done by ROM 1 = ECC handled by NAND	Don't care for ROM code	Don't care for ROM code	0 = CLKOUT1 disabled 1 = CLKOUT1 enabled	10011b	NAND	NANDI2C	MMC0	UART0
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