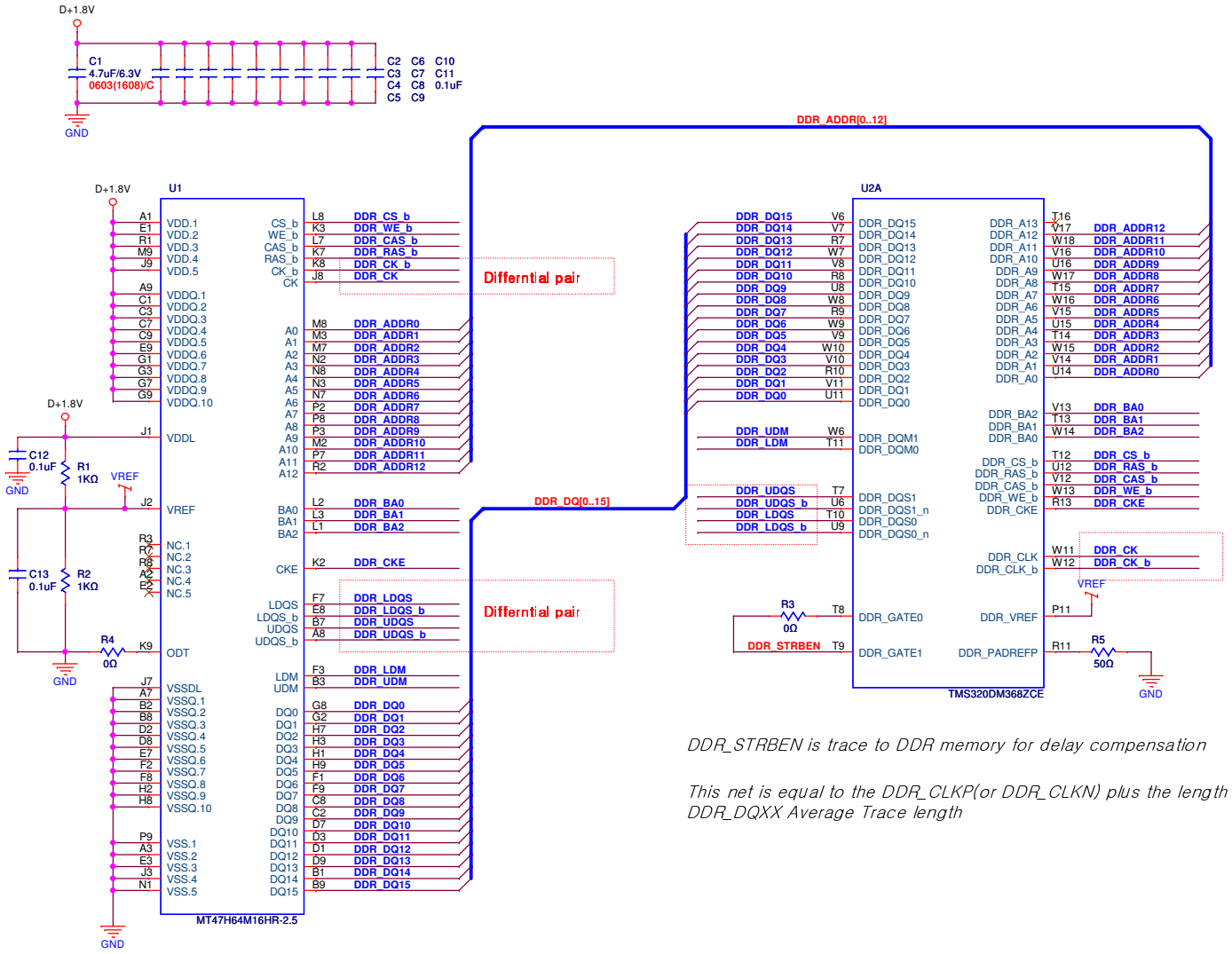
[DDR Memory Interface]



DDR_STRBEN is trace to DDR memory for delay compensation

This net is equal to the DDR_CLKP(or DDR_CLKN) plus the length of DDR_DQXX Average Trace length

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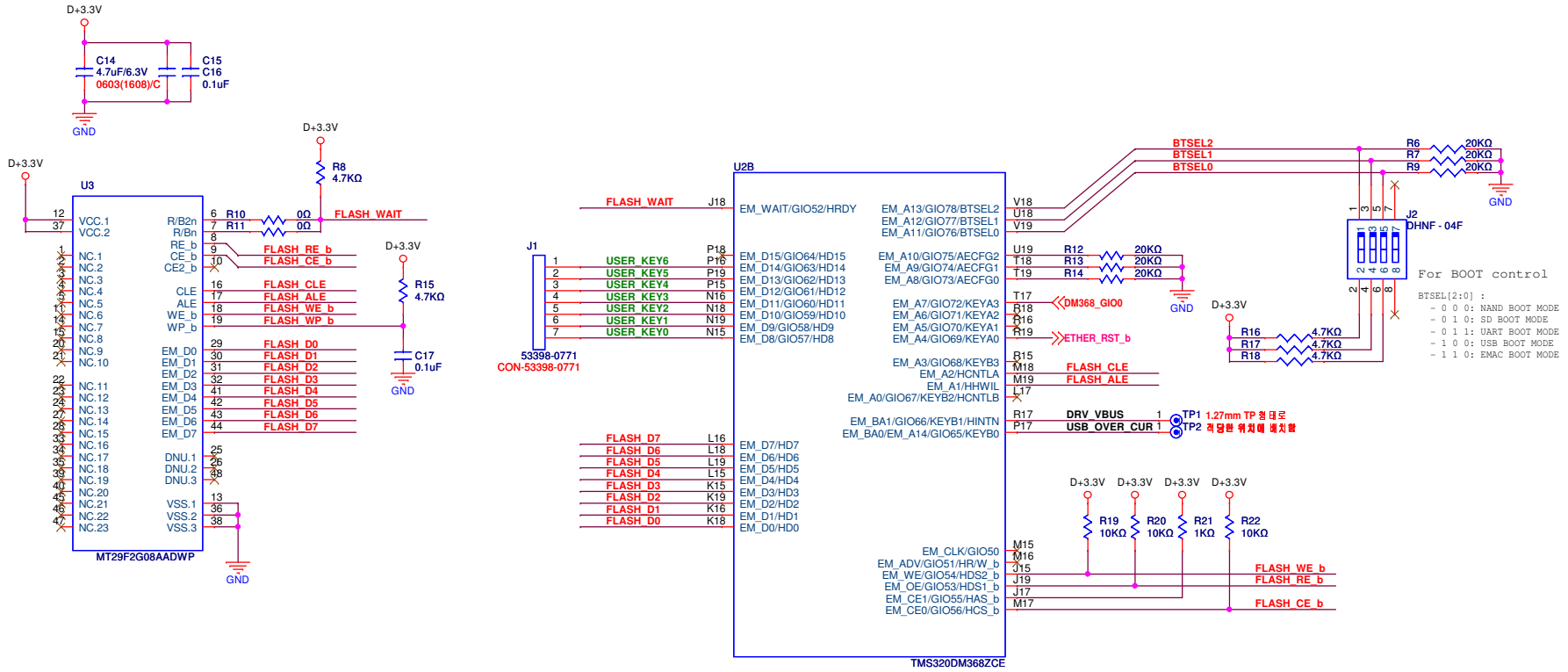
[Flash Memory and Boot Mode Select I/F]



Title		
Size: A3	Document Number: Flash Memory and Boot Mode Select I/F	Rev: 2.0
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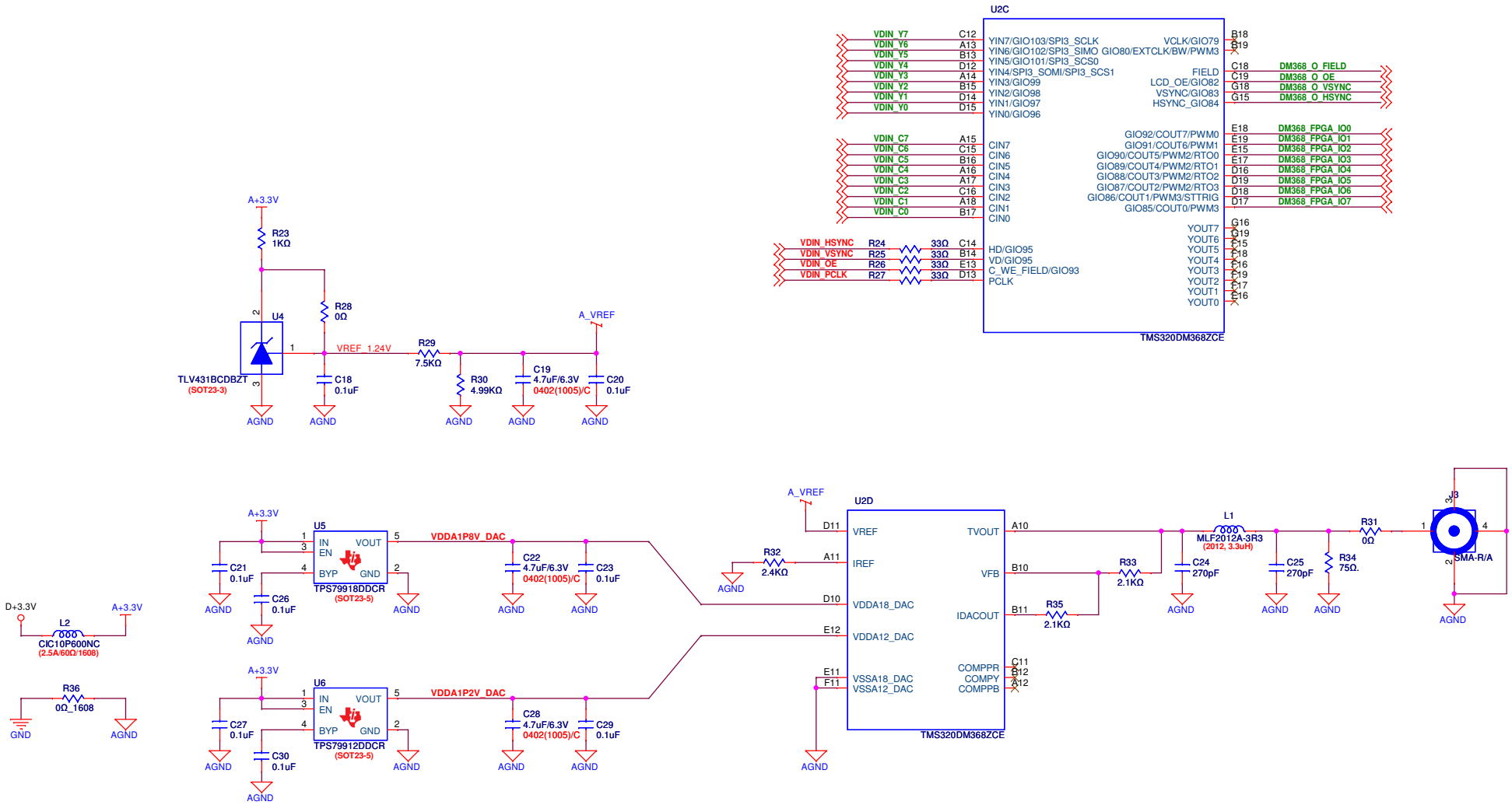
[Camera Interface and Vout Interface]



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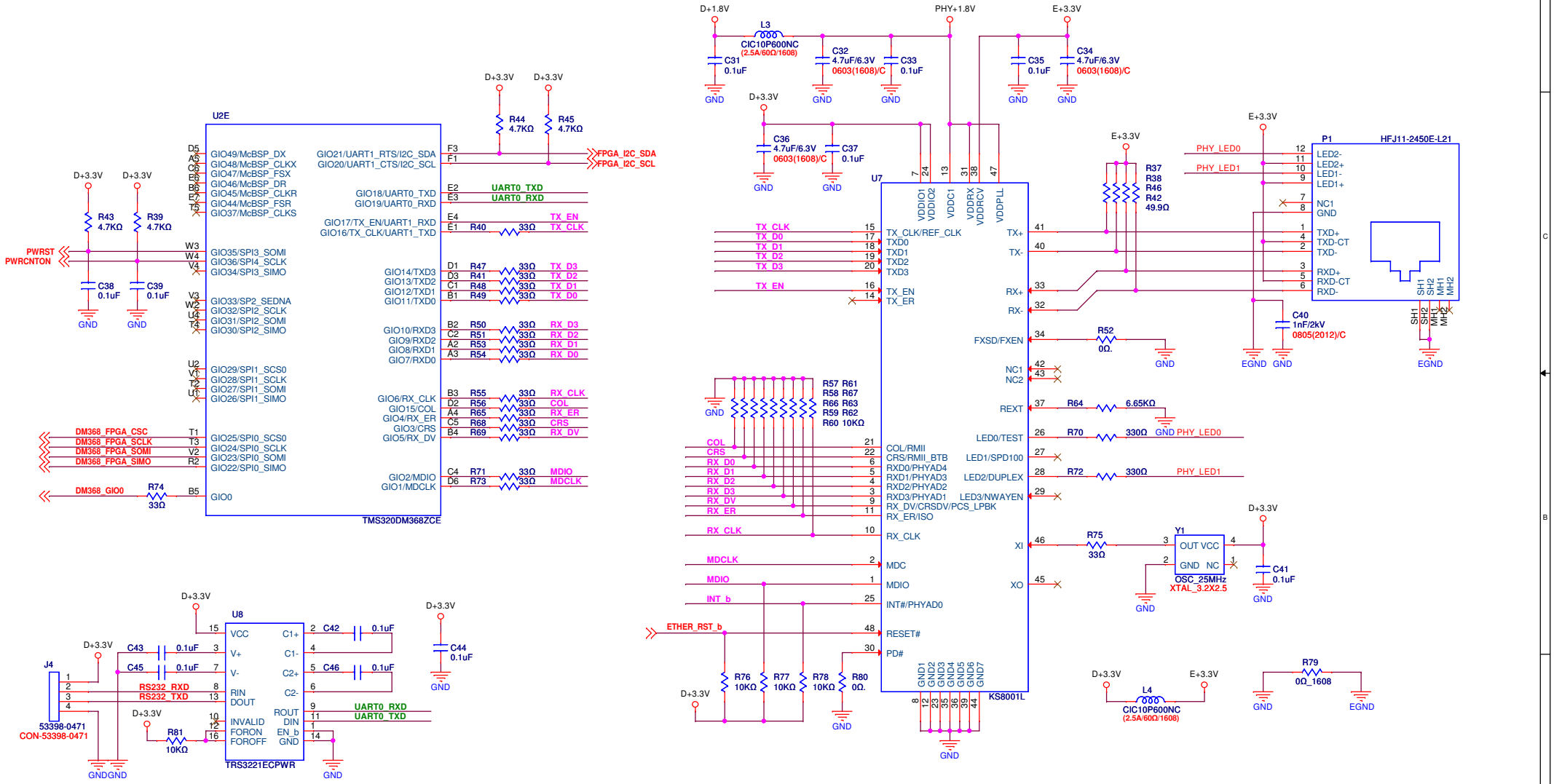
[RS232 and ETHERNET Interface]



Title	..	
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Size A3	Document Number: RS232 and ETHERNET Interface	Rev 2.0
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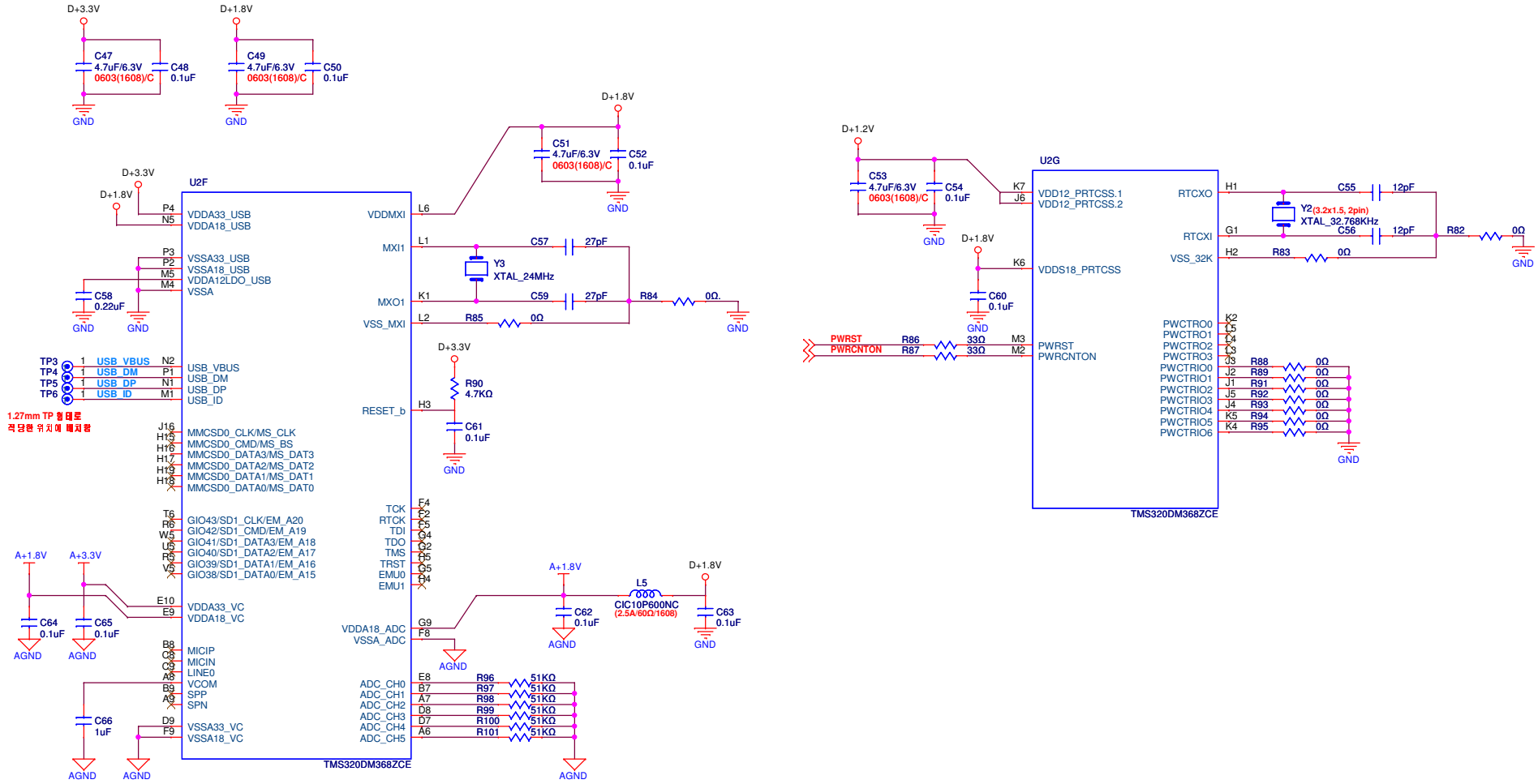
[JTAG and Etc Interface]



Title	..	
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Size A3	Document Number JTAG and Etc Interface	Rev 2.0
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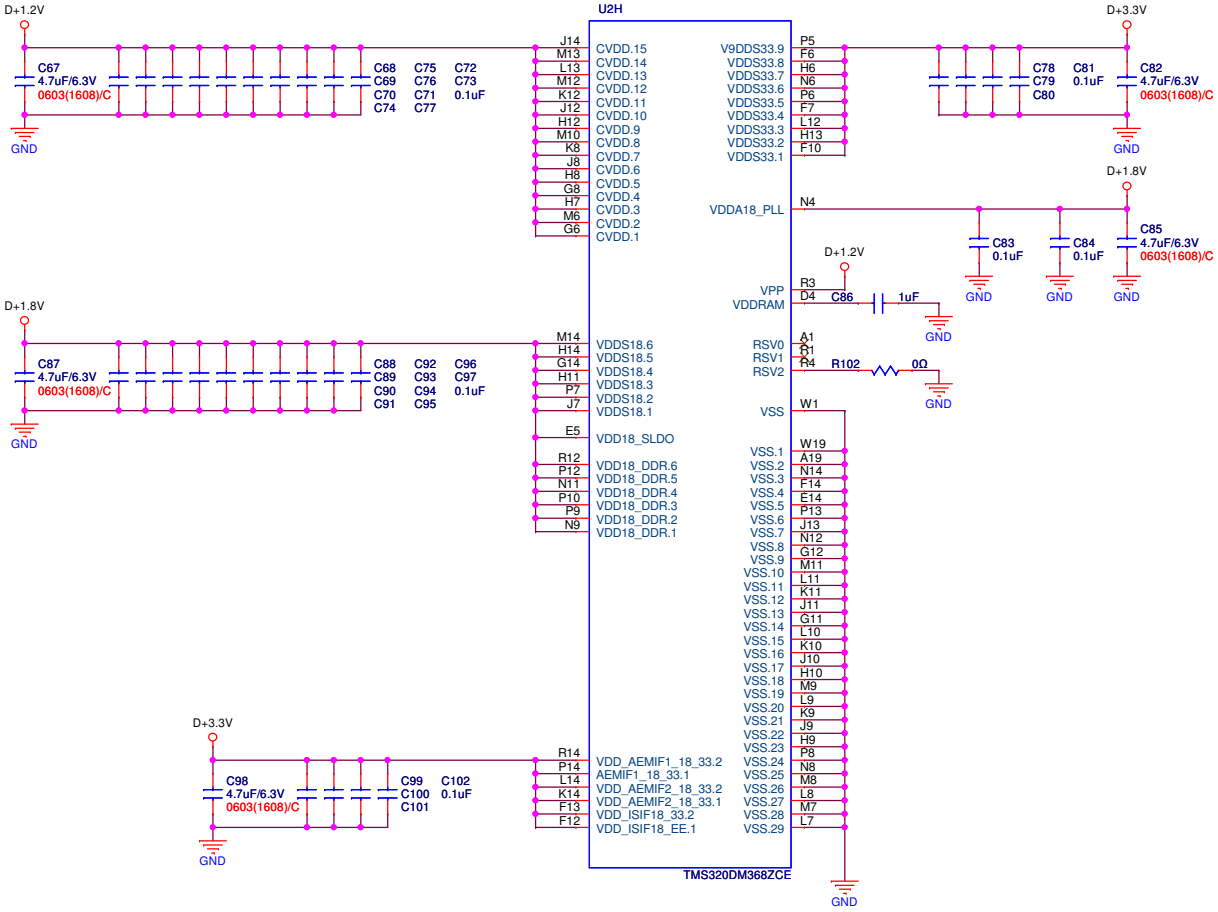
[DM368 Core and I/O Power Interface]



Title	..	
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Size A3	Document Number DM368 Core and I/O Power Interface	Rev 2.0
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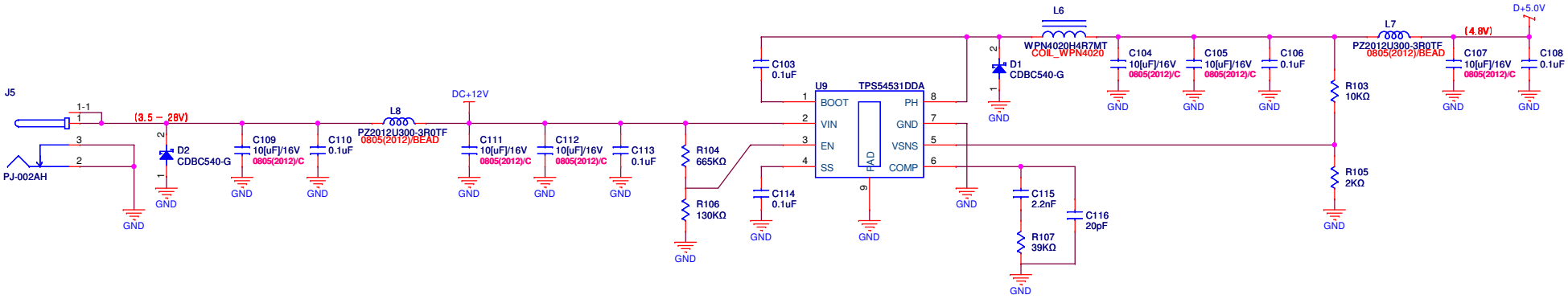
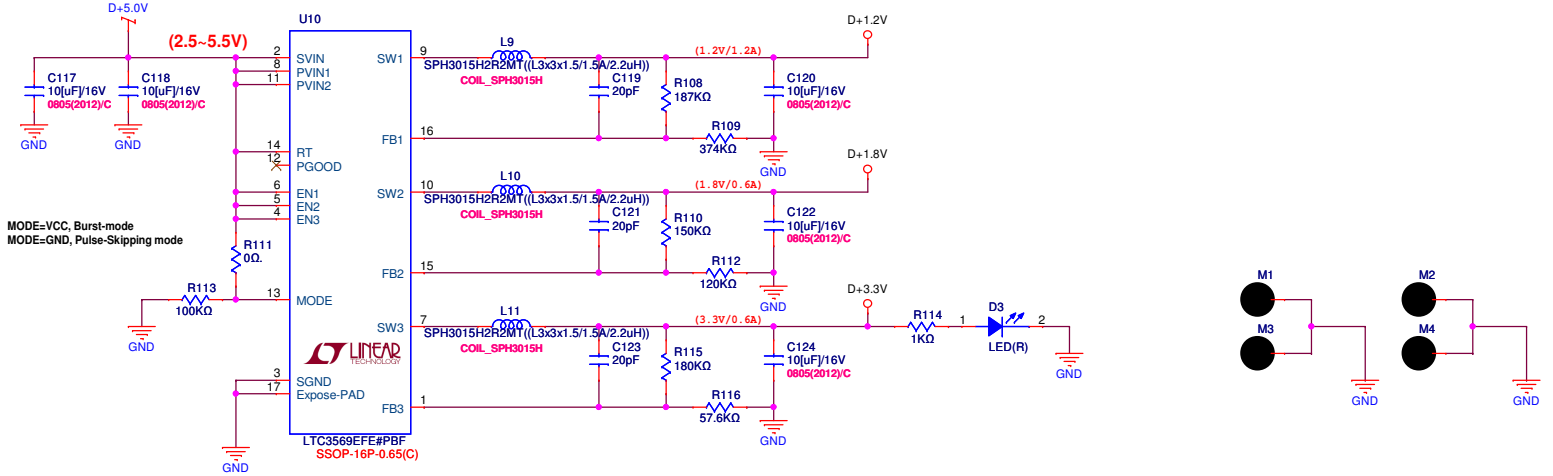
[Power Interface]





Title		
Size A3	Document Number: Power Interface	Rev 2.0
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